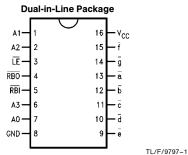
# DM9370 7-Segment Decoder/Driver/Latch with Open-Collector Outputs

## **General Description**

The DM9370 is a 7-segment decoder driver incorporating input latches and output circuits to directly drive incandescent displays. It can also be used to drive common anode LED displays in either a multiplexed mode or directly with the aid of external current limiting resistors.

# **Connection Diagram**



Order Number DM9370N See NS Package Number N16E

Pin Names	Description
A0-A3	Address Inputs
<u>LE</u>	Latch Enable Input (Active LOW)
RBI	Ripple Blanking Input (Active LOW)
RBO	Ripple Blanking as Output (Active LOW)
	as Input (Active LOW)
ā-g	Segment Outputs (Active LOW)

## **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage Input Voltage 5.5V Operating Free Air Temperature Range  $0^{\circ}$ C to  $+70^{\circ}$ C

Commercial

Storage Temperature Range  $-65^{\circ}\text{C to} + 150^{\circ}\text{C}$ 

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The devic should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter		Units		
	raiametei	Min	Nom	Max	V
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	
$V_{IH}$	High Level Input Voltage	2			V
$V_{IL}$	Low Level Input Voltage			0.8	٧
I <sub>OH</sub>	High Level Output Current			-80	μΑ
l <sub>OL</sub>	Low Level Output Current			3.2	mA
$T_A$	Free Air Operating Temperature	0		70	°C
t <sub>s</sub> (H) t <sub>s</sub> (L)	Setup Time HIGH or LOW A <sub>n</sub> to LE			30 20	ns
t <sub>h</sub> (H) t <sub>h</sub> (L)	Hold Time HIGH or LOW A <sub>n</sub> to <del>LE</del>			0 0	ns
t <sub>w</sub> (L)	LE Pulse Width LOW			45	ns

#### **Electrical Characteristics**

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter		Conditions		Min	Typ (Note 1)	Max	Units
$V_{I}$	Input Clamp Voltage		$V_{CC} = Min, I_I = -12 \text{ mA}$	١			-1.5	V
$V_{OH}$	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max$	2.4	3.4		٧		
$V_{OL}$	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$		0.2	0.4	V		
lį	Input Current @ Max Input Voltage		$V_{CC} = Max, V_I = 5.5V$			1	mA	
I <sub>IH</sub>	High Level Input Current		$V_{CC} = Max, V_I = 2.4V$			40	μΑ	
I <sub>IL</sub>	Low Level Input Current		$V_{CC} = Max, V_I = 0.4V$			-1.6	mA	
los	Short Circuit Output Current		V <sub>CC</sub> = Max (Note 2)		-20		-70	mA
V <sub>OH</sub>	Output HIGH Voltage	RBO	$V_{CC} = Min, I_{OH} - 80 \mu A$		2.4			٧
V <sub>OL</sub>	Output LOW Voltage	RBO ā-g	$I_{OL} = 3.2 \text{ mA}$ $I_{OL} = 25 \text{ mA}$	Min			0.4 0.4	V
I <sub>OH</sub>	Output HIGH Current, a-g						250	μΑ
Icc	Power Supply Current		V <sub>CC</sub> = Max				105	μΑ
		$A_1, A_2, A_3, \overline{LE} = GND$ $V_{CC} = Max, Outputs Open$				105	mA	
			$A_0$ , $A_1$ , $A_2$ , $\overline{LE} = GND$ $V_{CC} = Max$ , Outputs Ope			94		

#### **Switching Characteristics**

 $V_{CC} = +5.0V$ ,  $T_A = +25$ °C (See Section 1 for waveforms and load configurations)

Symbol	Parameter	C <sub>L</sub> =	Units	
		Min	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay A <sub>n</sub> to a-g		75 50	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay  LE to a-g		90 70	ns

## **Functional Description**

The '70 has active LOW outputs capable of sinking in excess of 25 mA which allows it to drive a wide variety of 7-segment incandescent displays directly. It may also be used to drive common anode LED displays, multiplexed or directly with the aid of suitable current limiting resistors. This device accepts a 4-bit binary code and produces output drive to the appropriate segments of the 7-segment display. It has a hexadecimal decode format which produces numeric codes "0" through "9" and alpha codes "A" through "F" using upper and lower case fonts.

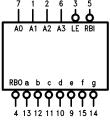
Latches on the four data inputs are controlled by an active LOW latch enable  $\overline{\mathbb{L}}$ . When the  $\overline{\mathbb{L}}$  is LOW, the state of the outputs is determined by the input data. When the  $\overline{\mathbb{L}}$  goes HIGH, the last data present at the inputs is stored in the latches and the outputs remain stable. The  $\overline{\mathbb{L}}$  pulse width necessary to accept and store data is typically 30 ns which allows data to be strobed into the '70 at normal TTL speeds. This feature means that data can be routed directly from high speed counters and frequency dividers into the display without slowing down the system clock or providing intermediate data storage.

The latch/decoder combination is a simple system which drives incandescent displays with multiplexed data inputs from MOS time clocks, DVMs, calculator chips, etc. Data inputs are multiplexed while the displays are in static mode. This lowers component and insertion costs since several circuits—seven diodes per display, strobe drivers, a sepa-

rate display voltage source, and clock failure detect circuits—traditionally found in incandescent multiplexed display systems are eliminated. It also allows low strobing rates to be used without display flicker.

Another '70 feature is the reduced loading on the data inputs when the Latch Enable is HIGH (only 10  $\mu$ A typ). This allows many '70s to be driven from a MOS device in multiplex mode without the need for drivers on the data lines. The '70 also provides automatic blanking of the leading and/or trailing-edge zeroes in a multidigit decimal number, resulting in an easily readable decimal display conforming to normal writing practice. In an 8-digit mixed integer fraction decimal representation, using the automatic blanking capability, 0060.0300 would be displayed as 60.03. Leadingedge zero suppression is obtained by connecting the Ripple Blanking Output (RBO) of a decoder to the Ripple Blanking Input (RBI of the next lower stage device. The most significant decoder stage should have the RBI input grounded; and since suppression of the least significant integer zero in a number is not usually desired, the RBI input of this decoder stage should be left open. A similar procedure for the fractional part of a display will provide automatic suppression of trailing-edge zeroes. The RBO terminal of the decoder can be OR-tied with a modulating signal via an isolating buffer to achieve pulse duration intensity modulation. A suitable signal can be generated for this purpose by forming a variable frequency multivibrator with a cross coupled pair of TTL or DTL gates.

### **Logic Symbol**



 $V_{CC} = Pin 16$ GND = Pin 8 TL/F/9797-2

## **Truth Table**

			INP	UTS				OUTPUTS							
BINARY STATE	ΙE	RBI	<b>A</b> 3	A2	<b>A</b> 1	A0	ā	Б	č	ā	ē	ř	ģ	RBO	DISPLAY
0 0 1	HLLL	* L H X	X L L	X L L	X L L	X L L	<b>+</b> H L H	H L L	— S H L L	TABL H L H	E — H L	H L H	<b>H</b> H	H H H	STABLE BLANK
2 3 4 5	L L L	X X X	L L L	L H H	HLLL	L H L	L H L	L L H	H L L	L H L	L H H	H L L	L L L	н н н	00000
6 7 8 9 10		X X X X	L H H	H L L	H L L	L H L H L	1111	HLLLL		L H L H H	L H L H L	HLLL	LHLLL	H H H H	57898
11 12 13 14 15		X X X X	H H H	L H H H	H L H H	H L H L	H L H L L	H L H H	L H L H	L L L H	L L L L	L H L L	L H L L	H H H H	meno, mer
Х	Χ	Χ	Χ	Χ	Χ	Х	Н	Н	Н	Н	Н	Н	Н	L**	BLANK



TL/F/9797-4

TL/F/9797-6

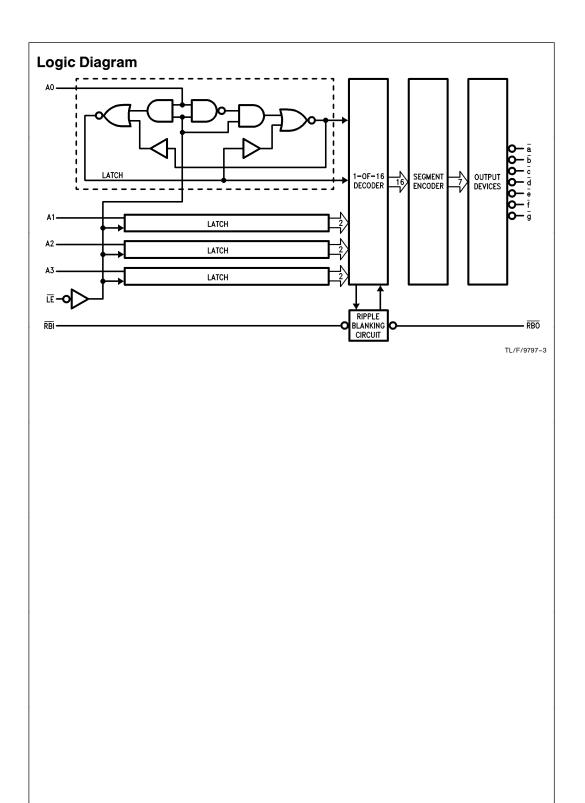
\*The  $\overline{\mbox{RBI}}$  will blank the display only if binary zero is stored in the latches.

 $\ensuremath{^{**}\overline{\text{RBO}}}$  used as an input overdrives all other input conditions.

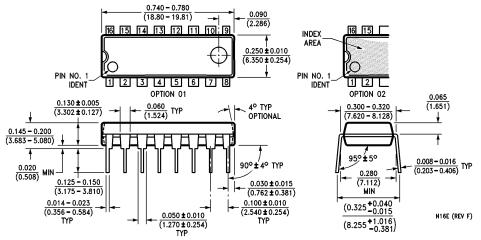
H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial

#### **Numerical Designation**





## Physical Dimensions inches (millimeters)



16-Lead Molded Dual-In-Line Package (N) Order Number DM9370N NS Package Number N16E

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