TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

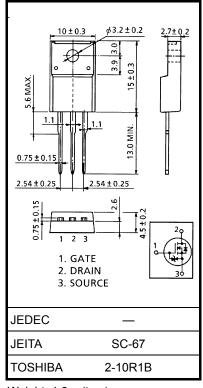
2SK3126

Switching Regulator Applications

- Low drain-source ON resistance $: RDS (ON) = 0.48 \Omega (typ.)$
- High forward transfer admittance $|Y_{fs}| = 7.5 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 450 \ V)$
- Enhancement mode $: V_{th} = 2.4 \sim 3.4 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	450	V
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	450	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	Ι _D	10	А
	Pulse (Note 1)	I _{DP}	40	А
Drain power dissipation	n (Tc = 25°C)	PD	40	W
Single pulse avalanche	e energy (Note 2)	E _{AS}	222	mJ
Avalanche current		I _{AR}	10	А
Repetitive avalanche e	energy (Note 3)	E _{AR}	4	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature ra	ange	T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal reverse, channel to case	R _{th (ch−c)}	3.125	°C / W
Thermal reverse, channel to ambient	R _{th (ch−a)}	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 3.7 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution. Unit: mm

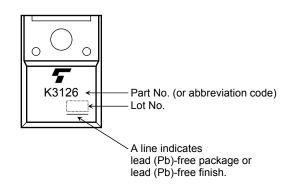
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V		_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 450 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	450	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.4	_	3.4	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 5 A	_	0.48	0.65	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3.5	7.5	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	1400	—	pF
Reverse transfer capacitance		C _{rss}		_	240	—	
Output capacitance		Coss		_	590	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \stackrel{I_{D}=5A}{}_{0V} \stackrel{OUT}{}_{VOUT}$ $V_{GS} \stackrel{0V}{}_{0V} \stackrel{I_{D}=5A}{}_{0V} \stackrel{OUT}{}_{VOUT}$ $V_{DD}= = 200V$ $Duty \le 1\%, t_{w} = 10\mu s$	_	35	_	- ns
	Turn-on time	t _{on}		_	50	_	
	Fall time	t _f		_	80	_	
	Turn-off time	t _{off}		_	260	—	
Total gate charge (Gate-source plus gate-drain)		Qg		_	35	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 10 A		19	_	nC
Gate-drain ("miller") charge		Q _{gd}			16	_	

Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	10	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	40	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V	_	1400	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 100 A / µs	_	14	_	μC

Marking



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20070701-EN

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