

# 2SJ528(L), 2SJ528(S)

Silicon P Channel MOS FET

REJ03G0878-0300 (Previous: ADE-208-641A) Rev.3.00 Sep 07, 2005

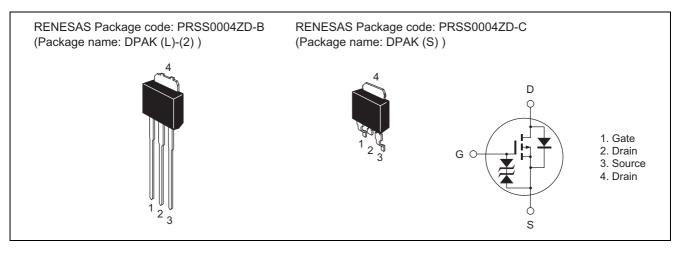
### Description

High speed power switching

### Features

- Low on-resistance
- $R_{DS (on)} = 0.17 \ \Omega \text{ typ.}$
- 4 V gate drive devices
- High speed switching

# Outline





# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	-7	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-28	A
Body to drain diode reverse drain current	I <sub>DR</sub>	-7	A
Avalanche current	I <sub>AP</sub> Note 3	-7	A
Avalanche energy	E <sub>AR</sub> Note 3	4.2	mJ
Channel dissipation	Pch Note 2	20	W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	٦°

Notes: 1. PW  $\leq$  10 µs, duty cycle  $\leq$  1%

2. Value at  $Tc = 25^{\circ}C$ 

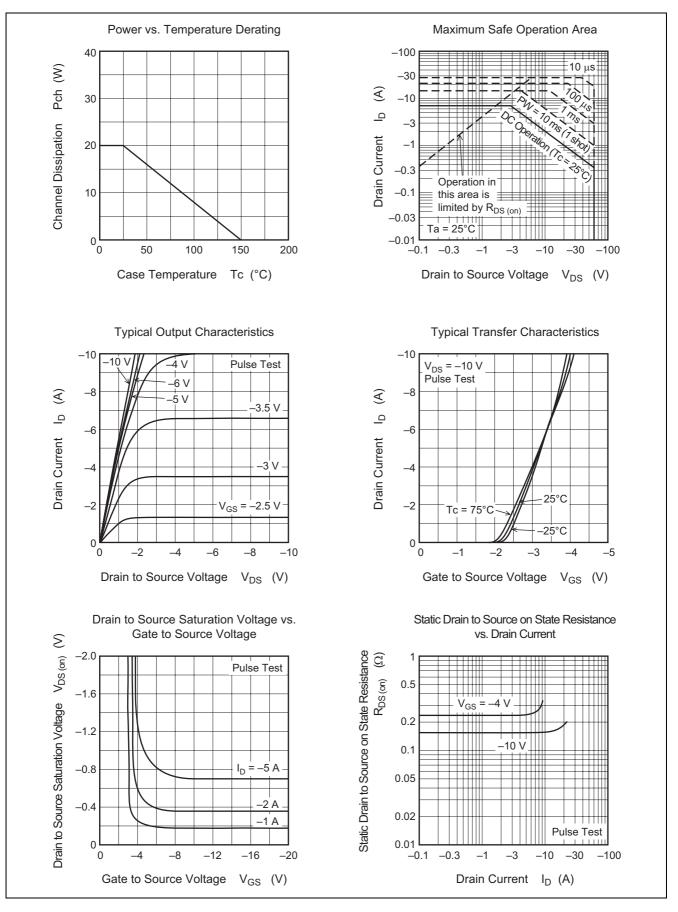
3. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

### **Electrical Characteristics**

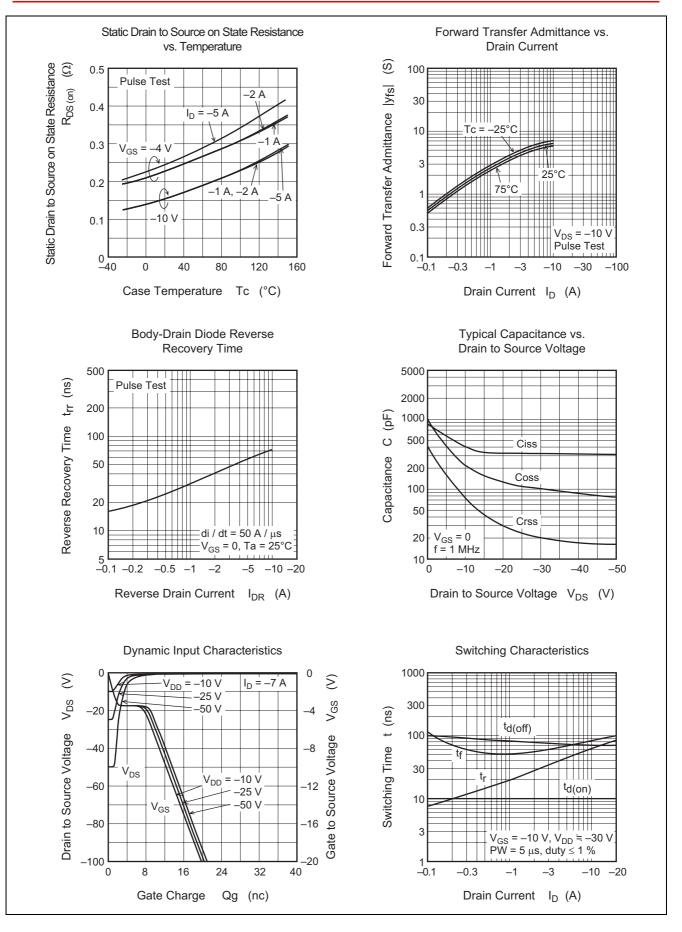
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-60			V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20		—	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	_	±10	μΑ	$V_{GS} = \pm 16 V, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0	_	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	0.17	0.22	Ω	$I_D = -4 \text{ A}, V_{GS} = -10 \text{ V}^{Note 4}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	0.24	0.37	Ω	$I_D = -4 A, V_{GS} = -4 V^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	3.0	5.0	—	S	$I_D = -4 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	—	400	—	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	—	220	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	75	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	10	—	ns	$V_{GS} = -10 \text{ V}$
Rise time	tr	_	40	—	ns	$I_D = -4 A$
Turn-off delay time	t <sub>d (off)</sub>	_	75	—	ns	$R_L = 7.5 \Omega$
Fall time	t <sub>f</sub>	_	65	_	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	-1.1	—	V	$I_F = -7 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>		65	—	ns	$I_F = -7 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/µs

Note: 4. Pulse test

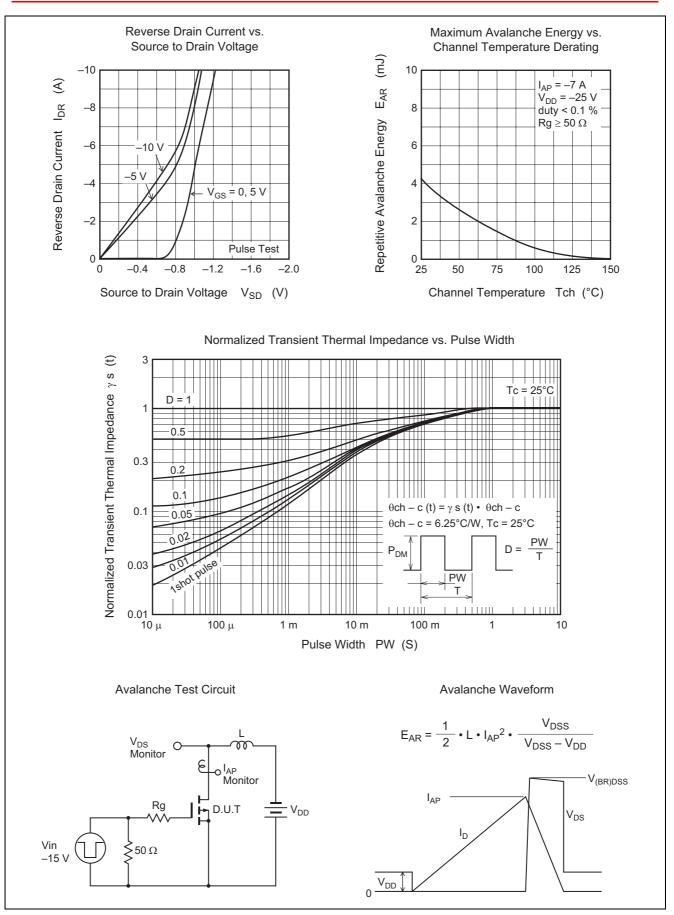
### **Main Characteristics**



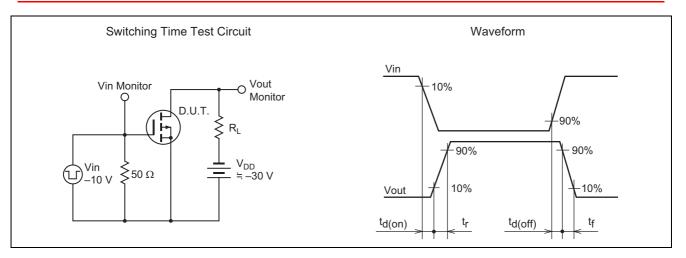






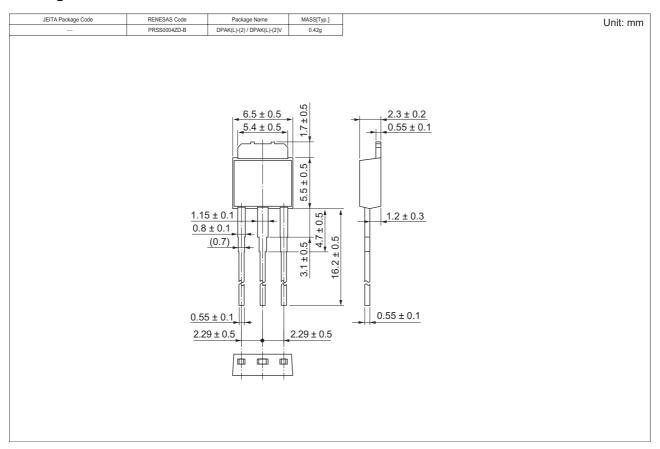


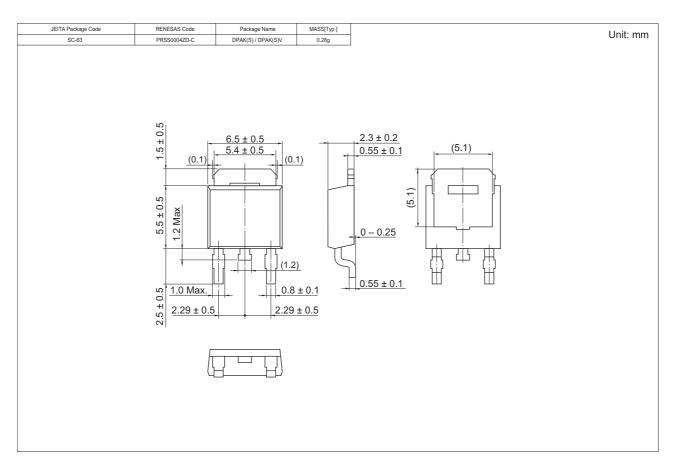






### **Package Dimensions**







## **Ordering Information**

Part Name	Quantity	Shipping Container
2SJ528L-E	3200 pcs	Box (Sack)
2SJ528STL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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