

# BD 695 A

## BD 697 • BD 697 A

## BD 699 • BD 699 A

## BD 701

### PLASTIC MEDIUM-POWER NPN TRANSISTORS

... for use as output devices in complementary general-purpose amplifier applications.

- High DC Current Gain –  
 $h_{FE} = 750$  (Min) @  $I_C = 3.0$  and  $4.0$  Adc
- Monolithic Construction
- BD 695A, 697, 697A, 699, 699A, 701 are complementary with BD 696A, 698, 698A, 700, 700A, 702

### 8.0 AMPERE DARLINGTON POWER TRANSISTORS NPN SILICON

45, 60, 80, 100 VOLTS  
70 WATTS

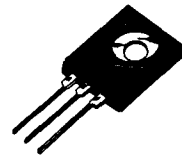
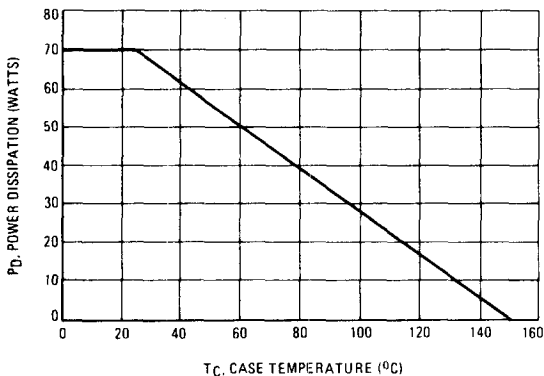
#### MAXIMUM RATINGS

| Rating   | Symbol         | BD 695A                   | BD 697<br>BD 697A | BD 699<br>BD 699A | BD 701 | Unit             |
|--|----------------|---------------------------|-------------------|-------------------|--------|------------------|
|  |                | Collector-Emitter Voltage | $V_{CEO}$         | 45                | 60     |                  |
| Collector-Base Voltage   | $V_{CB}$       | 45                        | 60                | 80                | 100    | Vdc              |
| Emitter-Base Voltage   | $V_{EB}$       | 5.0                       |                   |                   |        | Vdc              |
| Collector Current  | $I_C$          | 8.0                       |                   |                   |        | Adc              |
| Base Current   | $I_B$          | 0.1                       |                   |                   |        | Adc              |
| Total Device Dissipation @ $T_C = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$          | 70                        |                   |                   |        | Watts            |
| Operating and Storage Junction<br>Temperating Range                                    | $T_J, T_{stg}$ | -55 to +150               |                   |                   |        | $^\circ\text{C}$ |

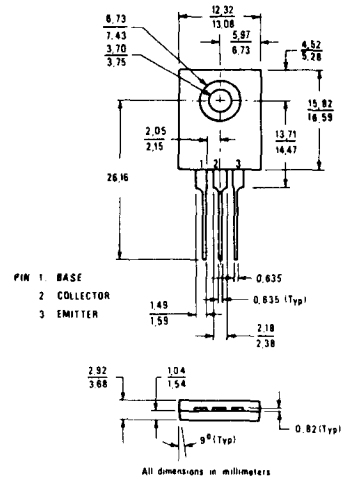
#### THERMAL CHARACTERISTICS

| Characteristic                       | Symbol        | Max  | Unit                      |
|--------------------------------------|---------------|------|---------------------------|
| Thermal Resistance, Junction to Case | $\theta_{JC}$ | 1.79 | $^\circ\text{C}/\text{W}$ |

FIGURE 1 – POWER TEMPERATURE DERATING CURVE



HARDWARE AVAILABLE:  
1. MICA WASHER – 14B 52600 FO13  
2. NYLON ISOLATION WASHER



If lead bending is required use suitable clamps or other supports between transistor case and point of bend

Case 199\_04

**BD 695 A**  
**BD 697 · BD 697 A**  
**BD 699 · BD 699 A**  
**BD 701**

**ELECTRICAL CHARACTERISTICS** ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

| Characteristic   | Symbol  | Min        | Max                   | Unit             |                 |
|--|---|------------|-----------------------|------------------|-----------------|
| <b>OFF CHARACTERISTICS</b>   |   |            |                       |                  |                 |
| Collector-Emitter Breakdown Voltage <sup>(1)</sup><br>( $I_C = 100 \text{ mAdc}$ , $I_B = 0$ )   | BD 695A<br>BD 697, 697A<br>BD 699, 699A<br>BD 701 | $BV_{CEO}$ | 45<br>60<br>80<br>100 | —<br>—<br>—<br>— | Vdc             |
| Collector Cutoff Current<br>( $V_{CE} = \text{Half Rated } BV_{CEO}$ , $I_B = 0$ )   |   | $I_{CEO}$  | —                     | 500              | $\mu\text{Adc}$ |
| Collector Cutoff Current<br>( $V_{CB} = \text{Rated } BV_{CEO}$ , $I_E = 0$ )<br>( $V_{CB} = \text{Rated } BV_{CEO}$ , $I_E = 0$ , $T_C = 100^\circ\text{C}$ ) |   | $I_{CBO}$  | —<br>—                | 0.2<br>2.0       | mAdc            |
| Emitter Cutoff Current<br>( $V_{BE} = 5.0 \text{ Vdc}$ , $I_C = 0$ )   |   | $I_{EBO}$  | —                     | 2.0              | mAdc            |

**ON CHARACTERISTICS**

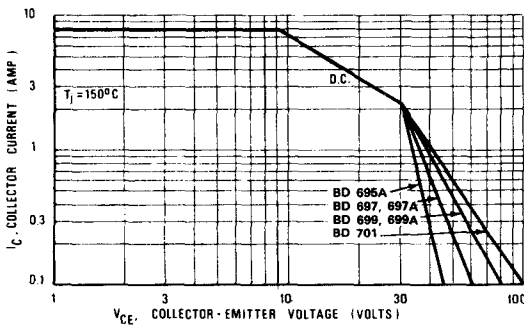
|  |   |               |            |            |     |
|--|---|---------------|------------|------------|-----|
| DC Current Gain <sup>(1)</sup><br>( $I_C = 3.0 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ )<br>( $I_C = 4.0 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ )         | BD 697, 699, 701<br>BD 695A, 697A, 699A | $h_{FE}$      | 750<br>750 | —<br>—     | —   |
| Collector-Emitter Saturation Voltage<br>( $I_C = 3.0 \text{ Adc}$ , $I_B = 12 \text{ mAdc}$ )<br>( $I_C = 4.0 \text{ Adc}$ , $I_B = 16 \text{ mAdc}$ )         | BD 697, 699, 701<br>BD 695A, 697A, 699A | $V_{CE(sat)}$ | —<br>—     | 2.5<br>2.8 | Vdc |
| Base-Emitter On Voltage <sup>(1)</sup><br>( $I_C = 3.0 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ )<br>( $I_C = 4.0 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ ) | BD 697, 699, 701<br>BD 695A, 697A, 699A | $V_{BE(on)}$  | —<br>—     | 2.5<br>2.5 | Vdc |

**DYNAMIC CHARACTERISTICS**

|   |  |          |     |   |   |
|---|--|----------|-----|---|---|
| Small-Signal Current Gain<br>( $I_C = 3.0 \text{ Adc}$ , $V_{CE} = 3.0 \text{ Vdc}$ , $f = 1.0 \text{ MHz}$ ) |  | $h_{fe}$ | 1.0 | — | — |
|---|--|----------|-----|---|---|

(1) Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

**FIGURE 2 – DC SAFE OPERATING AREA**



There are two limitations on the power handling ability of a transistor: junction temperature and secondary breakdown. Safe operating area curves indicate  $I_C$ - $V_{CE}$  limits of the transistor that must be observed for reliable operation; e.g., the transistor must not be subjected to greater dissipation than the curves indicate.

At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by secondary breakdown. (See AN-415)

**FIGURE 3 – DARLINGTON CIRCUIT SCHEMATIC**

