

#### **GENERAL DESCRIPTION**

The CM3718 is a low-noise, pulse-width-modulated (PWM), DC-DC step-down converter. It powers logic and transmitters in small wireless systems such as cellular phones, communicating PDAs, and handy-terminals. The device features an internal synchronous rectifier for high efficiency; it requires no external Schottky diode. Excellent noise characteristics and fixed-frequency operation provide easy post-filtering. The CM3718 is ideally suited for Li-lon battery applications. It is also useful for +3V or +5V fixed input applications.

The device operates in one of four modes. Forced PWM mode operates at a fixed frequency regardless of the load. Shutdown mode places the device in standby, reducing quiescent supply current to under 0.1µA.

The CM3718 can deliver over 1.5A. The output voltage can be adjusted from VREF to VIN. The input range is from 2.0V to 5.0V. Other features of the CM3718 include high efficiency, low dropout voltage. It is available in a space-saving 8-pin SOP package.

#### **FEATURES**

- ◆ Patent Number #6,452,366
- ◆ 1.2MHz switching and synchronization
- ◆ Dynamic output-voltage adjustment from VREF to VIN
- ♦ 1.5A Guaranteed Output Current
- ♦ 95% Efficiency
- No Schottky Diode Required
- External Soft Start
- ♦ 8-pin PSOP power packages

#### **APPLICATIONS**

- Cellular Phone
- ◆ Cordless Phone
- ♦ PDAs and Handy-Terminals
- ◆ CPU I/O Supplies
- Notebook Chipset Supplies
- Battery Operated Devices

#### PIN CONFIGURATION

PSOP-8 (PT-08) Top View

1	VIN	PVIN	8
2	GND	LX	7
3	SD	PGND	6
4	VREF	VFB	5

# **PIN DESCRIPTION**

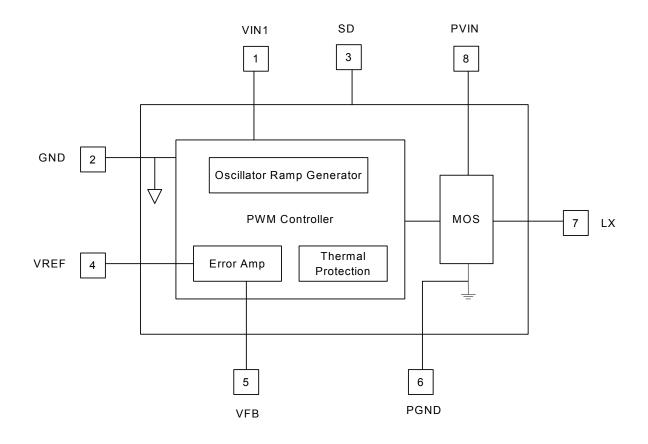
Pin No.	Symbol	Description	1	Operating Rat		Rating	ng
CM3718				Min.	Тур.	Max.	Uni t
1	VIN	Voltage supply for internal circuits		2	2.5	5.5	V
2	GND	Ground for internal reference voltage d					
3	SD		tdown level	0.75 x VIN		VIN + 0.3	٧
3 SI		CMOS input level Enal	ble level	0		2.0	
4	VREF	V <sub>OUT</sub> Set Voltage		1.1		VIN	V
5	VFB	Feedback node for the V <sub>OUT</sub>			VREF		٧
6	PGND	Ground for output power transistors					
7	LX	Inductor connection to the Drains of the	e internal power MOSFETs			5.5	V
8	PVIN	Voltage supply for output power transis	stors	2	2.5	5.5	V

# **ORDERING INFORMATION**

Part Number	Temperature Range	Package		
CM3718IS	-40°C to 85°C	8-Pin PSOP (PS08)		
CM3718GIS*	-40°C to 85°C	8-Pin PSOP (PS08)		

\*Note: G : Suffix for Pb Free Product

# **BLOCK DIAGRAM**





# **ABSOLUTE MAXIMUM RATINGS**

Absolute maximum ratings are those values beyond which the	Junction Temperature150°C
device could be permanently damaged.	Storage Temperature65°C to 125°C
PVIN/VIN0.3V to 6.0V	Lead Temperature (Soldering, 5 sec) 260°C
Voltage on Any Other Pin GND – 0.3V to VIN + 0.3V	Thermal Dissipation( $\theta$ JC )
Output Current, Source or Sink1.5A	

# **OPERATING CONDITIONS**

Temperature Range .....-40°C to 85°C PVIN Operating Range ......2.0V to 4.0V

**ELECTRICAL CHARACTERISTICS** (Unless otherwise stated, these specifications apply T<sub>A</sub>=25°C; VIN=+3.3V and PVIN=+3.3V) maximum ratings are stress ratings only and functional device operation is not implied. (Note 1)

	<b>D</b>	<b>-</b> . 0	CM3718			
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
SWITCHING	REGULATOR					
$V_{REF}$	Adjustable Output Voltage		VREF		VIN	V
fsw	Switching Frequency	CM3718		1.2		MHz
I <sub>OUT(RMS)</sub>	Maximum Output RMS Current	CM3718			1.5	Α
I <sub>OUT(PEAK)</sub>	Maximum Output Peak Current	CM3718			3	Α
MOSFETs						
RDS <sub>(ON)</sub>	Drain to Source on-State Resistance	PVIN=5V		250		$m\Omega$
SUPPLY						
	Quiescent Current	VFB = 1.4V		220		μΑ
$I_{VIN}$		LC unconnected				
		VFB = 1.4V		500		μΑ
I <sub>PVIN</sub>		LC unconnected		500		



#### FUNCTIONAL DESCRIPTION

The CM3718 step-down, pulse-width-modulated (PWM), DC-DC converter has an adjustable output range from VREF to the input voltage (VIN). An internal synchronous rectifier improves efficiency and eliminates an external Schottky diode. Fixed-frequency operation enables easy post-filtering, thereby providing excellent noise characteristics. As a result, the CM3718 is an ideal choice for many small wireless systems.

#### **VREF**

The reference voltage could be ranged from 1.1V to VIN.

#### **OUPUTS**

The output voltage pins (LX) are tied to the RF power amp, via an external inductor. Output voltage is determined by the VREF inputs.

#### **INPUTS**

The input voltage reference pin, VREF determine the output voltages (LX). If a specific voltage is forced at the VREF pin, the output voltage follows the voltage at the VREF pin.

CM3718

#### **OTHER SUPPLY VOLTAGES**

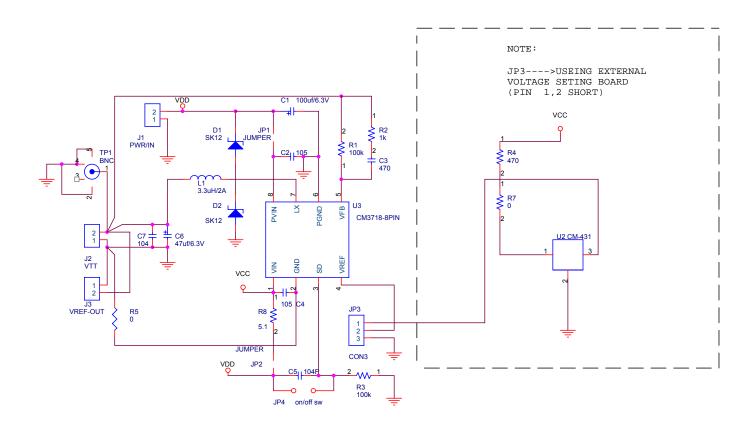
Several inputs are provided for the supply voltages: PVIN and VIN.

The PVIN provide the power supply to the power MOSFETs. VIN provides the voltage supply to the logic section and internal error amplifiers.

#### **FEEDBACK**

The VFB pin is an input that can be used for closed loop compensation. This input is derived from the voltage output. AGND pin is a contact node of internal resistor divider for remote sense.

#### APPLICATION CIRCUIT

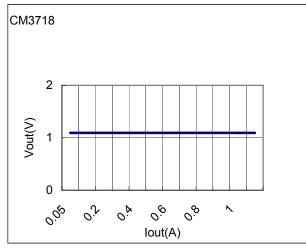




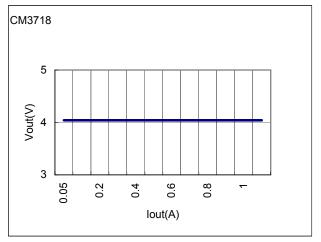
# 1.5A Low-Noise PWM Step-Down Regulator

#### LOAD REGULATION

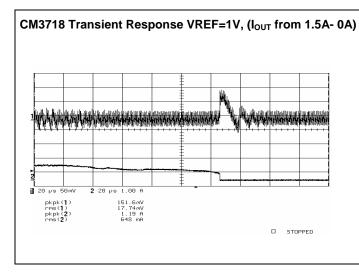
Vin=5V, VREF input=1.09V

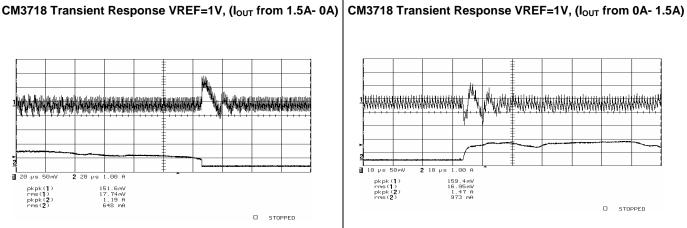


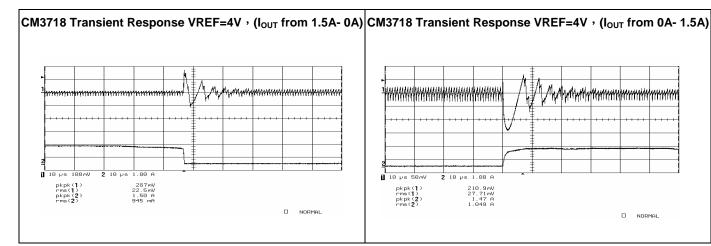
Vin=5V, VREF input=4.04V

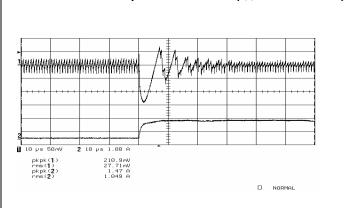


#### LOAD TRANSIENT RESPONSE

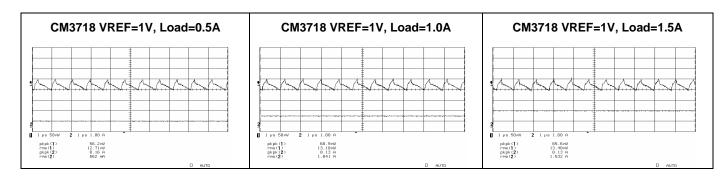


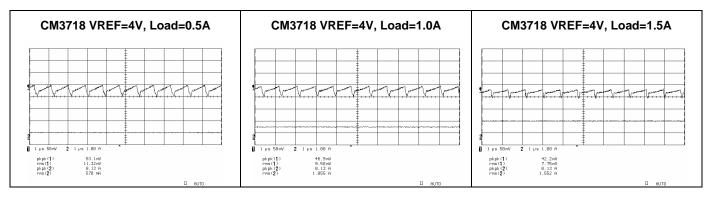




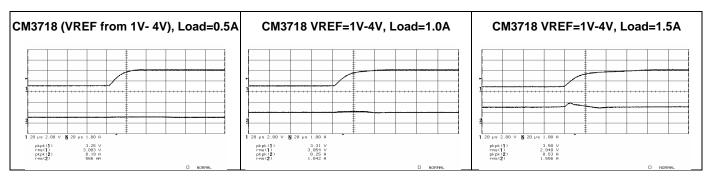


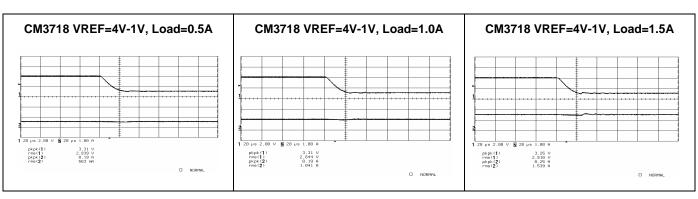
#### **VOUT OUTOUT RIPPLE AND NOISE**





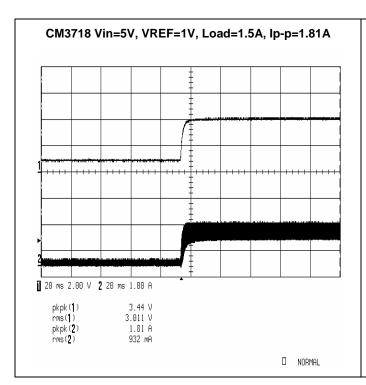
#### **VOLTAGE SETTING STEP RESPONSE**

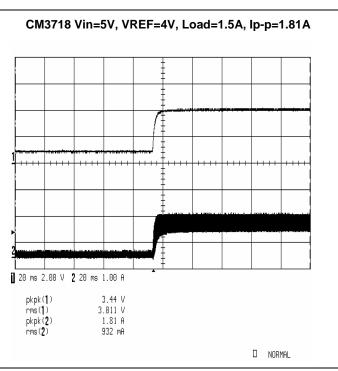






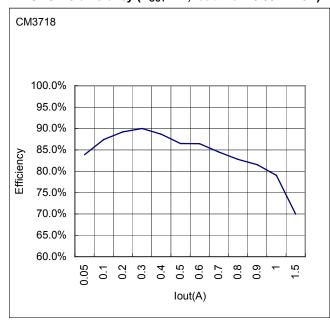
#### **IN-RUSH CURRENT**



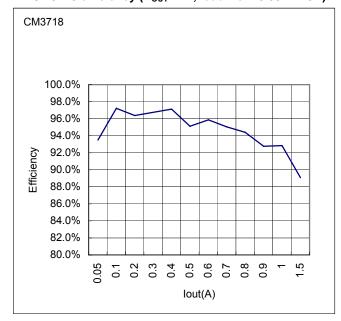


#### **EFFICIENCY**

#### CM3718 efficiency (V<sub>OUT</sub>=1V, load from 0.05A~1.5A)



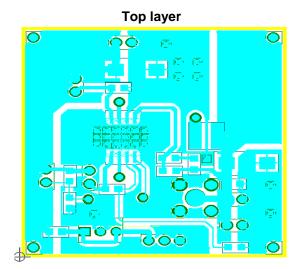
#### CM3718 efficiency (V<sub>OUT</sub> =4V, load from 0.05A~1.5A)



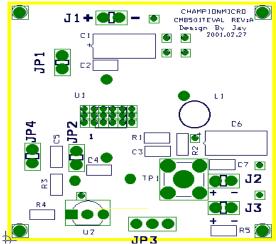


# 1.5A Low-Noise PWM Step-Down Regulator

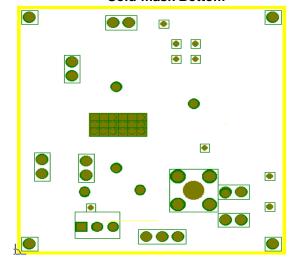
# **PCB LAYOUT**



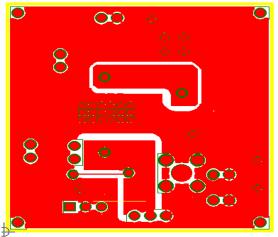




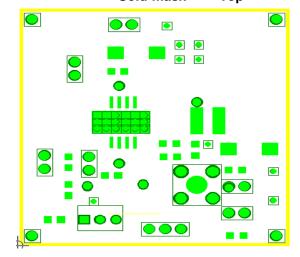
**Sold-mask Bottom** 



# **Bottom Layer**

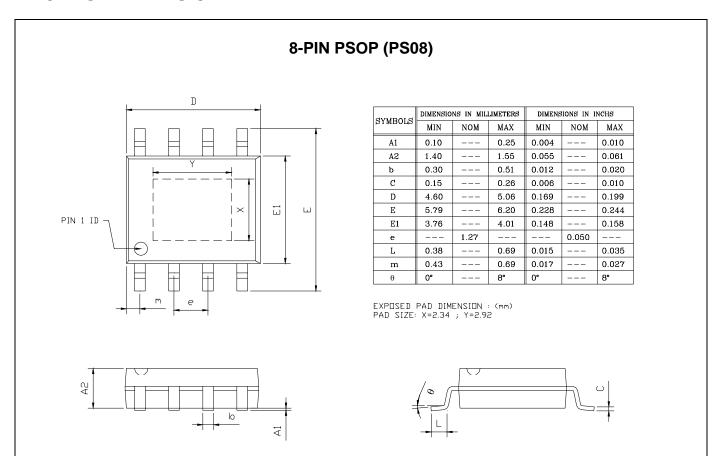


Sold-mask Top





# **PACKAGE DIMENSION**



**CM3718** 



#### IMPORTANT NOTICE

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