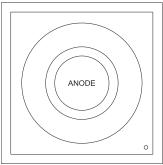


CPD66X-CLL459A

Low Leakage Diode Die 0.2 Amp, 200 Volt

The CPD66X-CLL459A is a silicon 0.2 Amp, 200 Volt diode designed for low leakage applications.



MECHANICAL SPECIFICATIONS:

Die Size	17.5 x 17.5 MILS
Die Thickness	5.9 MILS
Anode Bonding Pad Size	7.0 MILS DIAMETER
Top Side Metalization	AI – 30,000Å
Back Side Metalization	Au-As – 9,000Å
Scribe Alley Width	2.0 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	54,848

CKSIDE	CATHODE	R

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	200	V
Peak Working Reverse Voltage	V_{RWM}	175	V
Average Forward Current	IO	200	mA
Continuous Forward Current	I _F	500	mA
Peak Forward Surge Current, tp=1.0µs	I _{FSM}	4.0	Α
Operating and Storage Junction Temperature	T.I. T _{sta}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise)

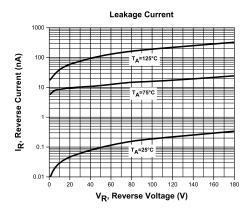
SYMBOL I _R	TEST CONDITIONS V _R =175V	MIN	MAX 25	UNITS nA
BV_R	I _R =100μA	200		V
VF	I _F =100mA		1.0	V
$C_{.1}$	V _R =0, f=1.0MHz		6.0	pF

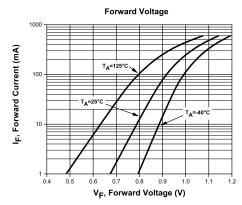
CPD66X-CLL459A

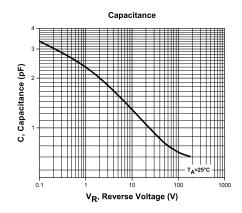
Typical Electrical Characteristics

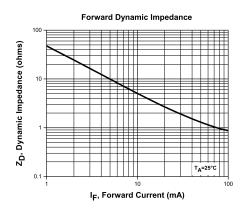


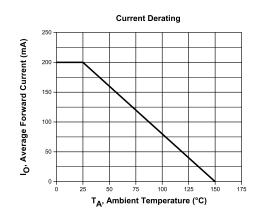
www.centralsemi.com

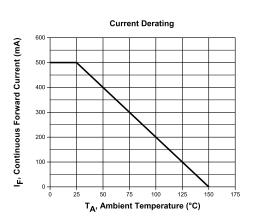








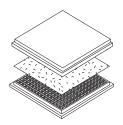




R0 (13-September 2016)

BARE DIE PACKING OPTIONS

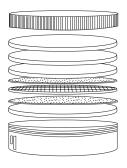




BARE DIE IN TRAY (WAFFLE) PACK

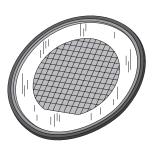
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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