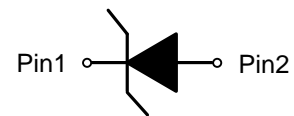


ESD9N5V
1-Line, Uni-directional, Transient Voltage Suppressor
<http://www.sh-willsemi.com>
Descriptions

The ESD9N5V is a uni-directional TVS (Transient Voltage Suppressor). It is specifically designed to protect sensitive electronic components that may be subjected to ESD (Electrostatic Discharge), EFT (Electrical Fast Transients) and Lightning. It is particularly well-suited for cellular phones, portable device, digital cameras, power supplies and many other portable applications because of its small package and low weight.

The ESD9N5V may be used to provide ESD protection up to $\pm 30\text{kV}$ (contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to 10 A (8/20 μs) according to IEC61000-4-5.

The ESD9N5V is available in DFN1006-2L package. Standard products are Pb-free and Halogen-free.


DFN1006-2L

Circuit diagram


* = Date code (A~Z)
H = Device code

Marking (Top View)
Features

- Stand-off voltage: 5V Max.
- Transient protection for each line according to IEC61000-4-2 (ESD): $\pm 30\text{kV}$ (contact discharge)
IEC61000-4-4 (EFT): 40A (5/50ns)
IEC61000-4-5 (surge): 10A (8/20 μs)
- Capacitance: $C_J = 60\text{pF}$ Typ.
- Solid-state silicon technology

Applications

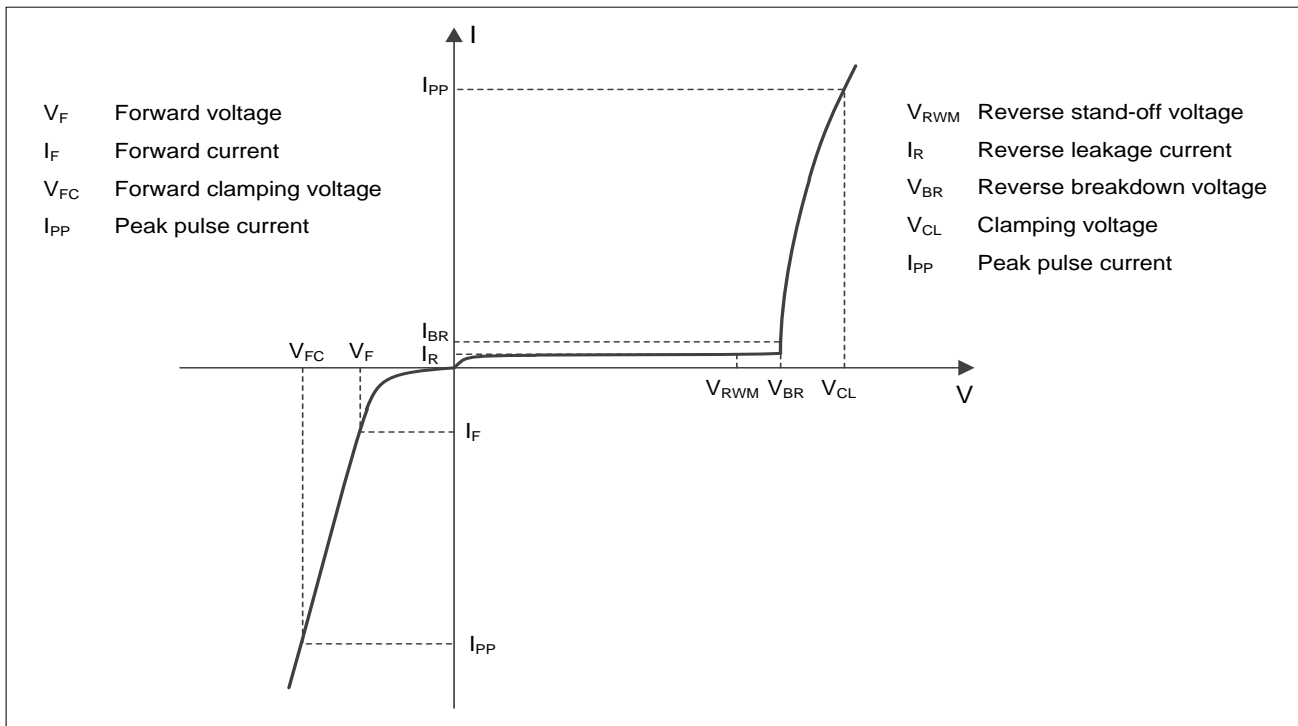
- Cell phone handsets and accessories
- Personal Digital Assistants (PDAs)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Digital Cameras

Order information

Device	Package	Shipping
ESD9N5V-2/TR	DFN1006-2L	10000/Tape&Reel

Absolute maximum ratings

Parameter	Symbol	Rating	Unit
Peak pulse power ($t_p = 8/20\mu s$)	P_{pk}	150	W
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	10	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 30	kV
ESD according to IEC61000-4-2 contact discharge		± 30	
Junction temperature	T_J	125	$^{\circ}C$
Operating temperature	T_{OP}	-40~85	$^{\circ}C$
Lead temperature	T_L	260	$^{\circ}C$
Storage temperature	T_{STG}	-55~150	$^{\circ}C$

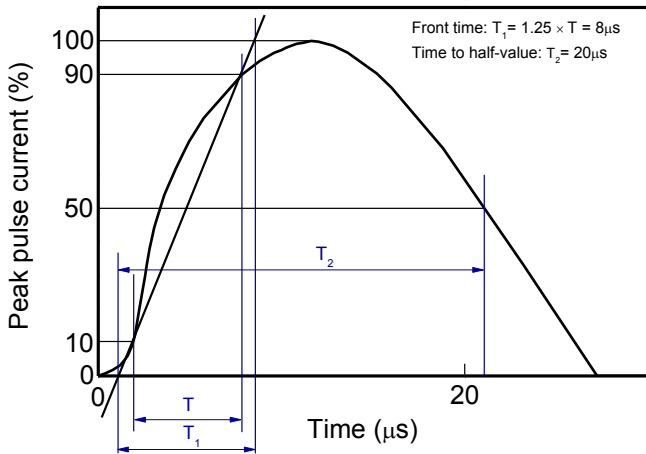
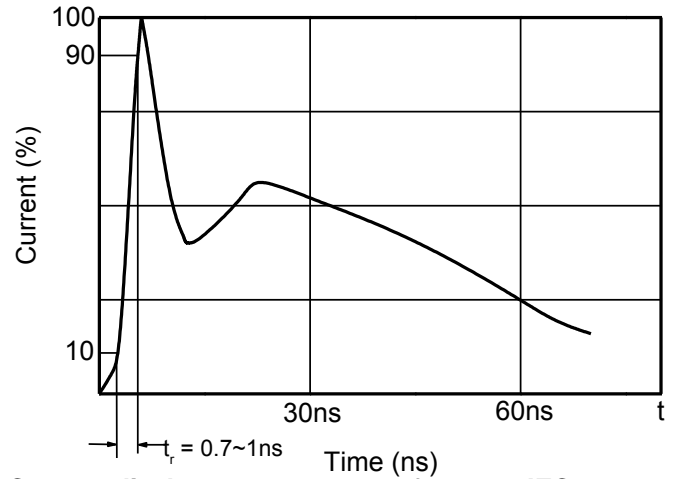
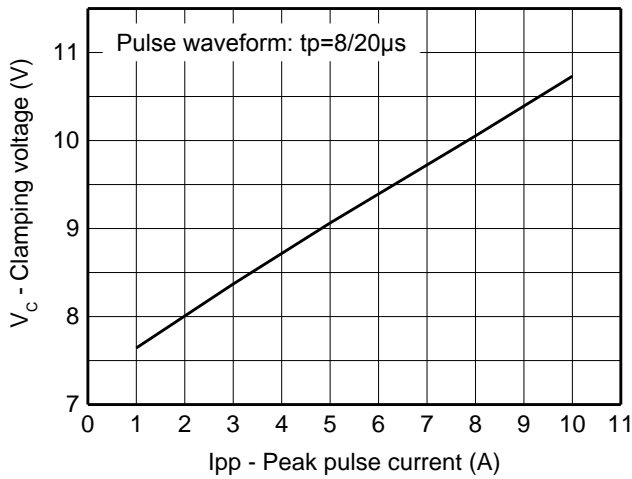
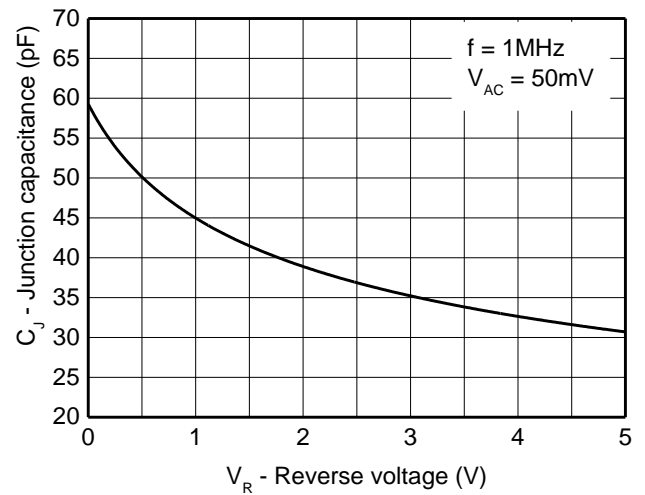
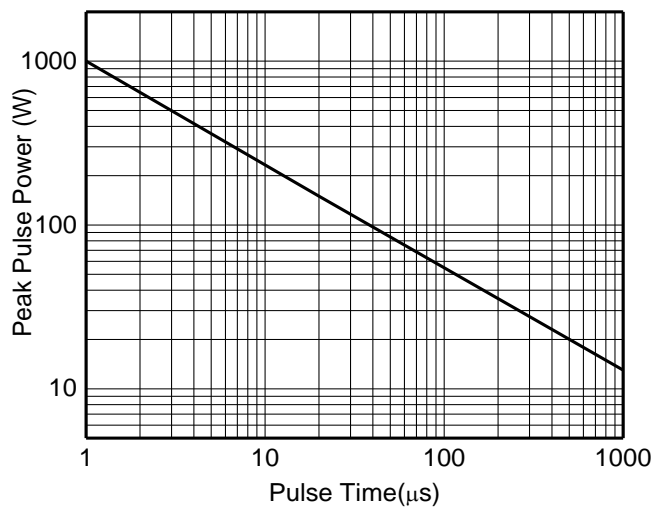
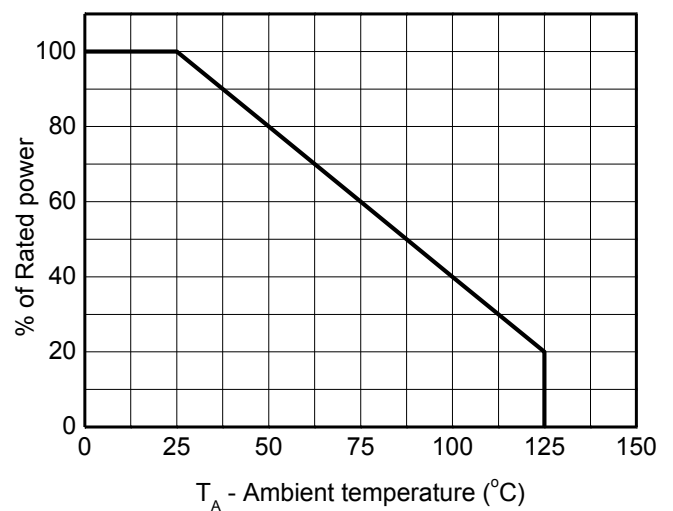
Electrical characteristics ($T_A = 25^{\circ}C$, unless otherwise noted)

Definitions of electrical characteristics

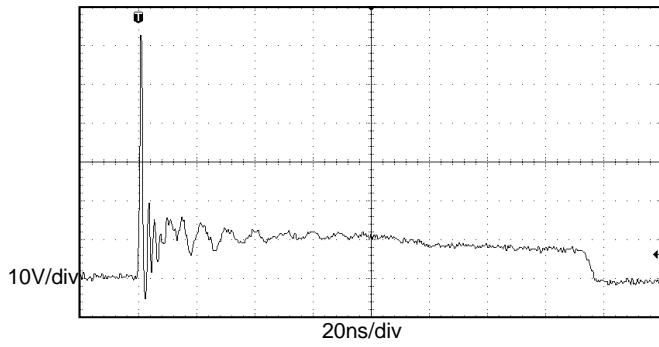
Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V _{RWM}				5	V
Reverse leakage current	I _R	V _{RWM} = 5V			1	μA
Reverse breakdown voltage	V _{BR}	I _T = 1mA	6.0		8.0	V
Forward voltage	V _F	I _F = 10mA	0.4	0.8	1.3	V
Clamping voltage ¹⁾	V _{CL}	I _{PP} = 16A, t _p = 100ns		11		V
Clamping voltage ²⁾	V _{CL}	V _{ESD} = 8kV		11		V
Clamping voltage ³⁾	V _C	I _{pp} =1A t _p =8/20μs			10	V
		I _{pp} =10A t _p =8/20μs			15	V
Dynamic resistance ¹⁾	R _{DYN}			0.25		Ω
Junction capacitance	C _J	V _R = 0V, f = 1MHz		60	80	pF
		V _R =5V, f = 1MHz		30	40	pF

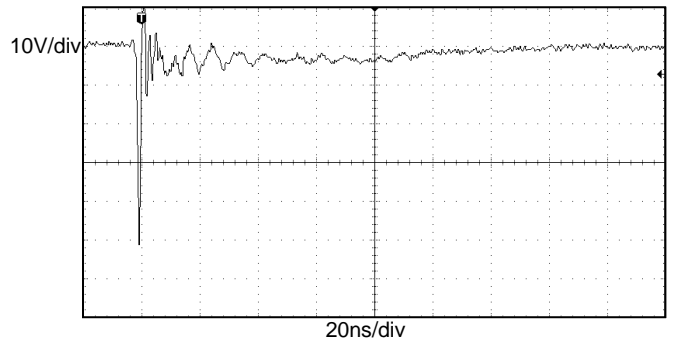
Notes:

- 1) TLP parameter: Z₀ = 50Ω, t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
- 2) Contact discharge mode, according to IEC61000-4-2.
- 3) Non-repetitive current pulse, according to IEC61000-4-5.

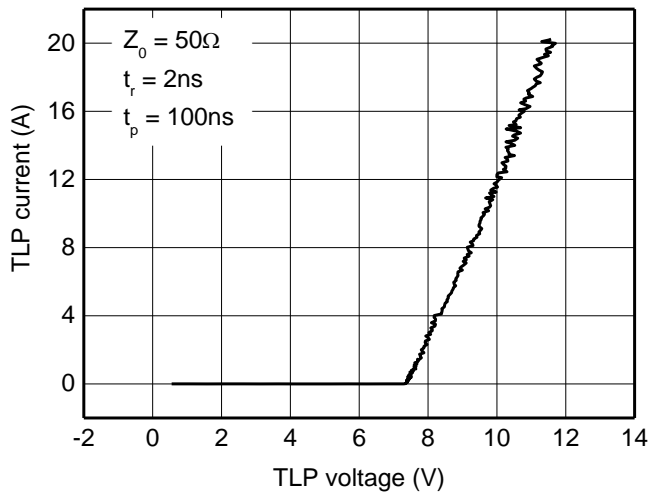
Typical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

8/20 μs waveform per IEC61000-4-5

Contact discharge current waveform per IEC61000-4-2

Clamping voltage vs. Peak pulse current

Capacitance vs. Reverse voltage

Non-repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature

Typical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)


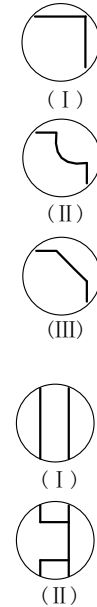
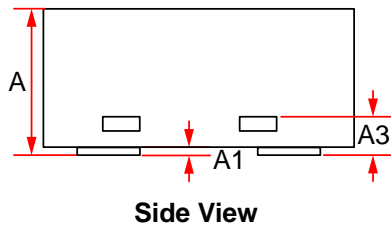
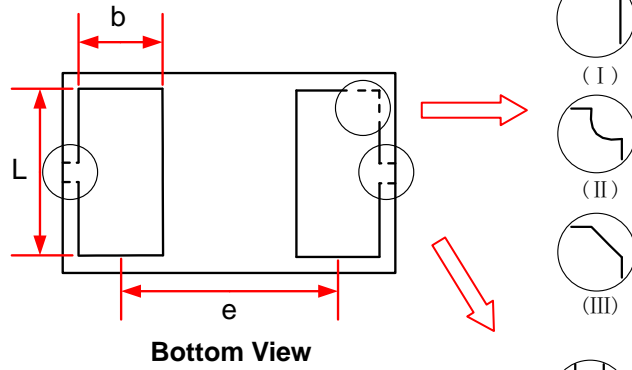
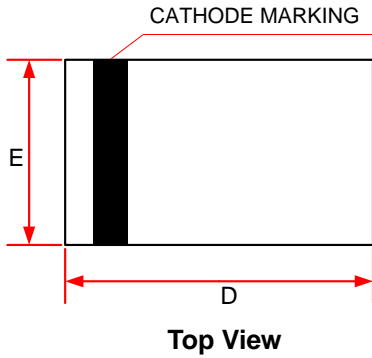
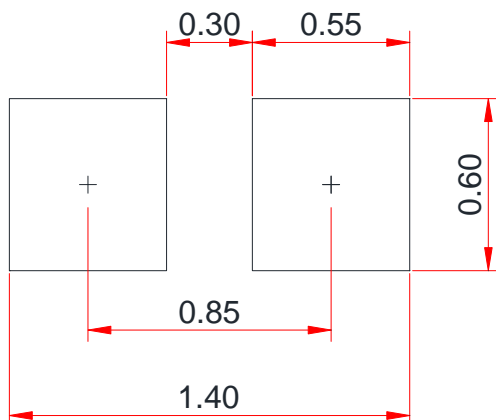
ESD clamping
(+8kV contact discharge per IEC61000-4-2)



ESD clamping
(-8kV contact discharge per IEC61000-4-2)



TLP Measurement

Package outline dimensions
DFN1006-2L

Recommend land pattern (Unit: mm)


Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.340	-	0.530
A1	0.000	-	0.050
A3	0.125 REF.		
D	0.950	1.000	1.075
E	0.550	0.600	0.675
b	0.200	0.250	0.300
L	0.450	0.500	0.550
e	0.650 Typ.		

Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.