

June 2014

FDMA430NZ Single N-Channel 2.5V Specified PowerTrench[®] MOSFET

Single N-Channel 2.5V Specified PowerTrench[®] MOSFET

30V, 5.0A, 40mΩ

General Description

This Single N-Channel MOSFET has been designed using Fairchild Semiconductor's advanced Power Trench process to optimize the $\rm R_{DS}(on) @V_{GS}{=}2.5V$ on special MicroFET leadframe.

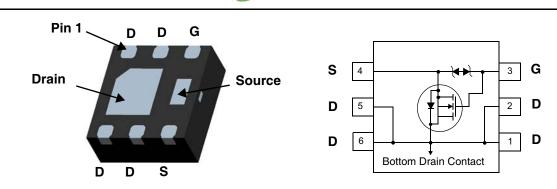
Applications

Li-lon Battery Pack



Features

- $R_{DS(on)} = 40m\Omega$ @ $V_{GS} = 4.5$ V, $I_D = 5.0$ A
- $R_{DS(on)} = 50m\Omega$ @ $V_{GS} = 2.5$ V, $I_D = 4.5A$
- Low Profile-0.8mm maximum-in the new package MicroFET 2x2 mm
- HBM ESD protection level > 2.5k V typical (Note 3)
- Free from halogenated compounds and antimony oxides
- RoHS Compliant



MicroFET 2X2 (Bottom View)

Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		±12	V
1	Drain Current -Continuous	(Note 1a)	5.0	
D	-Pulsed		20	A
D	Power dissipation (Steady State)	(Note 1a)	2.4	w
P _D		(Note 1b)	0.9	vv
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

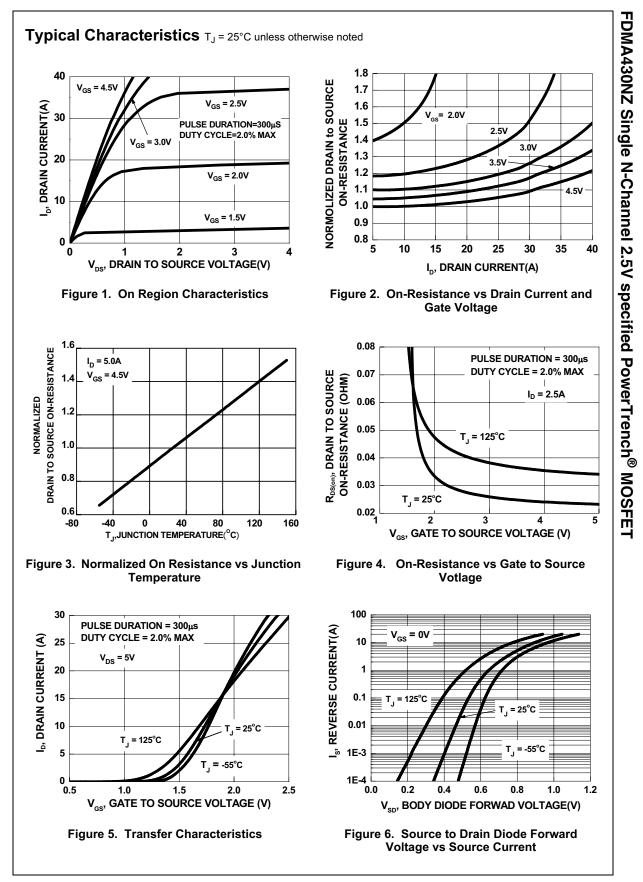
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	52	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1b)	145	°C/W

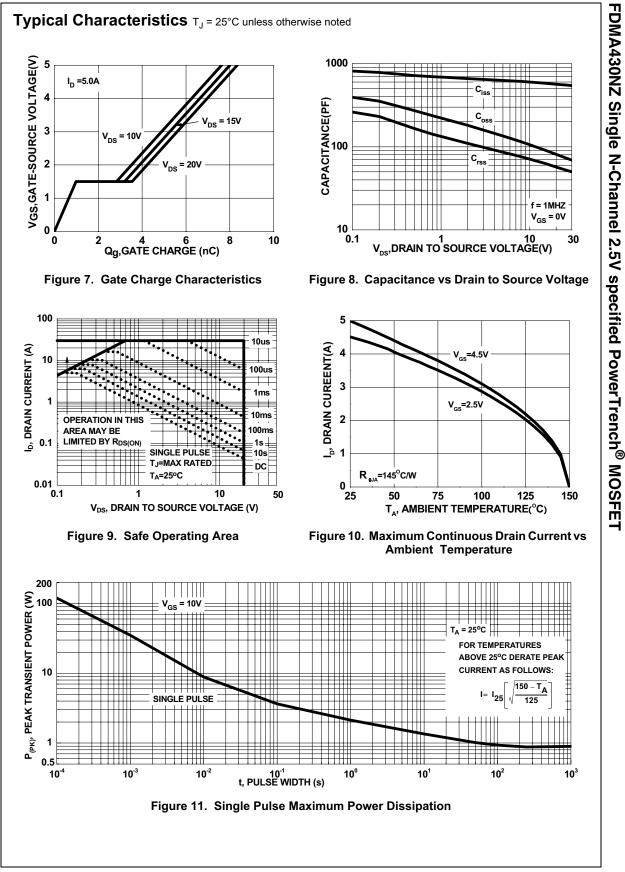
Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
430	FDMA430NZ	7"	8 mm	3000 units

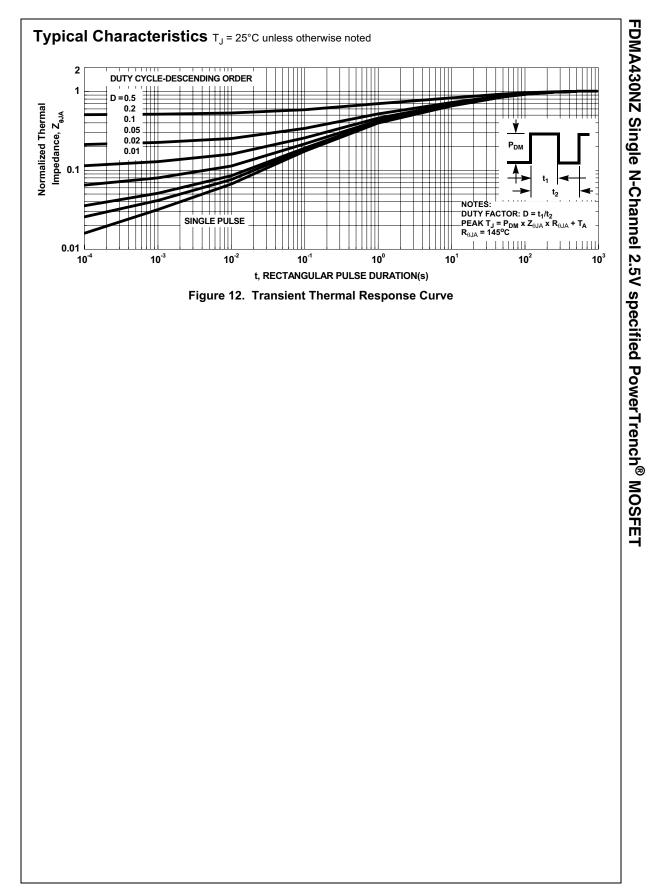
Off Chara B _{VDSS} ΔB _{VDSS}		Test Conditions	Min	Тур	Max	Units
B _{VDSS} <u> </u>	cteristics					
ΔB _{VDSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_{D} = 250 \mu A$	30			V
	Breakdown Voltage Temperature	$I_{\rm D} = 250 \mu A$,				
ΔT_{J}	Coefficient	Referenced to 25°C		25.2		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V,$			1	μΑ
I _{GSS}	Gate-Body Leakage,	$V_{GS} = \pm 12V, V_{DS} = 0V$			±10	μA
On Chara	cteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.6	0.81	1.5	V
$\Delta V_{GS(th)}$	Gate Threshold Voltage	$I_{\rm D} = 250 \mu A,$				
ΔT_J	Temperature Coefficient	Referenced to 25°C		-3.2		mV/°C
		$V_{GS} = 4.5V, I_D = 5.0A$		23.6	40	
		$V_{GS}=4.0V, \hspace{1em} I_{D}=5.0A$		23.9	41	
R _{DS(ON)}	Static Drain-Source On-Resistance	$V_{GS} = 3.1V, I_{D} = 4.5A$		25.4	43	0mΩ
D3(UN)		$V_{GS} = 2.5V, I_{D} = 4.5A$		27.6	50	
		V _{GS} = 4.5V, I _D =5.0A, T _{.1} =150°C		37.0	61	
9 _{FS}	Forward Transconductance	V _{DS} = 5V, I _D =5.0A		25.6		S
	Characteristics					•
	Input Capacitance	$V_{-} = 10V_{-}V_{-} = 0V_{-}$		600	800	pF
	Output Capacitance	V _{DS} = 10V, V _{GS} =0V, f = 1.0MHz		110	150	pF
C _{rss}	Reverse Transfer Capacitance			75	115	pF
R _G	Gate Resistance	f = 1.0MHz		3.5		ρ. Ω
	Characteristics (Note 2)	V = 10V L = 10		r		
t _{d(on)}	Turn-On Delay Time Turn-On Rise Time	$V_{DD} = 10V, I_{D} = 1A$		8.3 7.1	17 15	ns ns
t _{d(on)} t _r		$V_{DD} = 10V, I_D = 1A \\ V_{GS} = 4.5V, R_{GEN} = 6\Omega$				
t _{d(on)} t _r t _{d(off)}	Turn-On Rise Time			7.1	15	ns
t _{d(on)} t _r t _{d(off)} t _f	Turn-On Rise Time Turn-Off Delay Time	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$		7.1 18.1	15 37	ns ns
t _{d(on)} tr t _{d(off)} t _f Q _g	Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$ $V_{DS} = 10V, I_{D} = 5.0A,$		7.1 18.1 6.0	15 37 12	ns ns ns
t _{d(on)} tr t _{d(off)} t _f Q _g Q _{gs}	Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$		7.1 18.1 6.0 7.3	15 37 12 11	ns ns ns nC
t _{d(on)} t _r t _{d(off)} t _f Q _g Q _{gs} Q _{gd}	Turn-On Rise TimeTurn-Off Delay TimeTurn-Off Fall TimeTotal Gate ChargeGate-Source ChargeGate-Drain Charge	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$ $V_{DS} = 10V, I_D = 5.0A,$ $V_{GS} = 4.5V$		7.1 18.1 6.0 7.3 0.8	15 37 12 11 2	ns ns nS nC nC
t _{d(on)} t _r t _{d(off)} t _f Q _g Q _{gs} Q _{gd} Drain-Sou	Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$ $V_{DS} = 10V, I_D = 5.0A,$ $V_{GS} = 4.5V$ Maximum Ratings		7.1 18.1 6.0 7.3 0.8	15 37 12 11 2	ns ns nS nC nC
t _{d(on)} t _r t _{d(off)} t _f Q _g Q _{gs} Q _{gd} Drain-Sou	Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Irce Diode Characteristics and Maximum Continuous Drain-Source Diod	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$ $V_{DS} = 10V, I_D = 5.0A,$ $V_{GS} = 4.5V$ Maximum Ratings de Forward Current		7.1 18.1 6.0 7.3 0.8	15 37 12 11 2 3	ns ns nC nC nC
t _{d(on)} t _r t _{d(off)} t _f Q _g Q _{gs} Q _{gd}	Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{GS} = 4.5V, R_{GEN} = 6\Omega$ $V_{DS} = 10V, I_D = 5.0A,$ $V_{GS} = 4.5V$ Maximum Ratings		7.1 18.1 6.0 7.3 0.8 1.9	15 37 12 11 2 3 2.0	ns ns nC nC nC A

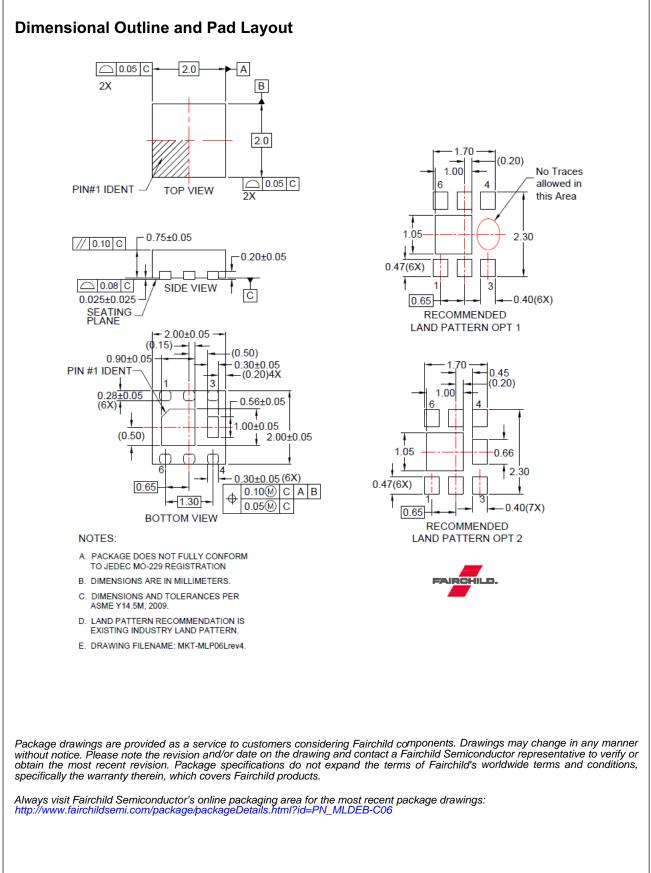
FDMA430NZ Rev B6





FDMA430NZ Rev B6







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