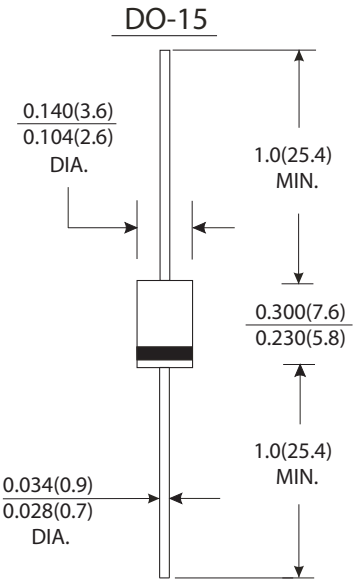


Features

- Fast switching
- Low leakage
- Low forward voltage drop
- High current capability
- High current surge
- High reliability

Mechanical Data

- Case : JEDEC DO-15 molded plastic body
- Terminals : Plated axial lead solderable per MIL-STD-750, method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight : 0.014 ounce, 0.39 gram



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

(Ratings at 25°C ambient temperature unless otherwise specified, Single phase, half wave 60Hz, resistive or inductive load. For capacitive load, derate by 20%)

	Symbols	FR 201	FR 202	FR 203	FR 204	FR 205	FR 206	FR 207	Units
Maximum recurrent peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	Volts
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	Volts
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	Volts
Maximum average forward rectified current 0.375"(9.5mm) lead length T _A =75°C	I <sub(av)< sub=""></sub(av)<>	2.0							Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	70.0							Amps
Maximum instantaneous forward voltage at 2.0A	V _F	1.3							Volts
Maximum DC reverse current at rated DC blocking voltage	I _R	5.0							μ A
Maximum full load reverse current full cycle average. 0.375"(9.5mm) lead length at T _L =55°C		100							
Maximum reverse recovery time (Note 1)	T _{rr}	150			250	500		ns	
Typical junction capacitance (Note 2)	C _J	40							pF
Operating junction and storage temperature range	T _J T _{STG}	-65 to +150							°C

Notes:

- (1) Test conditions: I_F=0.5A, I_R=1.0A, I_{rr}=0.25A.
- (2) Measured at 1MHz and applied reverse voltage of 4.0 Volts.

RATINGS AND CHARACTERISTIC CURVES FR201 THRU FR207

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

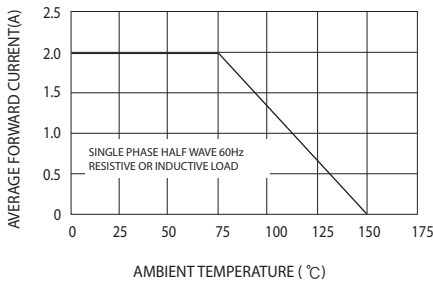


FIG.2-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

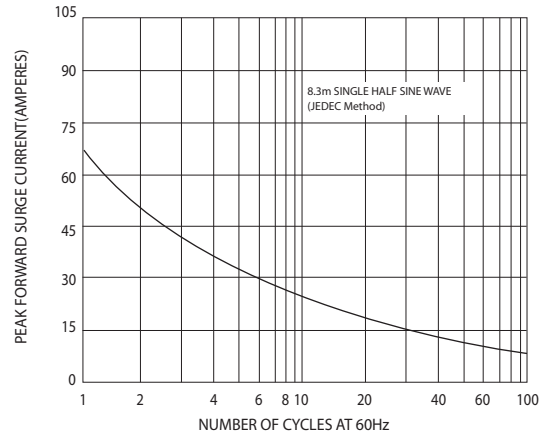


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

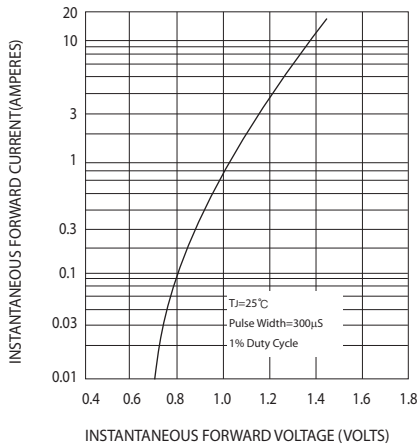


FIG.4-TYPICAL JUNCTION CAPACITANCE

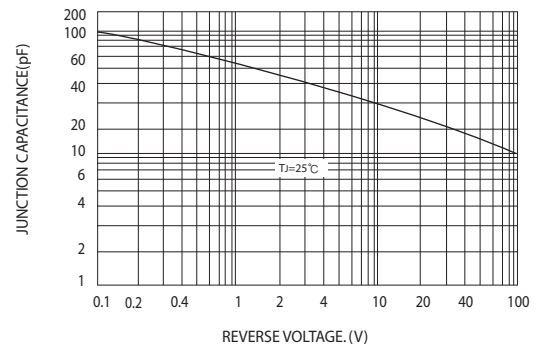
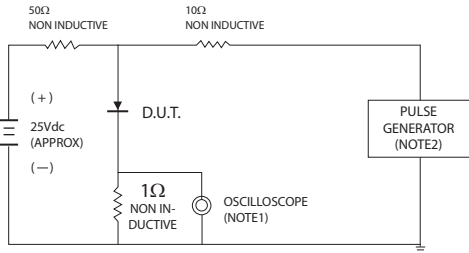
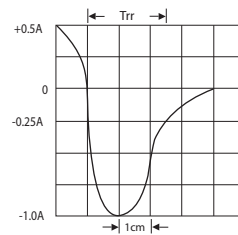


FIG.5-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



NOTES: 1. Rise Time=7ns max. input impedance=1 megohm 22pF
 2. Rise Time=10ns max. source impedance =50 ohms



SET TIME BASE FOR 50/100 ns/cm