Silicon Carbide Schottky Diode



VRRM =	650 V
F (Tc = 156°C) =	4 A
Qc =	10 nC

Features

Advantages

- Gen5 Thin Chip Technology for Low V_{F}
- Low Conduction Losses for All Load Conditions
- Superior Figure of Merit Q_C/I_F
- Enhanced Surge Current Robustness
- Low Thermal Resistance
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness

• Optimal Price Performance

Improved System Efficiency

• High System Reliability

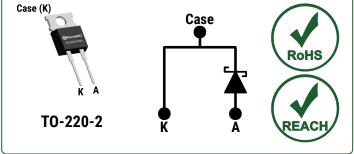
• Enables Extremely Fast Switching

Reduced Cooling Requirements

Increased System Power DensityZero Reverse Recovery Current

• Easy to Parallel without Thermal Runaway

Package



Applications

- Switched Mode Power Supply (SMPS)
- Solar Inverter
- Server and Telecom Power Supply
- Battery Charger
- Uninterruptible Power Supply (UPS)

Absolute Maximum Ratings (At Tc = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	V _{RRM}		650	V	
		T _C = 100°C, D = 1	9		
ontinuous Forward Current	lF	T _C = 135°C, D = 1	6	Α	Fig. 4
		T _C = 156°C, D = 1	4		
Non-Repetitive Peak Forward Surge Current, Half Sine	I	T _C = 25°C, t _P = 10 ms	22	٨	
Wave	I _{F,SM}	Tc = 150°C, t⊦ = 10 ms	18	А	
Popotitive Pook Forward Surge Current, Half Sine Wave	I _{F,RM}	T _C = 25°C, t _P = 10 ms	14	٨	
Repetitive Peak Forward Surge Current, Half Sine Wave		Tc = 150°C, t⊦ = 10 ms	10	A	
Non-Repetitive Peak Forward Surge Current	I _{F,MAX}	T _C = 25°C, t _P = 10 μs	110	Α	
i ² t Value	∫i²dt	T _C = 25°C, t _P = 10 ms	2.42	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 6.5 mH, I _{AS} = 4 A	53	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 520 V	200	V/ns	
Power Dissipation	Ртот	T _C = 25°C	55	W	Fig. 3
Operating and Storage Temperature	Tj, T _{stg}		-55 to 175	°C	



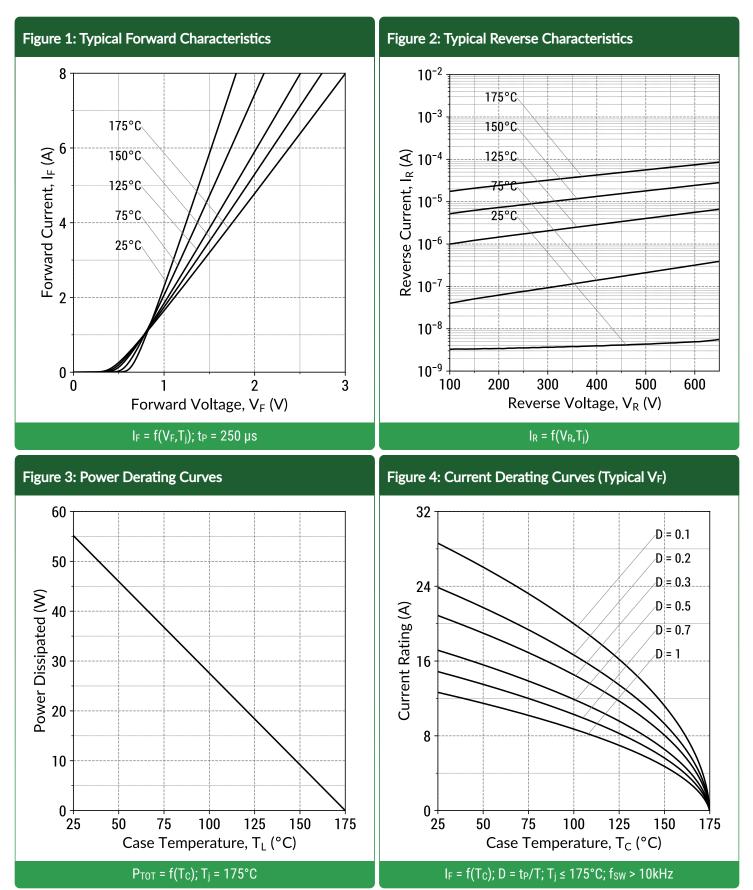
Electrical Characteristics

Deremeter	Cumbal	Conditions		Values		11	Nata	
Parameter	Symbol			Min.	Тур.	Max.	Unit	Note
Diode Forward Voltage	V _F	I _F = 4 A, T _j = 25°C			1.25	1.35	V	Fig. 1
Jioue Forwaru voltage	۷F	I _F = 4 A, T _j = 150°C			1.64			
Reverse Current	ln.	V _R = 650 V, T	j = 25°C		1	5	μA	Fig. 2
	IR	V _R = 650 V, T _j = 150°C			28		μΑ	1 ig. 2
Total Capacitive Charge	Qc		V _R = 200 V		7		nC	Fig. 7
	QC	I _F ≤ I _{F,MAX}	V _R = 400 V		10			
Switching Time	ts	dI _F /dt = 200 A/µs	V _R = 200 V		< 10		ne	
	ts		V _R = 400 V		< 10		ns	
Total Capacitance	С	V _R = 1 V, f = 1MHz V _R = 400 V, f = 1MHz			186		pF	Fig. 6
	U				13			

Thermal/Package Characteristics

Parameter	Symbol Co	Conditions		Values			Note
Parallieler		Conditions	Min.	Typ.	Max.	Unit	Note
Thermal Resistance, Junction - Case	RthJC			2.72		°C/W	Fig. 9
Weight	WT			2.0		g	
Mounting Torque	T _M	Screws to Heatsink			1.0	Nm	

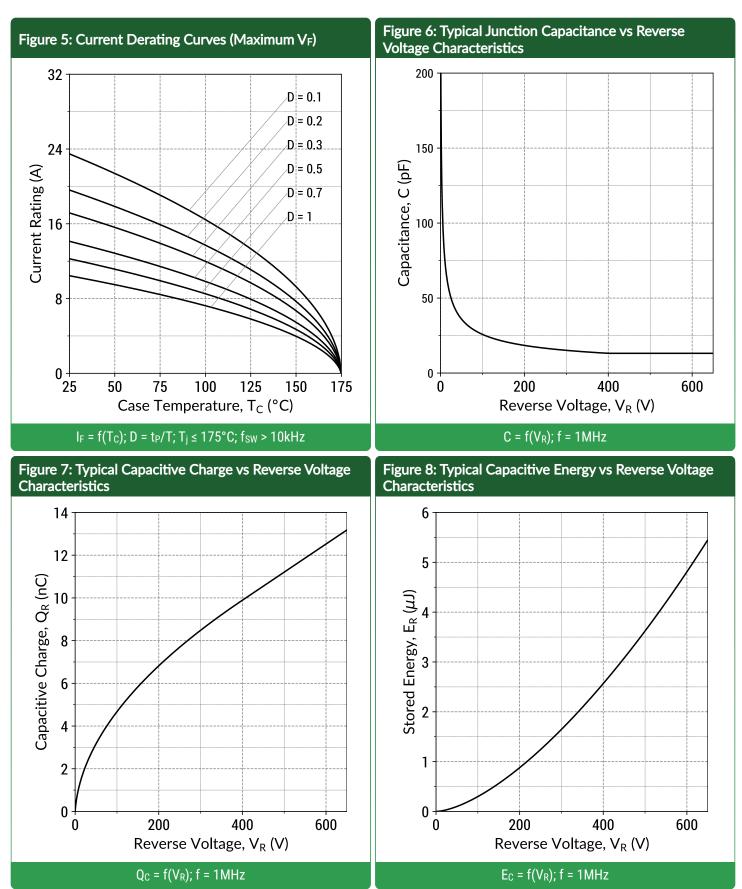


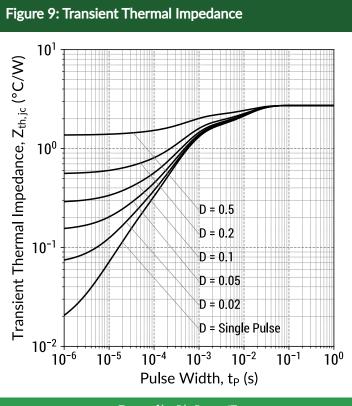


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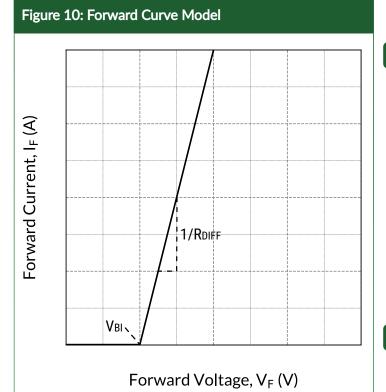
GE04MPS06A 650V 4A SiC Schottky MPS[™] Diode







 $Z_{th,jc} = f(t_P,D); D = t_P/T$



 $I_F = f(V_F, T_j)$

Forward Curve Model Equation:

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

 $V_{BI}(T_j) = m \times T_j + n (V)$ m = -0.00124 (V/°C) n = 0.72 (V)

Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ a = 2.41e-06 (\Omega/°C^2) b = 0.000681 (\Omega/°C) c = 0.12 (\Omega)

Forward Power Loss Equation:

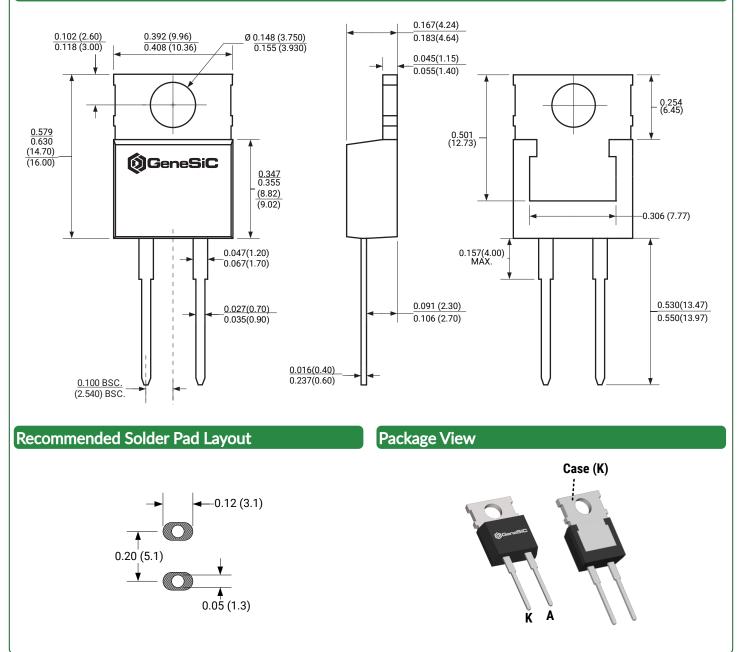
 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$





Package Dimensions

TO-220-2 Package Outline



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.



Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Related Links

- 1		
	SPICE Models:	https://www.genesicsemi.com/sic-schottky-mps/GE04MPS06A/GE04MPS06A_SPICE.zip
	PLECS Models:	https://www.genesicsemi.com/sic-schottky-mps/GE04MPS06A/GE04MPS06A_PLECS.zip
	CAD Models:	https://www.genesicsemi.com/sic-schottky-mps/GE04MPS06A/GE04MPS06A_3D.zip
	• Evaluation Boards:	: https://www.genesicsemi.com/technical-support
	 Reliability: 	https://www.genesicsemi.com/reliability
	 Compliance: 	https://www.genesicsemi.com/compliance
	• Quality Manual:	https://www.genesicsemi.com/quality

Revision History			
Date	Revision	Comments	Supersedes
Jul. 27, 2020	Rev 1	Initial Release	



www.genesicsemi.com/sic-schottky-mps/



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