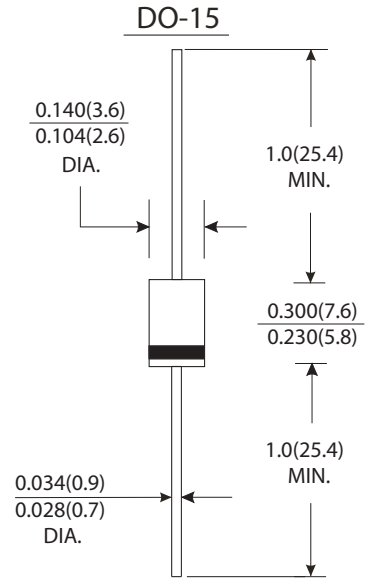


Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Low forward voltage drop
- High current capability
- High reliability
- Low power loss, high efficiency
- Glass passivated junction
- High speed switching
- Low leakage

Mechanical Data

- Case : JEDEC DO-15 molded plastic body
- Epoxy : UL94V-0 rate flame retardant
- Lead : Plated axial lead solderable per MIL-STR-750, method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight : 0.014 ounce, 0.39 gram



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

(Ratings at 25 °C ambient temperature unless otherwise specified, Single phase, half wave 60Hz, resistive or inductive load. For capacitive load, derate by 20%)

| | Symbols | HER 151G | HER 152G | HER 153G | HER 154G | HER 155G | HER 156G | HER 157G | HER 158G | Units |
|--|------------------------------------|-------------|----------|----------|----------|----------|----------|----------|----------|-------|
| Maximum recurrent peak reverse voltage | V _{RRM} | 50 | 100 | 200 | 300 | 400 | 600 | 800 | 1000 | Volts |
| Maximum RMS voltage | V _{RMS} | 35 | 70 | 140 | 210 | 280 | 420 | 560 | 700 | Volts |
| Maximum DC blocking voltage | V _{DC} | 50 | 100 | 200 | 300 | 400 | 600 | 800 | 1000 | Volts |
| Maximum average forward rectified current 0.375"(9.5mm) lead length @ at T _A =55 °C | I <sub(av)< sub=""></sub(av)<> | 1.5 | | | | | | | | Amps |
| Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC method) | I _{FSM} | 50.0 | | | | | | | | Amps |
| Maximum instantaneous forward voltage at 2.0A | V _F | 1.0 | | 1.3 | | 1.7 | | | Volts | |
| Maximum DC reverse current at rated DC blocking voltage at T _A =25 °C | I _R | 5.0 | | | | | | | | μA |
| Maximum DC reverse current at rated DC blocking voltage at T _A =125 °C | | 100 | | | | | | | | |
| Maximum reverse recovery time (Note 1) | T _{rr} | 50 | | | | | 75 | | | ns |
| Typical junction capacitance (Note 2) | C _J | 50 | | | | | 30 | | | pF |
| Operating junction and storage temperature range | T _J T _{STG} | -55 to +150 | | | | | | | | °C |

Notes:

- (1) Test conditions: I_F=0.5A, I_R=1.0A, I_{rr}=0.25A.
- (2) Measured at 1MHz and applied reverse voltage of 4.0 Volts.

