

HER151G - HER158G

1.5 AMPS. Glass Passivated High Efficient Rectifiers

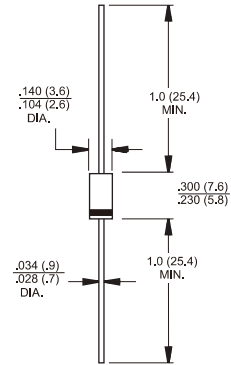
DO-15

Features

- ◇ Glass passivated chip junction.
- ◇ High efficiency, Low VF
- ◇ High current capability
- ◇ High reliability
- ◇ High surge current capability
- ◇ For use in low voltage, high frequency inverter, free wheeling, and polarity protection application.
- ◇ Green compound with suffix "G" on packing code & prefix "G" on datecode.

Mechanical Data

- ◇ Case: Molded plastic DO-15
- ◇ Epoxy: UL 94V0 rate flame retardant
- ◇ Lead: Pure tin plated, lead free, solderable per MIL-STD-202, Method 208 guaranteed
- ◇ Polarity: Color band denotes cathode
- ◇ High temperature soldering guaranteed: 260°C/10 seconds/.375", (9.5mm) lead lengths at 5 lbs., (2.3kg) tension
- ◇ Mounting position: Any
- ◇ Weight: 0.40 grams



Dimensions in inches and (millimeters)

Marking Diagram



- HER15XG = Specific Device Code
- G = Green Compound
- Y = Year
- WW = Work Week

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60 Hz, resistive or inductive load.
For capacitive load, derate current by 20%

Type Number	Symbol	HER 151G	HER 152G	HER 153G	HER 154G	HER 155G	HER 156G	HER 157G	HER 158G	Units	
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	300	400	600	800	1000	V	
Maximum RMS Voltage	V_{RMS}	35	70	140	210	280	420	560	700	V	
Maximum DC Blocking Voltage	V_{DC}	50	100	200	300	00	600	800	1000	V	
Maximum Average Forward Rectified Current .375 (9.5mm) Lead Length @ $T_A = 55^\circ C$	$I_{F(AV)}$	1.5								A	
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	50								A	
Maximum Instantaneous Forward Voltage @ 1.5A	V_F	1.0			1.3		1.7			V	
Maximum DC Reverse Current at Rated DC Blocking Voltage @ $T_A = 25^\circ C$ (Note 1) @ $T_A = 125^\circ C$	I_R	5.0					150				uA uA
Maximum Reverse Recovery Time (Note 4)	T_{rr}	50				75				nS	
Typical Junction Capacitance (Note 2)	C_j	35				20				pF	
Typical Thermal Resistance (Note 3)	$R_{\theta JA}$	60								°C/W	
Operating Temperature Range	T_J	-65 to +150								°C	
Storage Temperature Range	T_{STG}	-65 to +150								°C	

- Notes: 1. Pulse Test with PW=300 usec, 1% Duty Cycle
2. Measured at 1 MHz and Applied Reverse Voltage of 4.0 V D.C.
3. Mount on Cu-Pad Size 10mm x 10mm on PCB.
4. Reverse Recovery Test Conditions: $I_F = 0.5A$, $I_R = 1.0A$, $I_{RR} = 0.25A$

RATINGS AND CHARACTERISTIC CURVES (HER151G THRU HER158G)

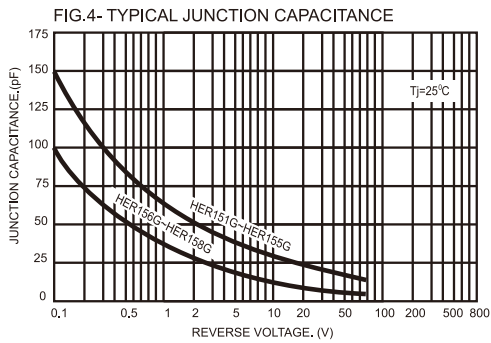
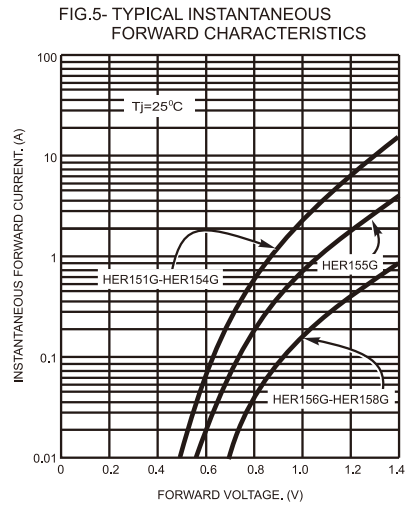
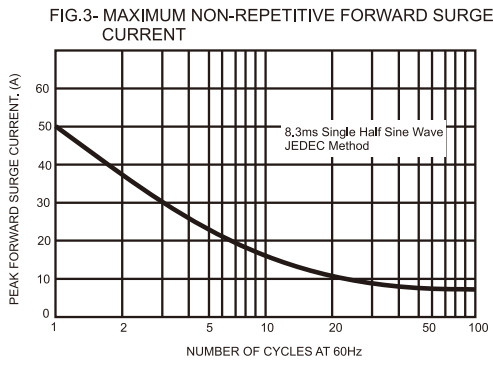
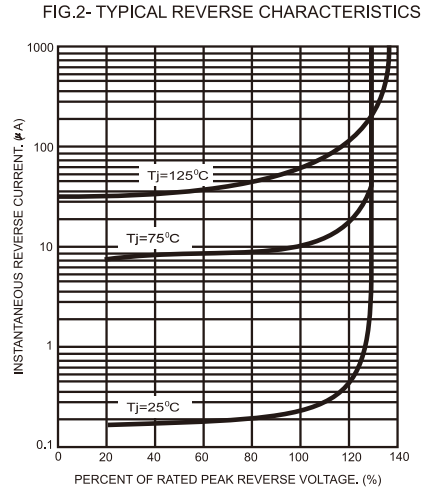
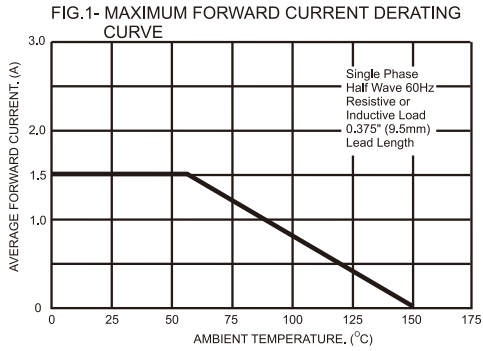


FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

