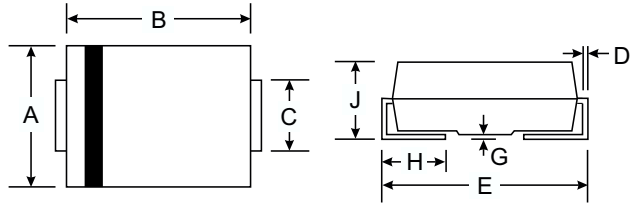


#### Features

- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop, High Efficiency
- Surge Overload Rating to 100A Peak
- Low Power Loss
- Ultra-Fast Recovery Time
- Plastic Case Material has UL Flammability Classification Rating 94V-O



#### Mechanical Data

- Case: SMC/DO-214AB, Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: SMB Weight: 0.093 grams (approx.)  
SMC Weight: 0.20 grams (approx.)
- **Lead Free: For RoHS / Lead Free Version**

SMC/DO-214AB		
Dim	Min	Max
A	5.59	6.22
B	6.60	7.11
C	2.75	3.25
D	0.152	0.305
E	7.75	8.13
F	2.00	2.62
G	0.051	0.203
H	0.76	1.27
All Dimensions in mm		

#### Maximum Ratings and Electrical Characteristics @T<sub>A</sub>=25°C unless otherwise specified

Characteristic	Symbol	HS5A	HS5 B	HS5 D	HS5G	HS5J	HS5K	HS5M	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Working Peak Reverse Voltage	V <sub>VRM</sub>								
DC Blocking Voltage	V <sub>R</sub>								
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	70	140	280	420	560	800	V
Average Rectified Output Current @T <sub>L</sub> = 100°C	I <sub>O</sub>	5.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	100							A
Forward Voltage @I <sub>F</sub> = 5.0A	V <sub>FM</sub>	1.0		1.3		1.7			V
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C	I <sub>RM</sub>	10 500							μA
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	50				75			nS
Typical Junction Capacitance (Note 2)	C <sub>j</sub>	25							pF
Typical Thermal Resistance (Note 3)	R <sub>θJL</sub>	30							°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-65 to +150							°C

Note: 1. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>rr</sub> = 0.25A. See figure 5.  
2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.  
3. Mounted on P.C. Board with 8.0mm<sup>2</sup> land area.

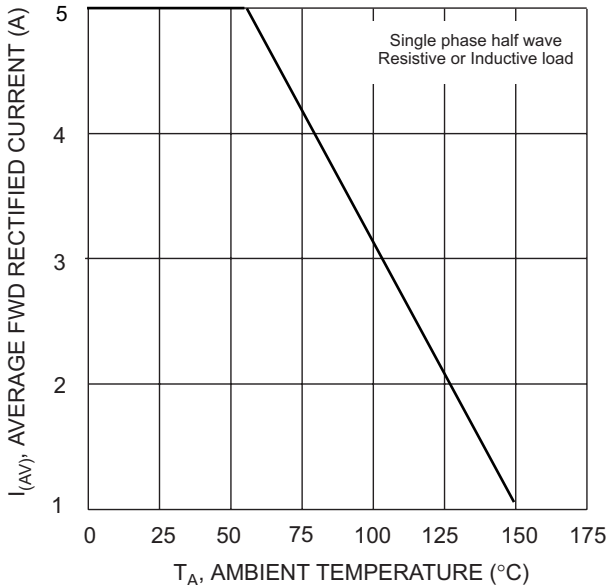


Fig. 1 Forward Current Derating Curve

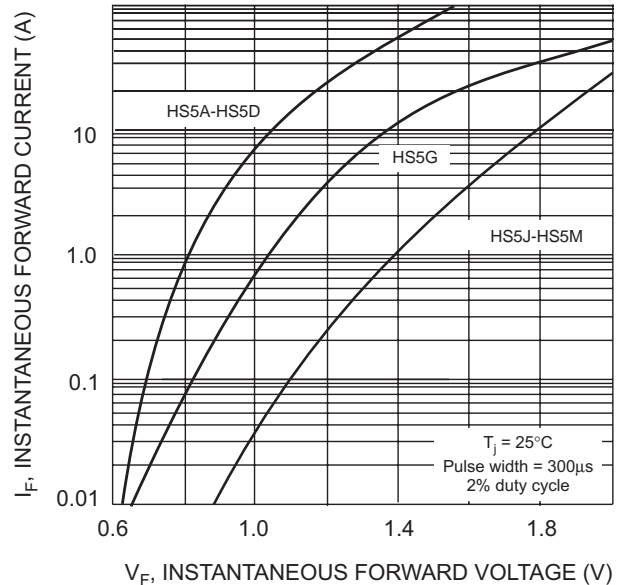


Fig. 2 Typical Forward Characteristics

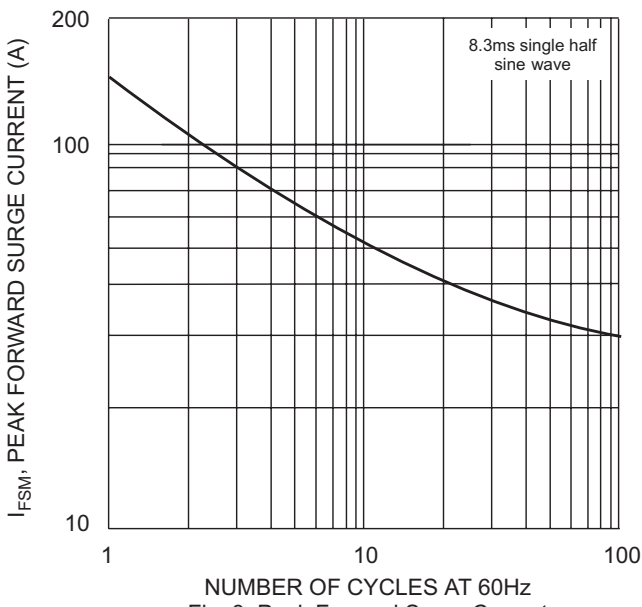


Fig. 3 Peak Forward Surge Current

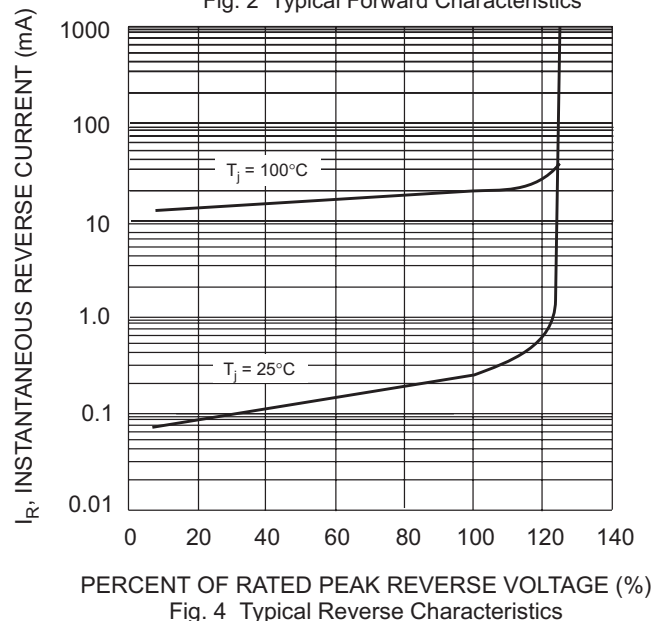
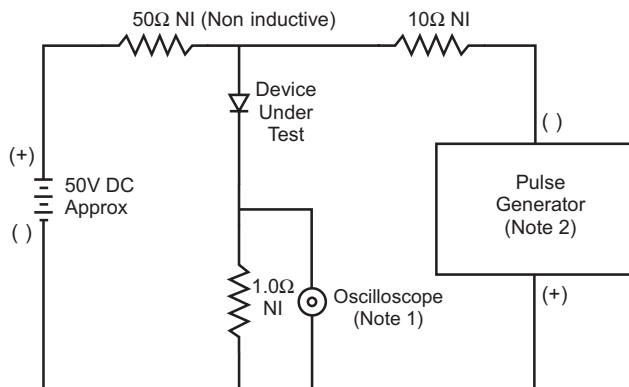
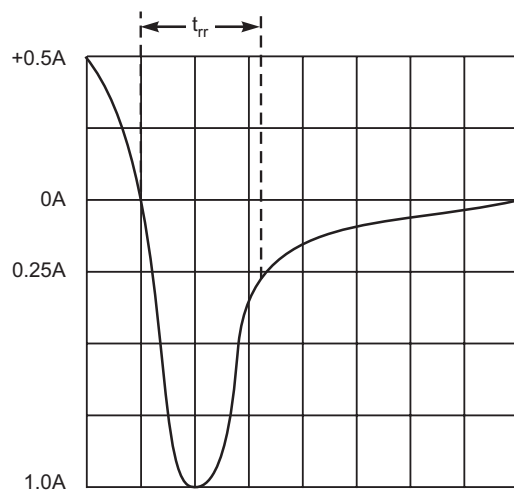


Fig. 4 Typical Reverse Characteristics



Notes:

1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.



Set time base for 5/10ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit