

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

## LM4862

Preliminary

# AUDIO POWER AMPLIFIER WITH SHUTDOWN MODE

#### DESCRIPTION

The UTC LM4862 is a built in bridge-connected form audio amplifier. In normal operation, it can deliver 500 mW (typ.) continuous power. The output load of the UTC LM4862 should be  $8\Omega$ , the supply voltage should be 5V, and the THD is as low as 1%.

As the most popular amplifiers, LM4862 is also designed to be able to provide high quality output power with less external components, such as the output capacitors for coupling, bootstrap capacitors, or snobbery networks.

In applications, the UTC LM4862 can be specially used as an ideal in low-power portable systems, for example, portable computers, desktop computers, and low voltage audio systems, etc.

#### **FEATURES**

\* Stable Unity Gain

\* Gain Configuration can be set by External Components

#### ORDERING INFORMATION

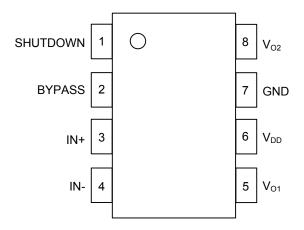
	DIP-8
FUT	60P-8

Ordering Number		Daakaga	Docking		
Lead Free	Halogen Free	Package	Packing		
LM4862L-D08-T	LM4862G-D08-T	DIP-8	Tube		
LM4862L-S08-R	LM4862G-S08-R	SOP-8	Tape Reel		

LM4862L-D08-T (1)Packing Type (2)Package Type	<ul> <li>(1) R: Tape Reel, T: Tube</li> <li>(2) D08: DIP-8, S08: SOP-8</li> <li>(3) G: Halogen Free L: Lead Free</li> </ul>
(3)Lead Free	(3) G: Halogen Free, L: Lead Free

# LM4862

### ■ PIN CONFIGURATION

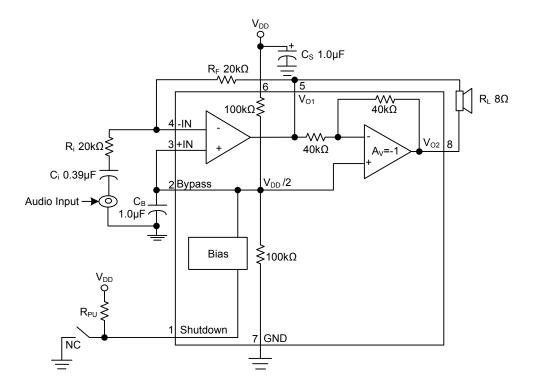


### ■ PIN DESCRIPTION

PIN NO	PIN NAME	DESCRIPTION
1	SHUTDOWN	Shutdown mode control input, high active
2	BYPASS	Connect to internal voltage divider for middle supply bias
3	IN+	Non-inverting power input pin
4	IN-	Inverting power input pin
5	V <sub>0</sub> 1	Output power pin 1
6	V <sub>DD</sub>	Power supply
7	GND	Ground
8	V <sub>0</sub> 2	Output power pin 2



### BLOCK DIAGRAM





### ■ **ABSOLUTE MAXIMUM RATING** (T<sub>a</sub>=25°C unless otherwise specified.)

	SYMBOL	RATINGS	UNIT
	V <sub>DD</sub>	6.0	V
	V <sub>IN</sub>	-0.3 ~ +(0.3+V <sub>DD</sub> )	V
DIP-8	P <sub>D</sub>	500	mW
SOP-8		300	mW
		220	°C
	TJ	+150	°C
	T <sub>STG</sub>	-65 ~ +150	°C
	DIP-8	V <sub>DD</sub> VIN           DIP-8           SOP-8           TJ	$\begin{tabular}{ c c c c c c } \hline $V_{DD}$ & $6.0$ \\ \hline $V_{IN}$ & $-0.3 \sim +(0.3 + V_{DD})$ \\ \hline $DIP-8$ & $P_D$ & $500$ \\ \hline $SOP-8$ & $P_D$ & $300$ \\ \hline $220$ & $220$ \\ \hline $T_J$ & $+150$ \\ \hline \end{tabular}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ ELECTRICAL CHARACTERISTICS (V<sub>CC</sub>=5V, f=1kHz, T<sub>a</sub>=25°C unless otherwise specified.)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V <sub>DD</sub>		2.0		5.5	V
V <sub>OS</sub>	V <sub>IN</sub> =0V		5.0	50	mV
I <sub>DD</sub>	V <sub>IN</sub> =0V, I <sub>O</sub> =0A		6.5	12.0	mA
I <sub>SD</sub>	V <sub>PIN1</sub> =V <sub>DD</sub>		0.6		μA
Po	THD=1%, f=1kHz, $R_L=8\Omega$		500		mW
	THD=10%, f=1kHz, R <sub>L</sub> =8Ω		800		mW
THD	f=1kHz, R∟=8Ω, Po = 500mW		0.5		%
PSRR	V <sub>CC</sub> =4.9V ~ 5.1V		60		dB
	V <sub>DD</sub> V <sub>OS</sub> I <sub>DD</sub> I <sub>SD</sub> P <sub>O</sub> THD	$\label{eq:VDD} V_{DD} & $$V_{IN}=0V$ \\ \hline V_{OS} & V_{IN}=0V, \ I_{O}=0A \\ \hline I_{DD} & V_{IN}=0V, \ I_{O}=0A \\ \hline I_{SD} & V_{PIN1}=V_{DD} \\ \hline P_{O} & $$THD=1\%, \ f=1kHz, \ R_{L}=8\Omega$ \\ \hline THD=10\%, \ f=1kHz, \ R_{L}=8\Omega$ \\ \hline THD & f=1kHz, \ R_{L}=8\Omega, \ Po=500mW \\ \hline \end{cases}$	$\begin{tabular}{ c c c c c c c } \hline V_{DD} & & & & & & & & & & & & & & & & & & $	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	$\begin{tabular}{ c c c c c c c } \hline V_{DD} & & & & & & & & & & & & & & & & & & $



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