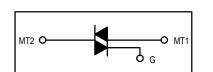
Triacs Silicon Bidirectional Thyristors

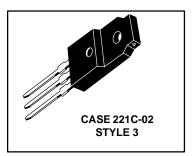
... designed primarily for full-wave ac control applications, such as solid-state relays, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

- Blocking Voltage to 800 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Three Modes (MAC15FP Series) or Four Modes (MAC15AFP Series)



MAC15FP Series MAC15AFP Series

> ISOLATED TRIACs THYRISTORS 15 AMPERES RMS 200 thru 800 VOLTS



MAXIMUM RATINGS (T_J = 25° C unless otherwise noted.)

Rating		Symbol	Value	Unit	
Repetitive Peak Off-State Voltage ⁽¹⁾ (T _J = -40 to +125°C, 1/2 Sine Wave 50 to 60 Hz, Gate Open)		VDRM		Volts	
	MAC15-4FP, MAC15A4FP MAC15-6FP, MAC15A6FP MAC15-8FP, MAC15A8FP MAC15-10FP, MAC15A10FP		200 400 600 800		
On-State RMS Current (T _C = +80°C) ⁽²⁾ Full Cycle Sine Wave 50 to 60 Hz (T _C = +95°C)		IT(RMS)	15 12	Amps	
Peak Nonrepetitive Surge Current (One Full Cycle, 60 Hz, T _C = +80°C) preceded and followed by rated current		ITSM	150	Amps	
Peak Gate Power (T _C = +80°C, Pulse Width = $2 \mu s$)		PGM	20	Watts	
Average Gate Power (T _C = +80°C, t = 8.3 ms)		PG(AV)	0.5	Watt	
Peak Gate Current		IGM	2	Amps	
Peak Gate Voltage		V _{GM}	10	Volts	
RMS Isolation Voltage (T _A = 25°C, Relative Humidity \leq 20%)		V _(ISO)	1500	Volts	
Operating Junction Temperature		Тј	-40 to +125	°C	
Storage Temperature Range		T _{stg}	-40 to +150	°C	

1. V_{DRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

2. The case temperature reference point for all T_C measurements is a point on the center lead of the package as close as possible to the plastic body.

MAC15FP Series MAC15AFP Series

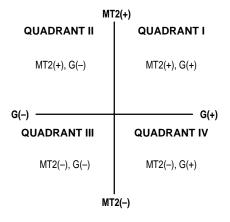
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction to Case	R _{θJC}	2	°C/W	
Thermal Resistance, Case to Sink	R _{0CS}	2.2 (typ)	°C/W	
Thermal Resistance, Junction to Ambient	R _{θJA}	60	°C/W	

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Blocking Current (Either Direction) $T_J = 25^{\circ}C$ ($V_D = Rated V_{DRM}$, $T_J = 125^{\circ}C$, Gate Open)	IDRM	_		10 2	μA mA
Peak On-State Voltage (Either Direction) (I _{TM} = 21 A Peak; Pulse Width = 1 to 2 ms, Duty Cycle ≤ 2%)	VTM	—	1.3	1.6	Volts
Gate Trigger Current (Continuous dc) (Main Terminal Voltage = 12 Vdc, $R_L = 100$ Ohms) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+) "A" SUFFIX ONLY	lgt	 		50 50 50 75	mA
Gate Trigger Voltage (Continuous dc) (Main Terminal Voltage = 12 Vdc, R_L = 100 Ohms) MT2(+), G(+) MT2(-), G(-) MT2(-), G(-) MT2(-), G(+) "A" SUFFIX ONLY (Main Terminal Voltage = Rated V _{DRM} , R_L = 10 kΩ, T_J = +110°C) MT2(+), G(+); MT2(-), G(-); MT2(+), G(-) MT2(-), G(+) "A" SUFFIX ONLY	VGT		0.9 0.9 1.1 1.4 —	2 2 2.5 —	Volts
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 200 mA)	Н	—	6	40	mA
Turn-On Time (V _D = Rated V _{DRM} , I _{TM} = 17 A, I _{GT} = 120 mA, Rise Time = 0.1 μ s, Pulse Width = 2 μ s)	tgt	—	1.5	—	μs
Critical Rate of Rise of Commutation Voltage (V_D = Rated V_{DRM} , I_{TM} = 21 A, Commutating di/dt = 7.6 A/ms, Gate Unenergized, T_C = 80°C)	dv/dt(c)	—	5	_	V/µs

QUADRANT DEFINITIONS



Trigger devices are recommended for gating on Triacs. They provide:

1. Consistent predictable turn-on points.

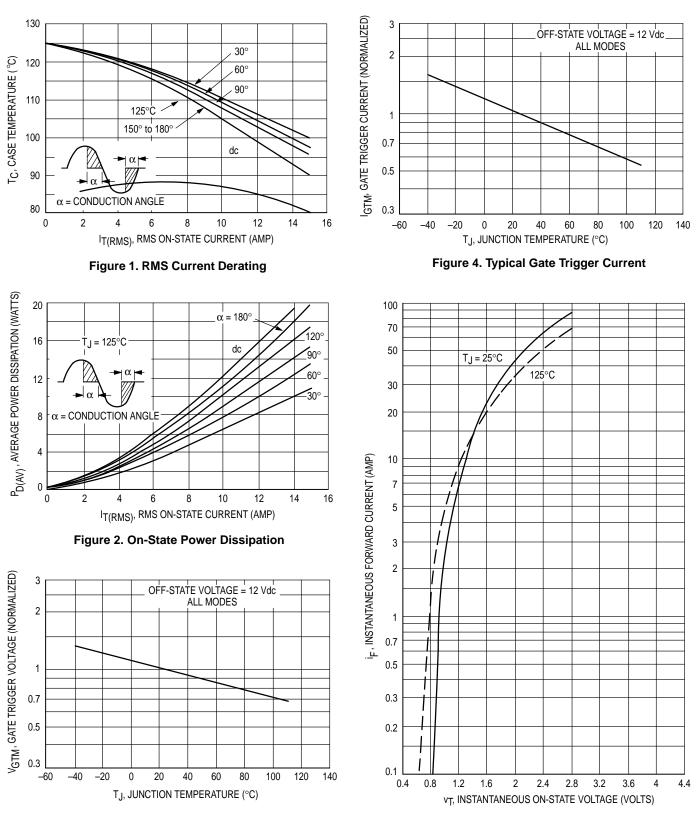
2. Simplified circuitry.

3. Fast turn-on time for cooler, more efficient and reliable operation.

ELECTRICAL CHARACTERISTICS of RECOMMENDED BIDIRECTIONAL SWITCHES

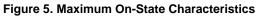
Usage	General		
Part Number	MBS4991	MBS4992	
٧ _S	6–10 V	7.5–9 V	
IS	350 µA Max	120 µA Max	
VS1-VS2	0.5 V Max	0.2 V Max	
Temperature Coefficient	0.02%/°C Typ		

1. Ratings apply for open gate conditions. Thyristor devices shall not be tested with a constant current source for blocking capability such that the voltage applied exceeds the rated blocking voltage.

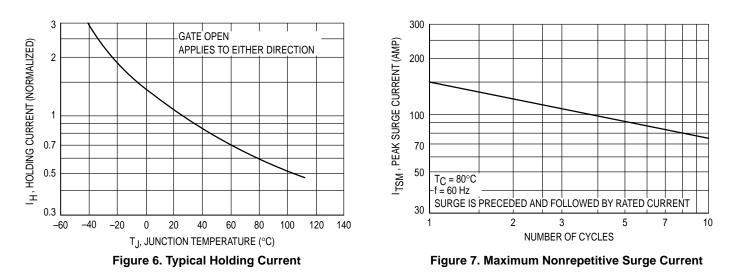


TYPICAL CHARACTERISTICS

Figure 3. Typical Gate Trigger Voltage



MAC15FP Series MAC15AFP Series



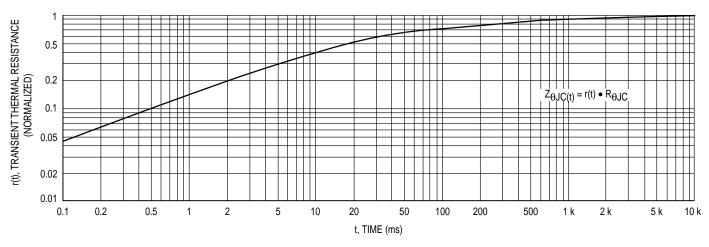


Figure 8. Thermal Response