

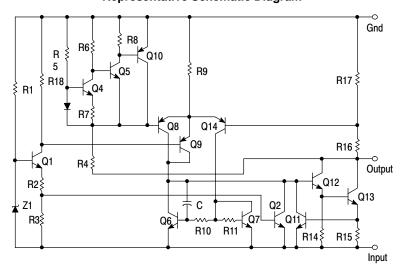
# **100 mA Negative Voltage Regulators**

The MC79L00, A Series negative voltage regulators are inexpensive, easy–to–use devices suitable for numerous applications requiring up to100 mA. Like the higher powered MC7900 Series negative regulators, this series features thermal shutdown and current limiting, making them remarkably rugged. In most applications, no external components are required for operation.

The MC79L00 devices are useful for on-card regulation or any other application where a regulated negative voltage at a modest current level is needed. These regulators offer substantial advantage over the common resistor/zener diode approach.

- No External Components Required
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- Low Cost
- Complementary Positive Regulators Offered (MC78L00 Series)
- Available in Either  $\pm 5\%$  (AC) or  $\pm 10\%$  (C) Selections

### **Representative Schematic Diagram**



\* Automotive temperature range selections are available with special test conditions and additional tests in 5, 12 and 15 V devices. Contact your local ON Semiconductor sales office for information.

# MC79L00, MC79L00A Series

# THREE-TERMINAL LOW CURRENT NEGATIVE FIXED VOLTAGE REGULATORS

SEMICONDUCTOR TECHNICAL DATA

P SUFFIX
PLASTIC PACKAGE
CASE 29









**D SUFFIX**PLASTIC PACKAGE
CASE 751
(SOP-8)\*

Pin 1. V<sub>out</sub> 5. GND 2. V<sub>in</sub> 6. V<sub>in</sub> 3. V<sub>in</sub> 7. V<sub>in</sub> 4. NC 8. NC

\*SOP-8 is an internally modified SO-8 package. Pins 2, 3, 6, and 7 are electrically common to the die attach flag. This internal lead frame modification decreases package thermal resistance and increases power dissipation capability when appropriately mounted on a printed circuit board. SOP-8 conforms to all external dimensions of the standard SO-8 package.

Device No. ±10%	Device No. 5%	Nominal Voltage
MC79L05C	MC79L05AC	-5.0
MC79L12C	MC79L12AC	-12
MC79L15C	MC79L15AC	-15
MC79L18C	MC79L18AC	-18
MC79L24C	MC79L24AC	-24

#### **ORDERING INFORMATION**

Device	Operating Temperature Range	Package
MC79LXXACD*		SOP-8
MC79LXXACP	$T_{J} = 0^{\circ} \text{ to } +125^{\circ}\text{C}$	Plastic Power
MC79LXXCP		Plastic Power
MC79LXXABD*	$T_{.1} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOP-8
MC79LXXABP*	1j = -40 t0 +125 C	Plastic Power

XX indicates nominal voltage

# **MAXIMUM RATINGS** ( $T_A = +25^{\circ}C$ , unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (-5 V)	VI	-30	Vdc
(-12, -15, -18 V)		-35	
(–24 V)		-40	
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Junction Temperature	TJ	+150	°C

**ELECTRICAL CHARACTERISTICS** (V<sub>I</sub> = -10 V, I<sub>O</sub> = 40 mA, C<sub>I</sub> = 0.33  $\mu$ F, C<sub>O</sub> = 0.1  $\mu$ F, -40°C < T<sub>J</sub> +125°C (for MC79LXXAB), 0°C < T<sub>J</sub> < +125°C (for MC79LXXAC)).

		MC79L05C, AB		MC79L05AC, AB				
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T <sub>J</sub> = +25°C)	Vo	-4.6	-5.0	-5.4	-4.8	-5.0	-5.2	Vdc
Input Regulation ( $T_J = +25^{\circ}C$ ) $-7.0 \text{ Vdc} \ge V_I \ge -20 \text{ Vdc}$ $-8.0 \text{ Vdc} \ge V_I \ge -20 \text{ Vdc}$	Reg <sub>line</sub>	_ _	_ _	200 150	-	_ _	150 100	mV
Load Regulation $T_J = +25^{\circ}\text{C}, \ 1.0 \ \text{mA} \le I_O \le 100 \ \text{mA}$ $1.0 \ \text{mA} \le I_O \le 40 \ \text{mA}$	Reg <sub>load</sub>	_ _	_ _	60 30	- -	_ _ _	60 30	mV
Output Voltage $ -7.0 \text{ Vdc} \ge V_l \ge -20 \text{ Vdc}, \ 1.0 \text{ mA} \le I_O \le 40 \text{ mA} $ $V_l = -10 \text{ Vdc}, \ 1.0 \text{ mA} \le I_O \le 70 \text{ mA} $	Vo	-4.5 -4.5	_ _	-5.5 -5.5	-4.75 -4.75	_ _	-5.25 -5.25	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I <sub>IB</sub>	_ _	_ _	6.0 5.5	_ _	_ _	6.0 5.5	mA
Input Bias Current Change -8.0 $\forall$ dc $\geq$ $\forall$ $\mid$ $\geq$ -20 $\forall$ dc 1.0 $\forall$ mA $\leq$ I $\mid$ $\leq$ 40 $\forall$ mA	I <sub>IB</sub>	_ _	_ _	1.5 0.2	_ _	_ _	1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V <sub>n</sub>	_	40	_	_	40	_	μV
Ripple Rejection $(-8.0 \ge V_1 \ge -18 \text{ Vdc}, f = 120 \text{ Hz}, T_J = +25^{\circ}\text{C})$	RR	40	49	-	41	49	_	dB
Dropout Voltage ( $I_O = 40 \text{ mA}, T_J = +25^{\circ}\text{C}$ )	V <sub>I</sub> -V <sub>O</sub>	-	1.7	_	_	1.7	-	Vdc

**ELECTRICAL CHARACTERISTICS** (V<sub>I</sub> = -19 V, I<sub>O</sub> = 40 mA, C<sub>I</sub> = 0.33  $\mu$ F, C<sub>O</sub> = 0.1  $\mu$ F, -40°C < T<sub>J</sub> +125°C (for MC79LXXAC), 0°C < T<sub>J</sub> < +125°C (for MC79LXXAB)).

		MC79L12C, AB		MC79L12AC, AB				
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T <sub>J</sub> = +25°C)	Vo	-11.1	-12	-12.9	-11.5	-12	-12.5	Vdc
Input Regulation (T <sub>.I</sub> = +25°C)	Reg <sub>line</sub>							mV
$-14.5 \text{ Vdc} \ge V_1 \ge -27 \text{ Vdc}$ $-16 \text{ Vdc} \ge V_1 \ge -27 \text{ Vdc}$			_	250 200	_ _	_	250 200	
Load Regulation $T_J = +25^{\circ}C$ , 1.0 mA $\leq I_O \leq$ 100 mA 1.0 mA $\leq I_O \leq$ 40 mA	Reg <sub>load</sub>		_ _	100 50	_ _ _		100 50	mV
Output Voltage -14.5 Vdc $\geq$ V <sub>I</sub> $\geq$ -27 Vdc, 1.0 mA $\leq$ I <sub>O</sub> $\leq$ 40 mA V <sub>I</sub> = -19 Vdc, 1.0 mA $\leq$ I <sub>O</sub> $\leq$ 70 mA	Vo	-10.8 -10.8	_ _	-13.2 -13.2	-11.4 -11.4	_ _	-12.6 -12.6	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I <sub>IB</sub>	_ _	_ _	6.5 6.0	_ _	_ _	6.5 6.0	mA
Input Bias Current Change $-16 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -27 \text{ Vdc}$ $1.0 \text{ mA} \le \text{I}_{\text{O}} \le 40 \text{ mA}$	I <sub>IB</sub>	_ _	_ _	1.5 0.2	_ _	_ _	1.5 0.2	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V <sub>n</sub>	-	80	_	-	80	_	μV
Ripple Rejection (-15 $\leq$ V <sub>I</sub> $\leq$ -25 Vdc, f = 120 Hz, T <sub>J</sub> = +25°C)	RR	36	42	_	37	42	_	dB
Dropout Voltage ( $I_O = 40 \text{ mA}, T_J = +25^{\circ}\text{C}$ )	V <sub>I</sub> -V <sub>O</sub>	-	1.7	-	_	1.7	-	Vdc

**ELECTRICAL CHARACTERISTICS** (V<sub>I</sub> = -23 V, I<sub>O</sub> = 40 mA, C<sub>I</sub> = 0.33  $\mu$ F, C<sub>O</sub> = 0.1  $\mu$ F, -40°C < T<sub>J</sub> +125°C (for MC79LXXAB), 0°C < T<sub>J</sub> < +125°C (for MC79LXXAC)).

		MC79L15C			MC79L15AC, AB			
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T <sub>J</sub> = +25°C)	Vo	-13.8	-15	-16.2	-14.4	-15	-15.6	Vdc
Input Regulation (T <sub>J</sub> = +25°C)	Reg <sub>line</sub>							mV
$-17.5 \text{ Vdc} \ge V_1 \ge -30 \text{ Vdc}$ $-20 \text{ Vdc} \ge V_1 \ge -30 \text{ Vdc}$		_ _	_ _	300 250	_ _	_ _	300 250	
Load Regulation $T_J = +25^{\circ}\text{C}, \ 1.0 \ \text{mA} \le I_O \le 100 \ \text{mA} \\ 1.0 \ \text{mA} \le I_O \le 40 \ \text{mA}$	Reg <sub>load</sub>		_ _	150 75			150 75	mV
Output Voltage -17.5 Vdc $\geq$ V <sub>I</sub> $\geq$ -Vdc, 1.0 mA $\leq$ I <sub>O</sub> $\leq$ 40 mA V <sub>I</sub> = -23 Vdc, 1.0 mA $\leq$ I <sub>O</sub> $\leq$ 70 mA	Vo	-13.5 -13.5	_ _	-16.5 -16.5	-14.25 -14.25	_ _	-15.75 -15.75	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I <sub>IB</sub>		_ _	6.5 6.0	_ _		6.5 6.0	mA
Input Bias Current Change $-20 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -30 \text{ Vdc}$ $1.0 \text{ mA} \le \text{I}_{\text{O}} \le 40 \text{ mA}$	$\Delta I_{IB}$			1.5 0.2	_ _	-	1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V <sub>N</sub>	_	90	-	_	90	-	μV
Ripple Rejection $(-18.5 \le V_1 \le -28.5 \text{ Vdc}, f = 120 \text{ Hz})$	RR	33	39	-	34	39	-	dB
Dropout Voltage $I_O = 40 \text{ mA}, T_J = +25^{\circ}\text{C}$	V <sub>I</sub> –V <sub>O</sub>	_	1.7	_	_	1.7	-	Vdc

# **ELECTRICAL CHARACTERISTICS** (V<sub>I</sub> = -27 V, I<sub>O</sub> = 40 mA, C<sub>I</sub> = 0.33 $\mu$ F, C<sub>O</sub> = 0.1 $\mu$ F, $0^{\circ}$ C < T<sub>J</sub> > $+125^{\circ}$ C, unless otherwise noted).

		MC79L18C			MC79L18AC			
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T <sub>J</sub> = +25°C)	Vo	-16.6	-18	-19.4	-17.3	-18	-18.7	Vdc
Input Regulation (T <sub>1</sub> = +25°C)	Reg <sub>line</sub>							mV
$-20.7 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}$		_	_	_	_	_	325	
$-21.4 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}$		_	_	325	_	_	_	
$-22 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -33 \text{ Vdc}$		-	_	275	_	-	_	
-21 Vdc ≥ V <sub>I</sub> ≥ -33 Vdc		-	_	_	_	_	275	
Load Regulation	Reg <sub>load</sub>							mV
$T_J = +25$ °C, 1.0 mA $\leq I_O \leq 100$ mA	0.044	_	_	170	_	_	170	
$1.0 \text{ mA} \le I_{O} \le 40 \text{ mA}$		-	_	85	_	_	85	
Output Voltage	Vo							Vdc
$-20.7 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 40 \text{ mA}$		_	_	_	-17.1	_	-18.9	
$-21.4 \text{ Vdc} \ge V_1 \ge -33 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 40 \text{ mA}$		-16.2	_	-19.8	_	_	_	
$V_1 = -27 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 70 \text{ mA}$		-16.2	_	-19.8	-17.1	_	-18.9	
Input Bias Current	I <sub>IB</sub>							mA
$(T_{.1} = +25^{\circ}C)$	.5	_	_	6.5	_	_	6.5	
$(T_J = +125^{\circ}C)$		_	_	6.0	_	_	6.0	
Input Bias Current Change	I <sub>IB</sub>							mA
$-21 \text{ Vdc} \ge \text{V}_1 \ge -33 \text{ Vdc}$	16	_	_	_	_	_	1.5	
-27 Vdc ≥ V <sub>1</sub> ≥ -33 Vdc		_	_	1.5	_	_	_	
1.0 mA $\leq I_{O} \leq 40$ mA		_	_	0.2	_	_	0.1	
Output Noise Voltage	V <sub>n</sub>	_	150	_	_	150	_	μV
$(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	"							
Ripple Rejection	RR	32	46	_	33	48	_	dB
$(-23 \le V_I \le -33 \text{ Vdc}, f = 120 \text{ Hz}, T_J = +25^{\circ}\text{C})$								
Dropout Voltage	IV <sub>I</sub> –V <sub>OI</sub>	_	1.7	-	_	1.7	_	Vdc
$I_{O} = 40 \text{ mA}, T_{J} = +25^{\circ}\text{C}$								

 $\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{I} = -33 \ V, \ I_{O} = 40 \ \text{mA}, \ C_{I} = 0.33 \ \mu\text{F}, \ C_{O} = 0.1 \ \mu\text{F}, \ 0^{\circ}\text{C} < T_{J} < +125^{\circ}\text{C}, \ unless \ otherwise \ noted).$ 

		MC79L24C			MC79L24AC			
Characteristics	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T <sub>J</sub> = +25°C)	Vo	-22.1	-24	-25.9	-23	-24	-25	Vdc
Input Regulation (T <sub>.1</sub> = +25°C)	Reg <sub>line</sub>							mV
–27 Vdc ≥ V <sub>I</sub> ≥ –38 Vdc		_	_	_	_	_	350	
–27.5 Vdc ≥ V <sub>I</sub> ≥ –38 Vdc		_	_	350	_	_	-	
$-28 \text{ Vdc} \ge V_1 \ge -38 \text{ Vdc}$		_	_	300	_	_	300	
Load Regulation $T_J = +25$ °C, 1.0 mA $\leq I_O \leq 100$ mA	Reg <sub>load</sub>	_		200	_		200	mV
$1.0 \text{ mA} \le I_0 \le 40 \text{ mA}$		_	_	100	_	_	100	
Output Voltage -27 Vdc $\geq$ V <sub>I</sub> $\geq$ -38 V, 1.0 mA $\leq$ I <sub>O</sub> $\leq$ 40 mA	Vo	_	_	_	-22.8	_	-25.2	Vdc
$-28 \text{ Vdc} \ge V_1 \ge -38 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 40 \text{ mA}$		-21.4	_	-26.4	_	_	_	
$V_1 = -33 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 70 \text{ mA}$		-21.4	_	-26.4	-22.8	_	-25.2	
Input Bias Current	I <sub>IB</sub>							mA
$(T_J = +25^{\circ}C)$		_	_	6.5	_	_	6.5	
$(T_J = +125^{\circ}C)$		_	_	6.0	_	_	6.0	
Input Bias Current Change	$\Delta l_{IB}$							mA
$-28 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -38 \text{ Vdc}$		_	_	1.5	_	_	1.5	
$1.0 \text{ mA} \le I_0 \le 40 \text{ mA}$		-	-	0.2	_	-	0.1	
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V <sub>n</sub>	_	200	_	_	200	_	μV
Ripple Rejection (-29 $\leq$ V <sub>I</sub> $\leq$ -35 Vdc, f = 120 Hz, T <sub>J</sub> = +25°C)	RR	30	43	-	31	47	-	dB
Dropout Voltage I <sub>O</sub> = 40 mA, T <sub>J</sub> = +25°C	V <sub>I</sub> -V <sub>O</sub>	_	1.7	_	_	1.7	_	Vdc

## **APPLICATIONS INFORMATION**

# **Design Considerations**

The MC79L00, A Series of fixed voltage regulators are designed with Thermal Overload Protections that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire length, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good

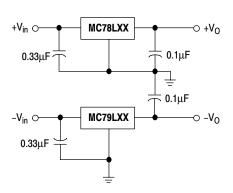
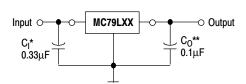


Figure 1. Positive and Negative Regulator

high–frequency characteristics to insure stable operation under all load conditions. A  $0.33~\mu F$  or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulator's input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the ripple voltage.

Figure 2. Standard Application

<sup>\*</sup> C<sub>I</sub> is required if regulator is located an appreciable distance from the power supply filter

<sup>\*\*</sup> Co improves stability and transient response.

# TYPICAL CHARACTERISTICS

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$ 

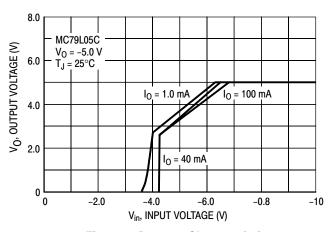


Figure 3. Dropout Characteristics

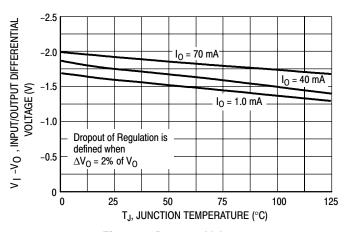


Figure 4. Dropout Voltage versus Junction Temperature

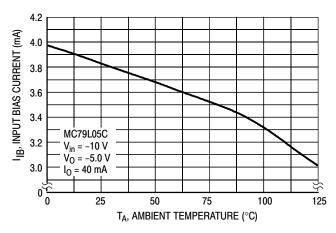


Figure 5. Input Bias Current versus Ambient Temperature

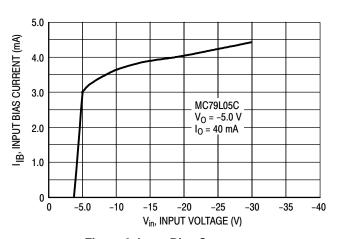


Figure 6. Input Bias Current versus Input Voltage

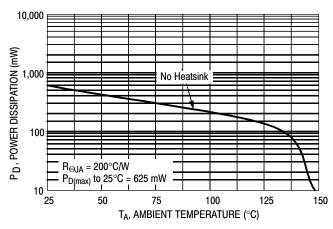


Figure 7. Maximum Average Power Dissipation versus Ambient Temperature (TO-92)

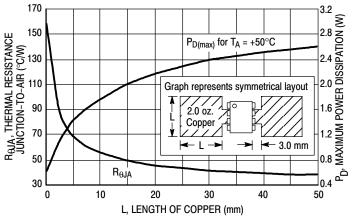
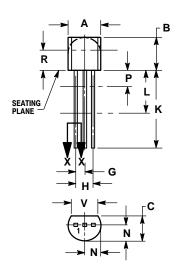


Figure 8. SOP-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

# **PACKAGE DIMENSIONS**

# **P SUFFIX**

PLASTIC PACKAGE CASE 29-11 **ISSUE AL** 





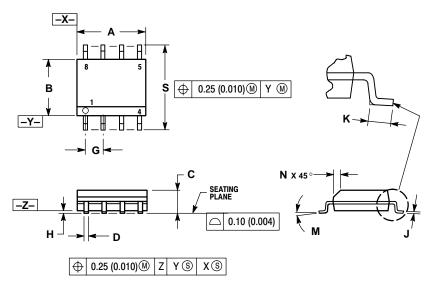
- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
  4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

# **PACKAGE DIMENSIONS**

### **D SUFFIX**

PLASTIC PACKAGE CASE 751-07 **ISSUE W** 



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.
  3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
  4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
  5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	4.80	5.00	0.189	0.197	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.053	0.069	
D	0.33	0.51	0.013	0.020	
G	1.2	7 BSC	0.05	50 BSC	
Н	0.10	0.25	0.004	0.010	
J	0.19	0.25	0.007	0.010	
K	0.40	1.27	0.016	0.050	
M	0 °	8 °	0 °	8 °	
N	0.25	0.50	0.010	0.020	
S	5.80	6.20	0.228	0.244	

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