

## PSM DC-to-DC Controller

### General Description

The RT9568 is a DC-to-DC controller IC for step-down (Buck), step-up (Boost), inverting (Buck-Boost), and Flyback converters.

The device consists of an internal temperature compensated reference, comparator, controlled duty cycle oscillator with an active current limit circuit, driver, and high current output switch.

### Ordering Information

RT9568□□	
	Package type
N	: DIP-8
S	: SOP-8
	Operating temperature range
C	: Commercial standard

### Marking Information

Part Number	Marking
RT9568CN	RT9568CN
RT9568CS	RT9568CS

### Features

- Pulse-Skipping Modulation
- Precision  $\pm 2\%$  Reference
- Operation from 3.0V to 30V Input
- Internal Switch Peak Current to 1.5A
- Design Flexibility
- Output Voltage Adjustable
- Current Limiting Adjustable
- Frequency Adjustable up to 200KHz

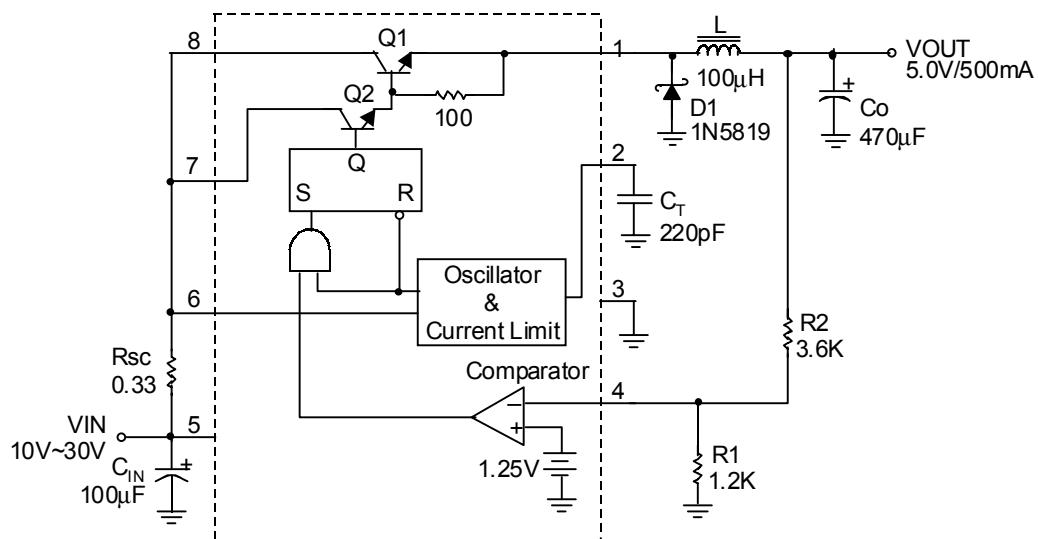
### Applications

- DC-DC Converter Module

### Pin Configurations

Part Number	Pin Configurations
RT9568CN (Plastic DIP-8)	TOP VIEW
RT9568CS (Plastic SOP-8)	

### Typical Application Circuit



Step-down Converter use Internal Switch

**Absolute Maximum Ratings**

• Power Supply Voltage	-----	30V
• Feedback Input Voltage Range	-----	0.3V to Vcc
• Switch Collector Voltage	-----	30V
• Switch Emitter Voltage( $V_{PIN8} = 30V$ )	-----	30V
• Switch Collector to Emitter Voltage	-----	30V
• Driver Collector Voltage	-----	30V
• Driver Collector Current (see Note 1)	-----	100mA
• Switch Current	-----	1.5A
• Power Dissipation, $P_D @ T_A = 25^\circ C$	DIP-8	1.25W
	SOP-8	0.625W
• Package Thermal Resistance	DIP-8, $\theta_{JA}$	100°C/W
	SOP-8, $\theta_{JA}$	160°C/W
• Operating Junction Temperature Range	-----	-40 ~ 125°C
• Storage Temperature Range	-----	-65 ~ 150°C

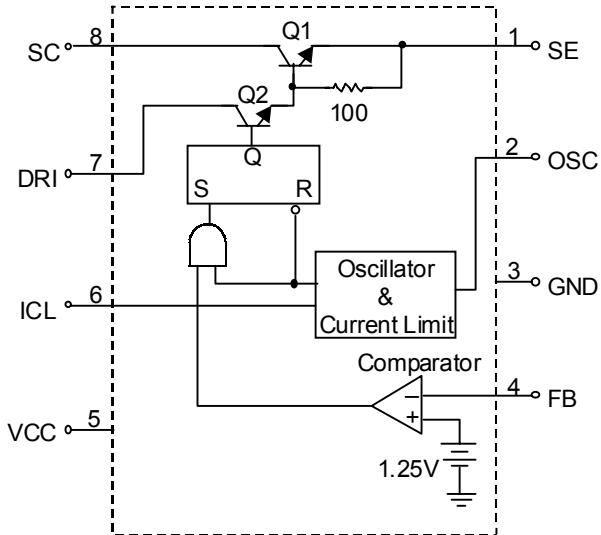
Note: 1. Maximum package power dissipation limits must be observed.

**Electrical Characteristics**

( $VCC = 5V$ ,  $T_A = 25^\circ C$ , unless otherwise specified)

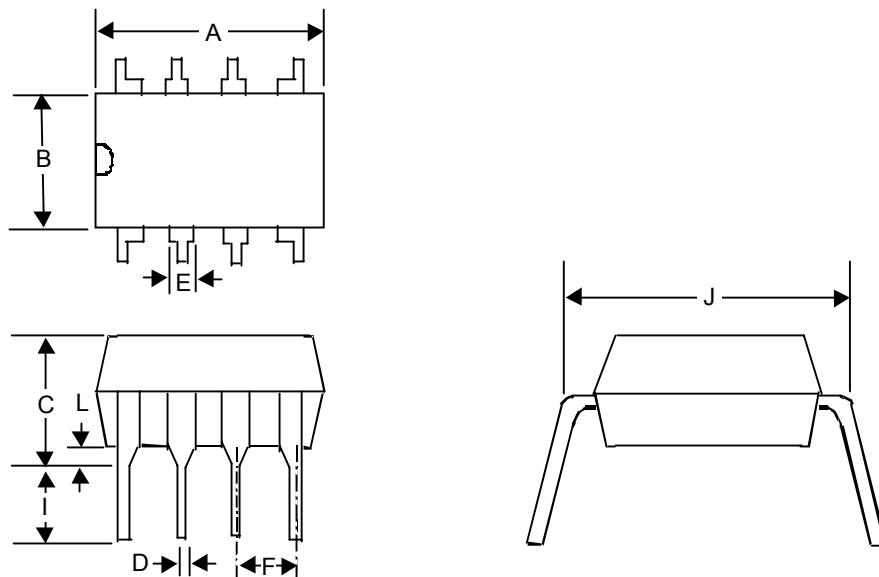
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Supply Current	$I_{CC}$	$VCC = 5.0V$ to $30V$ , $C_T = 1.0nF$ , Pin6 = VCC, $V_{pin4} > V_{ref}$ , Pin1 = GND, Remaining pins open	--	3.0	4.5	mA
Current Limit Sense Voltage	$V_{LIMIT}$	$I_{CHG} = I_{DISCHG}$	280	330	380	mV
Charge Current	$I_{CHG}$	$5.0V \leq VCC \leq 30V$	25	36	43	$\mu A$
Discharge Current	$I_{DISCHG}$	$5.0V \leq VCC \leq 30V$	160	250	290	$\mu A$
Discharge to Charge Current Ratio		Pin6 to VCC	5.5	6.9	7.9	--
Saturation Voltage, Darlington Connection		$I_{SW} = 1.0A$ , Pins 7,8 connected	--	1.0	1.3	V
Saturation Voltage, Darlington Connection		$I_{SW} = 1.0A$ , $R_{PIN7} = 82\Omega$ to VCC Forced $\beta \geq 20$	--	0.5	0.7	V
DC Current Gain		$I_{SW} = 1.0A$ , $V_{CE} = 5.0V$	50	75	--	--
Collector Off-state Current		$V_{CE} = 30V$	--	0.01	100	$\mu A$
Reference Voltage	$V_{REF}$		1.225	1.25	1.275	V
Reference Voltage Line Regulation		$3.0V \leq VCC \leq 30V$	--	1.4	5.0	mV
Input Bias Current	$I_{BIAS}$	$V_{IN} = 0V$	--	-20	-400	nA

## Function Block Diagram



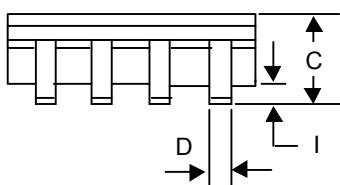
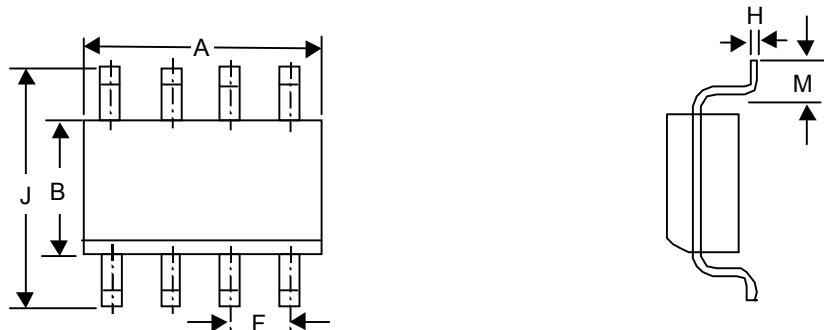
## Pin Description

Pin Name	Pin Function
SE	Darlington Switch Emitter
OSC	Oscillator Timing Capacitor
GND	Ground
FB	Feedback Input Pin
VCC	Power Supply Input
ICL	Current Limit Voltage Input
DRI	Driver Collector
SC	Collector of Darlington Transistor

**Package Information**

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.068	9.627	0.357	0.379
B	6.198	6.604	0.244	0.260
C	--	4.318	--	0.170
D	0.356	0.559	0.014	0.022
E	1.397	1.651	0.055	0.065
F	2.337	2.743	0.092	0.108
I	3.048	3.556	0.120	0.140
J	7.366	8.255	0.290	0.325
L	0.381	--	0.015	--

**8-Lead DIP Plastic Package**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.801	5.004	0.189	0.197
B	3.810	3.988	0.150	0.157
C	1.346	1.753	0.053	0.069
D	0.330	0.508	0.013	0.020
F	1.194	1.346	0.047	0.053
H	0.178	0.254	0.007	0.010
I	0.102	0.254	0.004	0.010
J	5.791	6.198	0.228	0.244
M	0.406	1.270	0.016	0.050

8-Lead SOP Plastic Package

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