

PNP Silicon Transistor

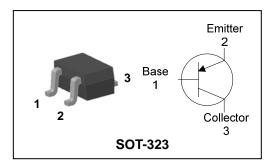
Descriptions

- General purpose application
- Switching application

Features

- Low Leakage current
- Low collector saturation voltage enabling low voltage operation
- Complementary pair with SBT2222AU

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SBT2907AU	<u>F2</u> □ ① ②	SOT-323

①Device Code ② Year&Week Code

Absolute maximum ratings

Ta=25°C

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	-60	V
Collector-Emitter voltage	$V_{\sf CEO}$	-60	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I _C	-600	mA
Collector dissipation	P _C *	350	mW
Junction temperature	Тј	150	°C
Storage temperature range	T_{stg}	-55~150	°C

^{* :} Package mounted on 99.5% alumina 10×8×0.6mm

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Electrical Characteristics

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	$I_C = -10 \mu A, I_E = 0$	-60	-	-	V
Collector-Emitter breakdown voltage	BV _{CEO}	I _C =-1mA, I _B =0	-60	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E = -10 \mu A, I_C = 0$	-5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} =-60V, I _E =0	-	-	-20	nA
DC current gain	h _{FE}	V _{CE} =-10V, I _C =-10mA	100	-	-	-
Collector-Emitter saturation voltage	V _{CE(sat)}	I _C =-150mA, I _B =-15mA	-	-	-0.4	V
Transition frequency	f _T	V_{CE} =-5.0V, I_{C} =-20mA, f =100MHz	200	-	-	MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10V$, $I_{E} = 0$, $f = 1MHz$	-	-	8	pF
Turn-on time	t _{on}		-	-	45	ns
Delay time	t _d	$V_{CC} = -30V_{dc}, I_{C} = -150mA_{dc},$ $I_{B1} = -15mA_{dc}$	-	-	10	ns
Rise time	t _r		-	-	40	ns
Turn-off time	t _{off}		-	-	100	ns
Storage time	t _s	$V_{CC} = -6.0V_{dc}, I_{C} = -150 \text{mA}_{dc}, I_{B1} = I_{B2} = -15 \text{mA}_{dc}$	-	-	80	ns
Fall time	t _f		-	-	30	ns

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Electrical Characteristic Curves

Fig. 1 P_C-T_a

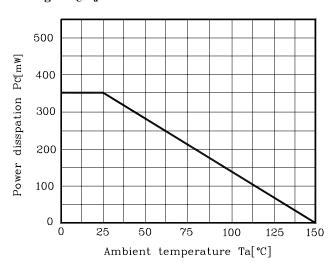


Fig. 2 $h_{FE}I_{C}$

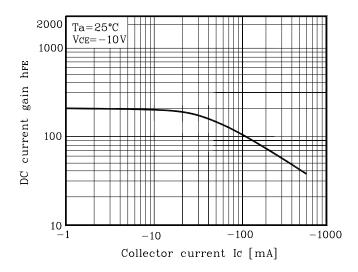


Fig. 3 $V_{\text{CE(sat)}}\text{-}I_{\text{C}}$

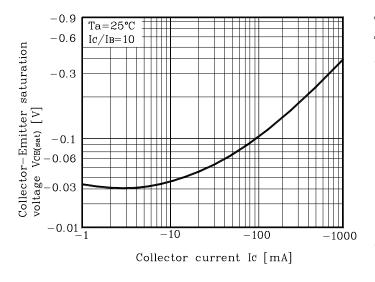
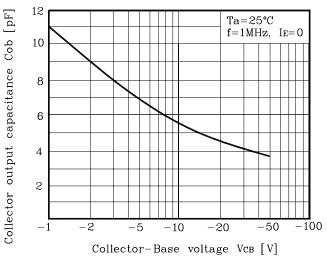


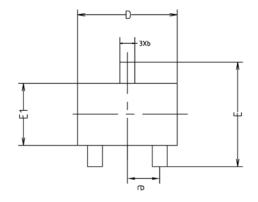
Fig. 4 C_{ob}-V_{CB}

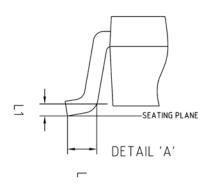


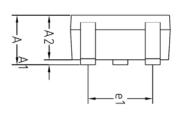
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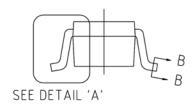
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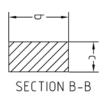
Outline Dimension





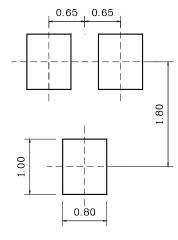






SYMBOL	MILLIMETERS			NOTE
3 THEOL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	0.90	-	1.25	
A1	0.00	-	0.10	
A2	0.85	0.90	0.95	
Ь	0.30	-	0.40	
С	0.10	-	0.25	
D	1.90	2.00	2.10	
Ε	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
е	0.65BSC			
e1	1.20	-	1.40	
L	0.10	-	-	
L1		0.12BS	C	

*Recommend PCB solder land [Unit: mm]



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