



Surface Mount Transient Voltage Suppressors

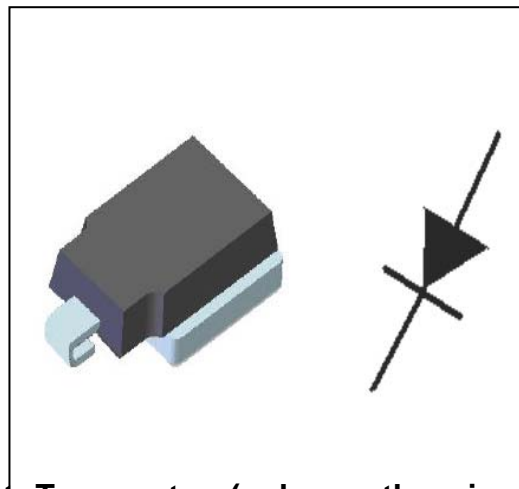
■ Features

- Surface mount package
- Excellent clamping capability
- Glass passivated junction
- Fast response time: Less than 1 ns for Uni-direction,
From 0 Volts to BV min
- Plastic material has UL flammability classification 94V-0
- RoHS compliant in lead-free versions

■ Mechanical Characteristics

- CASE: SOD-BLOCK Molded Plastic over glass passivated junction.
- Polarity: Heatsink is anode.

■ Outline Dimensions and Mark



■ Maximum Ratings And Characteristics @25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	Value	Units
Peak Pulse Power dissipation on 10/1000us Waveform (Note1, FIG.1)	PPPM	6600	W
Power Dissipation on Infinite Heatsink at Tc=25°C (FIG.2)	PD	8	W
Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load, (JEDEC Method) (Note2.3)	IFSM	700	Amps
Operating Junction Temperature Range	TJ	-55 to 150	°C
Storage Temperature Range	TSTG	-55 to 150	°C

Notes:

1. Non-repetitive current pulse, per Fig.1 and derated above TA=25°C per Fig.2.
2. Mounted on 5.0mm² (0.03mm thick) Copper Pads to each terminal.
3. 8.3ms single half sine-wave, or equivalent square wave, Duty cycle=4% plus per minute maximum.

■ Electrical Specification @ Tamb 25°C

Type Number	Reverse Stand-Off Voltage	Breakdown Voltage Min. @IT	Breakdown Voltage Max. @IT	Test Current	Maximum Clamping Voltage @IPP	Peak Pulse Current	Reverse Leakage @VRMW
(uni)	VRMW(V)	VBRMIN(V)	VBRMAX(V)	IT(mA)	VC(V)	IPP(A)	IR(uA)
SM8S10	10	11.1	14.1	5.0	18.8	351	15.0
SM8S10A	10	11.1	12.8	5.0	17.0	388	15.0
SM8S11	11	12.2	15.4	5.0	20.1	328	10.0
SM8S11A	11	12.2	14.0	5.0	20.1	363	10.0



SM8S10 THRU SM8S43A

■Electrical Characteristics

Type Number *	Reverse Stand-Off Voltage	Breakdown Voltage Min.@IT	Breakdown Voltage Max.@IT	Test Current	Maximum Clamping Voltage@IPP	Peak Pulse Curren	Reverse Leakage @VRMW
(uni)	VRMW(V)	VBRMIN(V)	VBRMAX(V)	IT(mA)	VC(V)	IPP(A)	IR(uA)
SM8S12	12	13.3	16.9	5.0	22.0	300	10.0
SM8S12A	12	13.3	15.3	5.0	19.9	332	10.0
SM8S13	13	14.4	18.2	5.0	23.8	277	10.0
SM8S13A	13	14.4	16.5	5.0	21.5	307	10.0
SM8S14	14	15.6	19.8	5.0	25.8	256	10.0
SM8S14A	14	15.6	17.9	5.0	23.2	284	10.0
SM8S15	15	16.7	21.1	5.0	26.9	245	10.0
SM8S15A	15	16.7	19.2	5.0	24.4	270	10.0
SM8S16	16	17.8	22.6	5.0	28.8	229	10.0
SM8S16A	16	17.8	20.5	5.0	26.0	254	10.0
SM8S17	17	18.9	23.9	5.0	30.5	216	10.0
SM8S17A	17	18.9	21.7	5.0	27.6	239	10.0
SM8S18	18	20.0	25.3	5.0	32.2	205	10.0
SM8S18A	18	20.0	23.3	5.0	29.2	226	10.0
SM8S20	20	22.2	28.1	5.0	35.8	184	10.0
SM8S20A	20	22.2	25.5	5.0	32.4	204	10.0
SM8S22	22	24.4	30.9	5.0	39.4	168	10.0
SM8S22A	22	24.4	28.0	5.0	35.5	186	10.0
SM8S24	24	26.7	33.8	5.0	43.0	153	10.0
SM8S24A	24	26.7	30.7	5.0	38.9	170	10.0
SM8S26	26	28.9	36.6	5.0	46.6	142	10.0
SM8S26A	26	28.9	33.2	5.0	42.1	157	10.0
SM8S28	28	31.1	39.4	5.0	50.0	132	10.0
SM8S28A	28	31.1	35.8	5.0	45.4	145	10.0
SM8S30	30	33.3	42.2	5.0	53.5	123	10.0
SM8S30A	30	33.3	38.3	5.0	48.4	136	10.0
SM8S33	33	36.7	46.5	5.0	59.0	112	10.0
SM8S33A	33	36.7	42.2	5.0	53.3	124	10.0
SM8S36	36	40.0	50.7	5.0	64.3	103	10.0
SM8S36A	36	40.0	46.0	5.0	58.1	114	10.0
SM8S40	40	44.4	56.3	5.0	71.4	92.4	10.0
SM8S40A	40	44.4	51.1	5.0	64.5	102	10.0
SM8S43	43	47.8	58.4	5.0	76.7	86	10.0
SM8S43A	43	47.8	52.8	5.0	69.4	95.1	10.0

Rating and Characteristic Curves TA=25°C unless otherwise noted

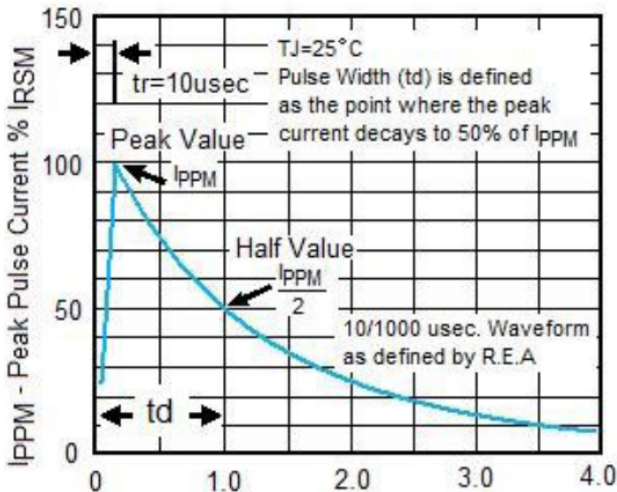


Fig.1 Pulse Waveform

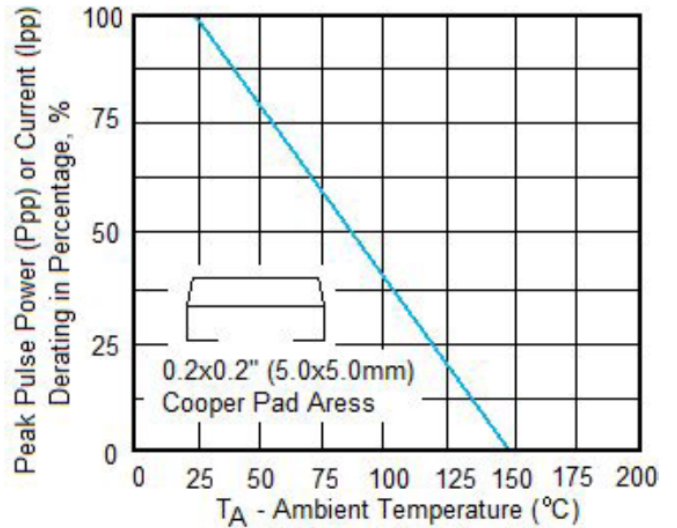


Fig.2 Pulse Derating Curve

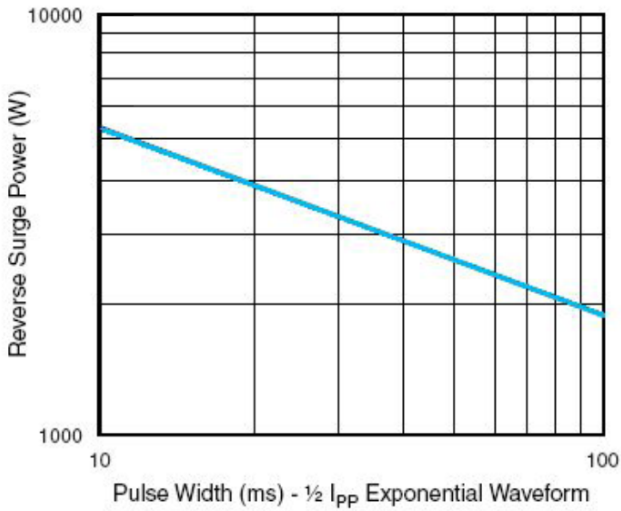
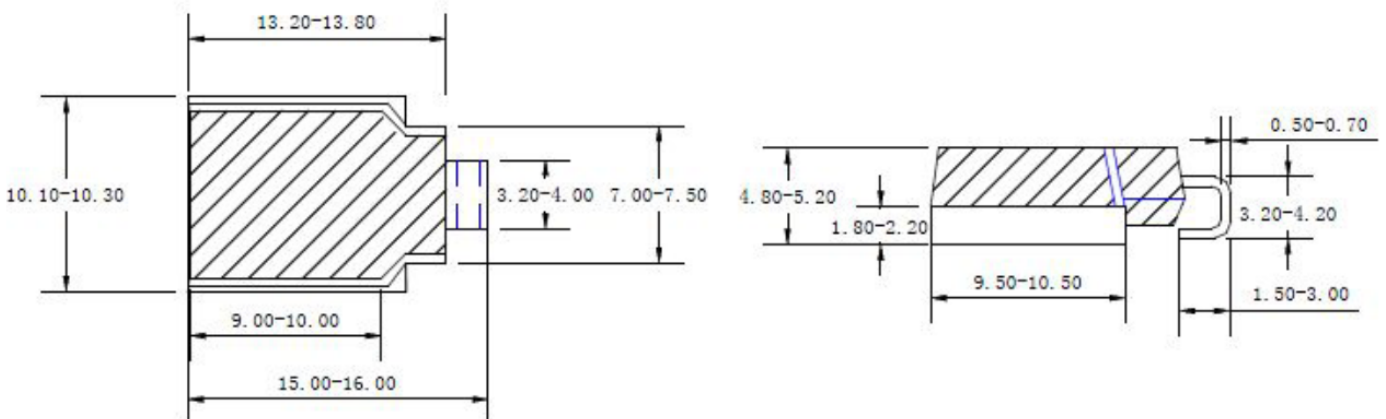


Figure 3. Reverse Power Capability

Package Outline Dimensions and Pad Lay outs

SMD-BLOCK (mm)





■ Package Outline Dimensions and Pad Lay outs

