

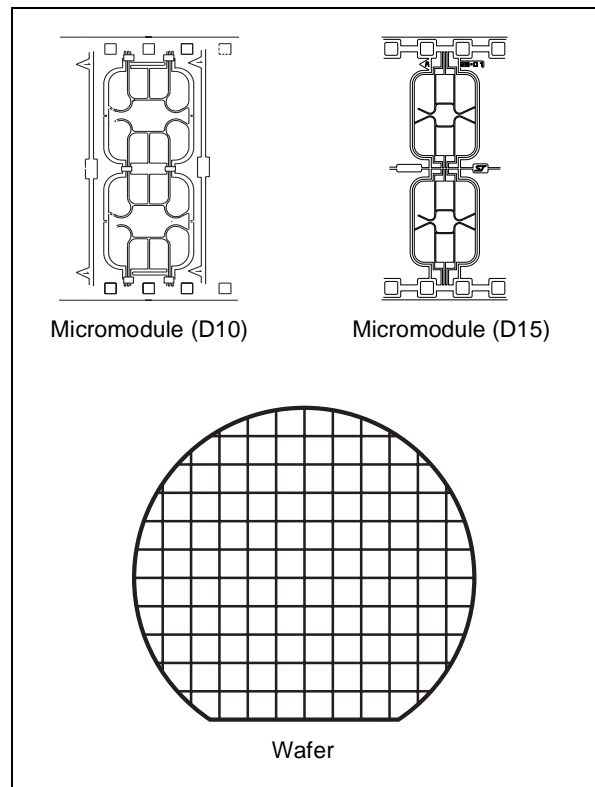


ST1305B

Memory Card IC 192 bit High Endurance EEPROM With Secure Logic Access Control

DATA BRIEFING

- Single Supply Voltage (5 V)
- Memory Divided Into:
 - 16 bits of Circuit Identification
 - 48 bits of Card Identification
 - 48 bits of Count Data
 - 16 bits of Certificate
 - 24 bits of Transport Code
 - 64 bits of Issuer Data
- Counting Capability up to 262,144
- Circuit Protected by Transport Code for Delivery from ST to the Customer
- 5 External Contacts Only (ISO 7816 Compatible)
- Answer to Reset (Fully Compatible with ISO 7816-3)
- E.S.D. Protection Greater Than 4000 V
- Power-On and Low V_{CC} Reset
- 1 Million Erase/Write Cycles (minimum)
- 10 Year Data Retention (minimum)
- 5 ms Programming Time (typical)



DESCRIPTION

The ST1305B is a 192-bit EEPROM device with associated security logic to control memory access. The circuit includes counting capabilities and thus is very well adapted to prepaid card applications.

The ST1305B is protected by hard-wired security logic and special fuses. The memory is arranged as a matrix of 24x8 cells, accessed in a serial bit-

Table 1. Signal Names

CLK	Clock
RST	Reset
I/O	Serial Data Input/Output
V _{CC}	Supply Voltage
GND	Ground

Figure 1. Logic Diagram

