2-Bit Bidirectional Voltage-Level Translator with Auto Direction Sensing and ±15kV ESD Protection

UM3302H CSP8 1.90×0.90
UM3302Q QFN8 1.40×1.20

General Description

The UM3302, an ESD protected level translator, provides the level shifting necessary to allow data transfer in multi-voltage system. This 2-channel non-inverting translator uses two separate configurable power-supply rails. The A port is designed to track V_{CCA}. V_{CCA} accepts any supply voltage from 1.2V to 3.6V. The B port is designed to track V_{CCB}. V_{CCB} accepts any supply voltage from 1.65V to 5.5V. This allows for universal low-voltage bidirectional translation between any of the 1.2V, 1.5V, 1.8V, 2.5V, 3.3V, and 5V voltage nodes. Both I/O ports are auto-sensing; thus, no direction pin is required, making it ideal for data transfer between low-voltage ASICs /PLDs and higher voltage systems.

The UM3302 operates at a guaranteed data rate of 20Mbps over the entire specified operating voltage range. Within specific voltage domains, higher data rates are up to 100Mbps.

When the output-enable (OE) input is low, all outputs are placed in the high-impedance state. The UM3302 is designed so that the OE input circuit is designed to track V_{CCA}.

To ensure the high-impedance state during power up or power down, OE should be tied to GND through a pull down resistor; the minimum value of the resistor is determined by the current-sourcing capability of the driver.

The UM3302H is a dual channel level translator available in 1.90mm×0.90mm CSP8 bump package while UM3302Q is available in 1.40mm×1.20mm QFN8 package.

Applications

- Low-Voltage ASIC Level Translation
- Cell-Phone Cradles
- Portable POS Systems
- Portable Communication Devices
- Low-Cost Serial Interfaces
- Cell-Phones
- GPS
- Telecommunications Equipment

Features

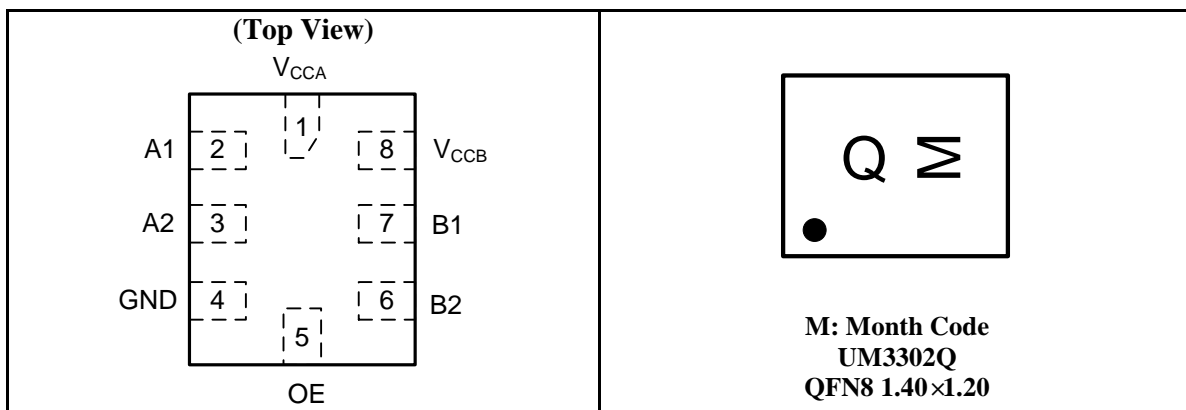- 1.2V to 3.6V on A Port and 1.65V to 5.5V on B Port ($V_{CCA} \leq V_{CCB}$)
- V_{CC} Isolation Feature – If Either V_{CC} Input is at GND, All Outputs are in the High-Impedance State
- OE Input Circuit Referenced to V_{CCA}
- Low Power Consumption
- Latch-Up Performance Exceeds 100mA
- ±15kV ESD Protection on B Ports

Pin Configurations

Top View

M: Month Code
UM3302H
CSP8 1.90×0.90

UM3302H/UM3302Q
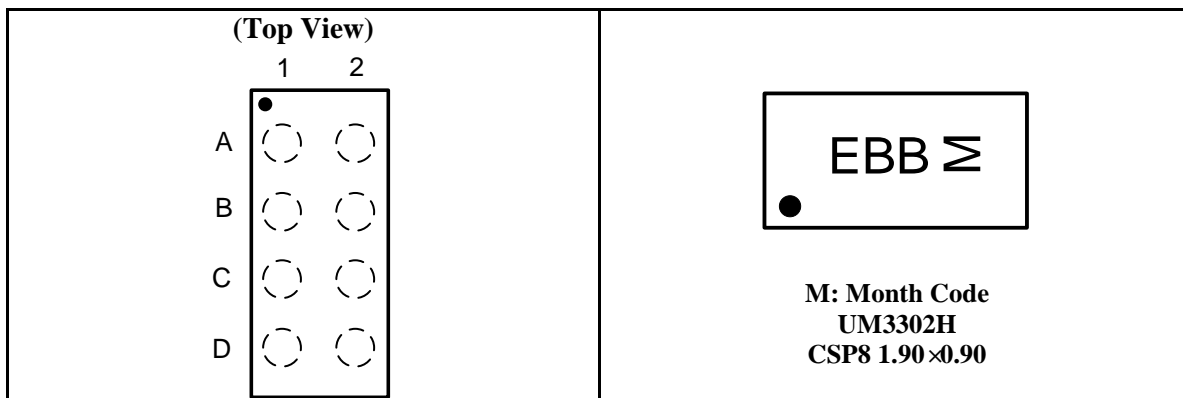
(Top View)

Ball Mapping for UM3302H

Transparent Top View

Pin Description

Pin No		Pin Name	Function
UM3302H	UM3302Q		
A1	6	B2	Input/Output B. Referenced to V_{CCB}
B1	4	GND	Ground
C1	1	V_{CCA}	A-Port supply voltage. $1.2V{\leq}V_{CCA}{\leq}3.6V$ and $V_{CCA}{\leq}V_{CCB}$
D1	3	A2	Input/Output A. Referenced to V_{CCA}
D2	2	A1	Input/Output A. Referenced to V_{CCA}
C2	5	OE	3-state output enable. Pull OE low to place all outputs in 3-state mode. Referenced to V_{CCA}
B2	8	V_{CCB}	B-Port supply voltage. $1.65V{\leq}V_{CCB}{\leq}5.5V$
A2	7	B1	Input/Output B. Referenced to V_{CCB}

Ordering Information

Part Number	Packaging Type	Marking Code	Shipping Qty
UM3302H	CSP8 1.90×0.90	EBB	3000pcs/7Inch
UM3302Q	QFN8 1.40×1.20	Q	Tape & Reel

Union SEMICONDUCTOR

Absolute Maximum Ratings (Note 1)

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{CCA}	Supply Voltage Range		-0.5 to +4.5	V
V_{CCB}	Supply Voltage Range		-0.5 to +6.5	V
V_I	Input Voltage Range (Note 2)	A port	-0.5 to +4.5	V
		B port	-0.5 to +6.5	
V_O	Voltage Range applied to any output in the high-impedance or power-off state (Note 2)	A port	-0.5 to +4.5	V
		B port	-0.5 to +6.5	
V_O	Voltage Range applied to any output in the high or low state (Note 2,3)	A port	-0.5 to (V_{CCA}+0.5)	V
		B port	-0.5 to (V_{CCB}+0.5)	
I_{IK}	Input Clamp Current	$V_I < 0$	-50	mA
I_{OK}	Output Clamp Current	$V_O < 0$	-50	mA
I_O	Continuous Output Current		±50	mA
	Continuous Current through V_{CCA}, V_{CCB}, or GND		±100	mA
T_{OP}	Operating Temperature Range		-40 to +85	℃
T_{STG}	Storage Temperature Range		-65 to +150	℃

Note 1: Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Note 2: The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

Note 3: The value of V_{CCA} and V_{CCB} are provided in the recommended operating conditions table.

Recommended Operating Conditions (Note 1, 2)

Symbol	Parameter		V_{CCA}	V_{CCB}	Min	Max	Unit
V_{CCA}	Supply Voltage				1.2	3.6	V
V_{CCB}					1.65	5.5	V
V_{IH}	High Level Input Voltage	Data Inputs	1.2V to 3.6V	1.65V to 5.5V	$V_{CCI} \times 0.65^{(Note3)}$	V_{CCI}	V
		OE Input	1.2V to 3.6V	1.65V to 5.5V	$V_{CCA} \times 0.65$	5.5	V
V_{IL}	Low Level Input Voltage	Data Inputs	1.2V to 3.6V	1.65V to 5.5V	0	$V_{CCI} \times 0.35$	V
		OE Input	1.2V to 3.6V	1.65V to 5.5V	0	$V_{CCA} \times 0.35$	V
V_O	Voltage range applied to any output in the high-impedance or power off state	A Port	1.2V to 3.6V	1.65V to 5.5V	0	3.6	V
		B Port			0	5.5	
$\Delta t/\Delta v$	Input Transition Rise or Fall Time	A-Port Inputs	1.2V to 3.6V	1.65V to 5.5V		40	ns/V
		B-Port Inputs	1.2V to 3.6V	1.65V to 1.95V		40	
				4.5V to 5.5V		30	

Note1. The A and B sides of an unused data I/O pair must be held in the same state, i.e., both at V_{CCI} or both at GND.

Note2. V_{CCA} must be less than or equal to V_{CCB} and must not exceed 3.6 V.

Note3. V_{CCI} is the supply voltage associated with the input port.

Electrical Characteristics (Note 1, 2)

Over recommended operating free-air temperature range (unless otherwise noted)

Parameter		Test Conditions	V_{CCA}	V_{CCB}	T_A=25℃ Min	Max	-40℃ to 85℃ Min	Max	Unit
V_{OHA}		I_{OH}=-20µA	1.2V		1.1				V
			1.4V to 3.6V				V_{CCA}-0.4		
V_{OLA}		I_{OL}=20µA	1.2V		0.9				V
			1.4V to 3.6V					0.4	
V_{OHB}		I_{OH}=-20µA		1.65V to 5.5V			V_{CCB}-0.4		V
V_{OLB}		I_{OL}=20µA		1.65V to 5.5V				0.4	V
I_I	OE	V_I=V_{CCI} or GND	1.2V to 3.6V	1.65V to 5.5V		±1		±2	µA
I_{off}	A Port	V_I or V_O=0 to 3.6V	0V	0V to 5.5V		±1		±2	µA
	B Port	V_I or V_O=0 to 5.5V	0V to 3.6V	0V		±1		±2	
I_{OZ}	A or B Port	OE=GND	1.2V to 3.6V	1.65V to 5.5V		±1		±2	µA
I_{CCA}		V_I=V_{CCI} or GND I_O=0	1.2V	1.65V to 5.5V	0.06				µA
			1.4V to 3.6V	1.65V to 5.5V				3	
			3.6V	0V				2	
			0V	5.5V				-2	
I_{CCB}		V_I=V_{CCI} or GND I_O=0	1.2V	1.65V to 5.5V	3.4				µA
			1.4V to 3.6V	1.65V to 5.5V				5	
			3.6V	0V				-2	
			0V	5.5V				2	
I_{CCA} +I_{CCB}		V_I=V_{CCI} or GND I_O=0	1.2V	1.65V to 5.5V	3.5				µA
			1.4V to 3.6V	1.65V to 5.5V				8	
I_{CCZA}		V_I=V_{CCI} or GND I_O=0, OE=GND	1.2V	1.65V to 5.5V	0.05				µA
			1.4V to 3.6V	1.65V to 5.5V				3	
I_{CCZB}		V_I=V_{CCI} or GND I_O=0, OE=GND	1.2V	1.65V to 5.5V	3.3				µA
			1.4V to 3.6V	1.65V to 5.5V				5	
C_i	OE		1.2V to 3.6V	1.65V to 5.5V	2.5			3	pF
C_{iO}	A Port		1.2V to 3.6V	1.65V to 5.5V	5			6	pF
	B Port				11			14	

Note1. V_{CCI} is the supply voltage associated with the input port.
Note2. V_{CCO} is the supply voltage associated with the output port.

Switching Characteristics

T_A=+25 ℃, V_{CCA}= 1.2V

Parameter	From (Input)	To (Output)	V_{CCB}=1.8V Typ	V_{CCB}=2.5V Typ	V_{CCB}=3.3V Typ	V_{CCB}=5V Typ	Unit
t_{pd}	A	B	7	6	5.3	5.5	ns
	B	A	7.5	6.5	6	6	
t_{en}	OE	A	1	1	1	1	μs
		B	1	1	1	1	
t_{dis}	OE	A	18	16	14	14	ns
		B	19	17	15	15	
t_{rA}, t_{fA}	A port rise and fall time		5	5	5	5	ns
t_{rB}, t_{fB}	B port rise and fall time		2.5	1.5	1.2	1	ns
$t_{SK(O)}$	Channel-to-channel		0.5	0.5	0.5	1.5	ns
Max data rate			20	20	20	20	Mbps

Switching Characteristics

Over recommended operating free-air temperature range, V_{CCA}=1.5V±0.1V (unless otherwise noted)

Parameter	From (Input)	To (Output)	V_{CCB}=1.8V ±0.15V Min	Max	V_{CCB}=2.5V ±0.2V Min	Max	V_{CCB}=3.3V ±0.3V Min	Max	V_{CCB}=5V ±0.5V Min	Max	Unit
t_{pd}	A	B	2	15	1.2	11	1.1	10	1	10	ns
	B	A	1	15	0.9	12	0.5	12	0.3	13	
t_{en}	OE	A		1		1		1		1	μs
		B		1		1		1		1	
t_{dis}	OE	A	6	30	5.5	28	5.5	25	5.5	23	ns
		B	5.5	30	5	25	5	20	5	20	
t_{rA}, t_{fA}	A port rise and fall time		1.5	5.0	1.5	5.0	1.5	5.0	1.5	5.0	ns
t_{rB}, t_{fB}	B port rise and fall time		0.9	4.5	0.6	3.5	0.5	3	0.4	2.5	ns
$t_{SK(O)}$	Channel-to-channel			0.5		0.5		0.5		0.5	ns
Max data rate			40		40		40		40		Mbps

Switching Characteristics

Over recommended operating free-air temperature range, V_{CCA}=1.8V±0.15V (unless otherwise noted)

Parameter	From (Input)	To (Output)	V_{CCB}=1.8V ±0.15V		V_{CCB}=2.5V ±0.2V		V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
			Min	Max	Min	Max	Min	Max	Min	Max	
t_{pd}	A	B	1.6	10	1.4	9	1.3	7	1.2	6.5	ns
	B	A	1.5	12	1.3	8.5	1	8	0.9	8	
t_{en}	OE	A		1		1		1		1	µs
	OE	B		1		1		1		1	
t_{dis}	OE	A	6	34	5.5	23	5	20	5	17.6	ns
	OE	B	5.5	33	4.5	22	4.2	16.8	4.4	16.3	
t_{rA}, t_{fA}	A port rise and fall time		1	4.2	1.1	4.0	1.1	4.0	1.1	4.0	ns
t_{rB}, t_{fB}	B port rise and fall time		0.9	4	0.6	3.2	0.5	2.8	0.4	2.8	ns
$t_{SK(O)}$	Channel-to-channel			0.5		0.5		0.5		0.5	ns
Max data rate			60		60		60		60		Mbps

Switching Characteristics

Over recommended operating free-air temperature range, V_{CCA}=2.5V±0.2V (unless otherwise noted)

Parameter	From (Input)	To (Output)	V_{CCB}=2.5V ±0.2V		V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
			Min	Max	Min	Max	Min	Max	
t_{pd}	A	B	1.1	6.5	1	5.2	0.9	5	ns
	B	A	1.2	6.6	1.1	5.1	0.9	4.4	
t_{en}	OE	A		1		1		1	µs
	OE	B		1		1		1	
t_{dis}	OE	A	5.0	23	4.6	15.2	4.6	13	ns
	OE	B	4.4	22	3.8	16	3.9	13.3	
t_{rA}, t_{fA}	A port rise and fall time		0.8	3	0.8	3	0.8	3	ns
t_{rB}, t_{fB}	B port rise and fall time		0.7	2.7	0.5	2.8	0.4	2.7	ns
$t_{SK(O)}$	Channel-to-channel			0.5		0.5		0.5	ns
Max data rate			100		100		100		Mbps

Switching Characteristics

Over recommended operating free-air temperature range, V_{CCA}=3.3V$\pm$0.3V (unless otherwise noted)

Parameter	From (Input)	To (Output)	V_{CCB}=3.3V $\pm$0.3V Min	V_{CCB}=3.3V $\pm$0.3V Max	V_{CCB}=5V $\pm$0.5V Min	V_{CCB}=5V $\pm$0.5V Max	Unit
t_{pd}	A	B	0.9	5	0.8	4	ns
	B	A	1	5.1	0.9	4	
t_{en}	OE	A		1		1	μs
	OE	B		1		1	
t_{dis}	OE	A	5	15	4	13	ns
	OE	B	4	16	3.4	13.8	
t_{rA}, t_{fA}	A port rise and fall time		0.7	2.5	0.7	2.5	ns
t_{rB}, t_{fB}	B port rise and fall time		0.5	2.1	0.4	2.8	ns
$t_{SK(O)}$	Channel-to-channel			0.5		0.5	ns
Max data rate			100		100		Mbps

Operating Characteristics

T_A=+25 ℃

Pamameter		Test Conditions	V_{CCB} 5V	1.8V	1.8V	1.8V	2.5V	5V	3.3V to 5V	Unit
			V_{CCA} 1.2V	1.2V	1.5V	1.8V	2.5V	2.5V	3.3V	
			Typ	Typ	Typ	Typ	Typ	Typ	Typ	
C_{pdA}	A-port input B-port output	C_L=0, f=10MHz, t_r=t_f=1ns, OE= V_{CCA}	9	10	10	10	10	10	10	pF
	B-port input A-port output		12	11	11	11	11	11	11	
C_{pdB}	A-port input B-port output		35	28	28	29	3.0	30	30	
	B-port input A-port output		27	19	19	19	20	21	23	
C_{pdA}	A-port input B-port output	C_L=0, f=10MHz, t_r=t_f=1ns, OE= GND	0.01	0.01	0.01	0.01	0.01	0.01	0.01	pF
	B-port input A-port output		0.01	0.01	0.01	0.01	0.01	0.01	0.01	
C_{pdB}	A-port input B-port output		0.01	0.01	0.01	0.01	0.01	0.01	0.03	
	B-port input A-port output		0.01	0.01	0.01	0.01	0.01	0.01	0.05	

Applications Information

UM3302 ESD protected level translator provides level shifting necessary to allow data transfer in a multi-voltage system. Externally applied voltages, V_{CCA} and V_{CCB} set the logic levels on either side of device. A low voltage signal present on V_{CCA} side of the device appears as a high voltage logic signal on the V_{CCB} side of the device, and vice-versa.

Block Diagram

The UM3302 (block diagram see Figure 1) does not require a direction-control signal to control the direction of data flow from A to B or from B to A. In a dc state, the output drivers of the UM3302 can maintain a high or low, but are designed to be weak, so that they can be overdriven by an external driver when data on the bus starts flowing the opposite direction.

The output one shot detects rising or falling edges on the A or B ports. During a rising edge, the one shot turns on the PMOS transistors (T1, T3) for a short duration, which speeds up the low-to-high transition. Similarly, during a falling edge, the one shot turns on the NMOS transistors (T2, T4) for a short duration, which speeds up the high-to-low transition.
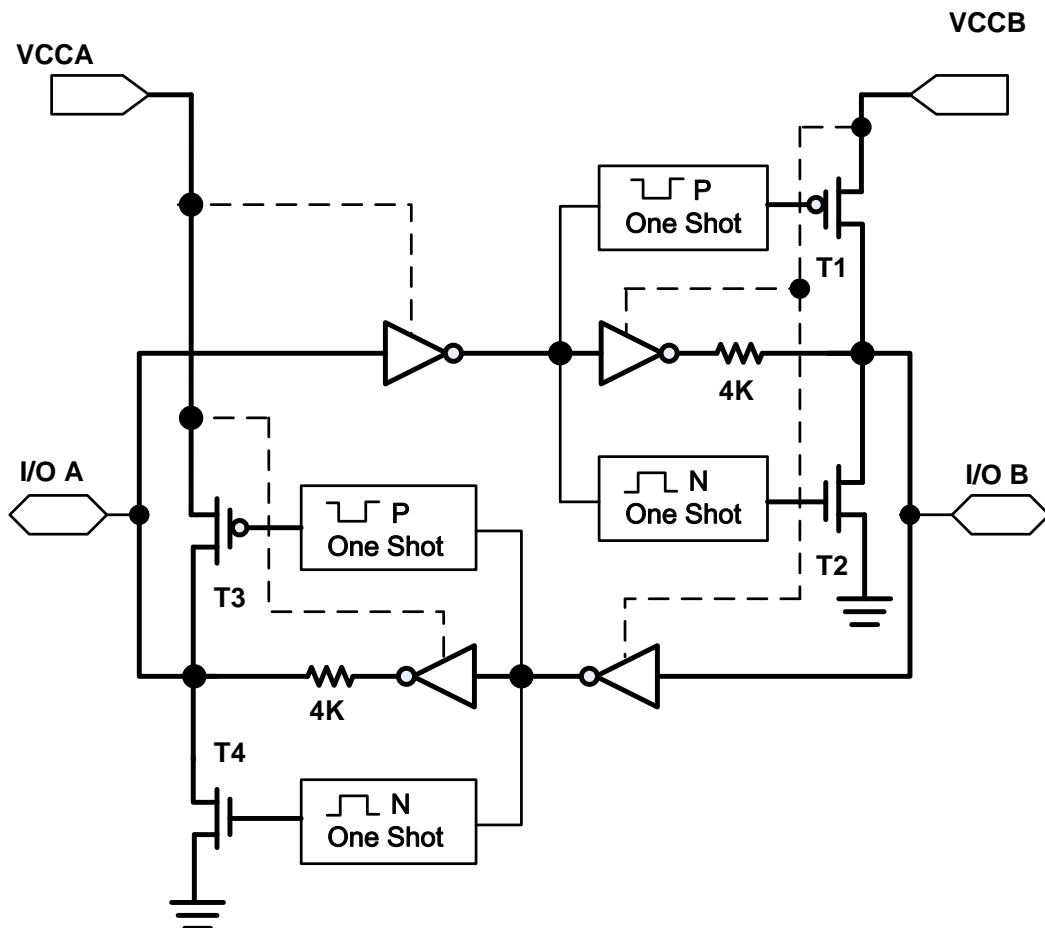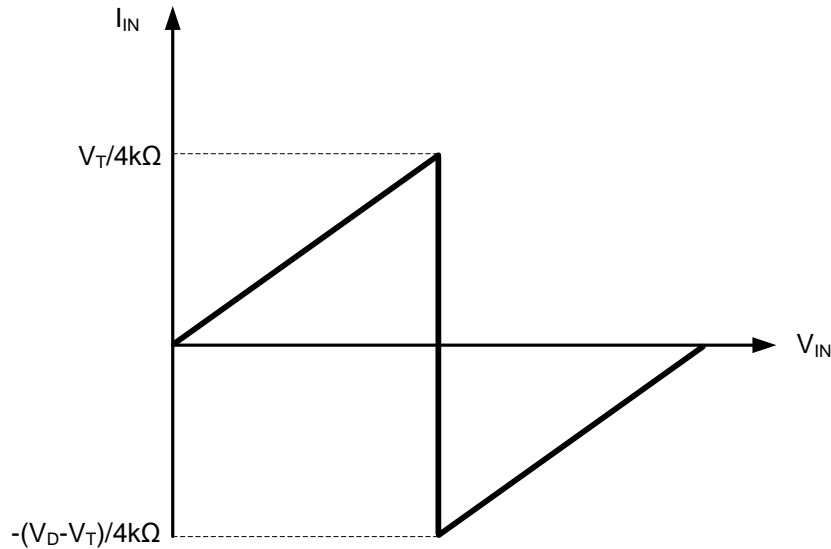

Figure 1 Block Diagram of UM3302 I/O Cell

Input Driver Requirements

Typical I_{IN} vs V_{IN} characteristics of the UM3302 are shown in Figure 2. For proper operation, the device driving the data I/Os of the UM3302 must have drive strength of at least ±2 mA.

A: V_T is the input threshold voltage of the UM3302 (typical $V_{CCI}/2$)
B: V_D is the supply voltage of the external driver.

Figure 2 Typical I_{IN} vs V_{IN} Curve

Power Up

During operation, ensure that $V_{CCA} \leq V_{CCB}$ at all times. During power-up sequencing, $V_{CCA} \geq V_{CCB}$ does not damage the device, so any power supply can be ramped up first. The UM3302 has circuitry that disables all output ports when either V_{CC} is switched off ($V_{CCA/B} = 0$ V).

Enable and Disable

The UM3302 has an OE input that is used to disable the device by setting OE = low, which places all I/Os in the high-impedance (Hi-Z) state. The disable time (tdis) indicates the delay between when OE goes low and when the outputs actually get disabled (Hi-Z). The enable time (ten) indicates the amount of time the user must allow for the one-shot circuitry to become operational after OE is taken high.

Pullup or Pulldown Resistors on I/O Lines

The UM3302 is designed to drive capacitive loads of up to 50pF. The output drivers of the UM3302H have low dc drive strength. If pullup or pulldown resistors are connected externally to the data I/Os, their values must be kept higher than 50kΩ to ensure that they do not contend with the output drivers of the UM3302.
For the same reason, the UM3302 should not be used in applications such as I^2C or 1-Wire where an open-drain driver is connected on the bidirectional data I/O.
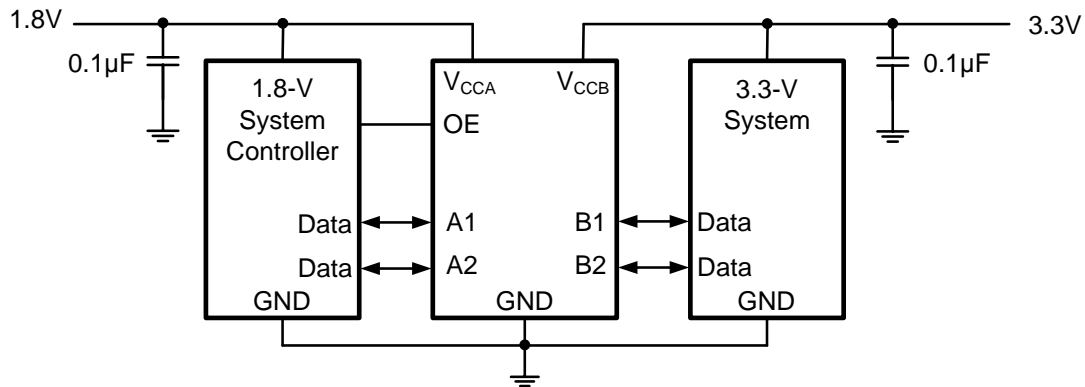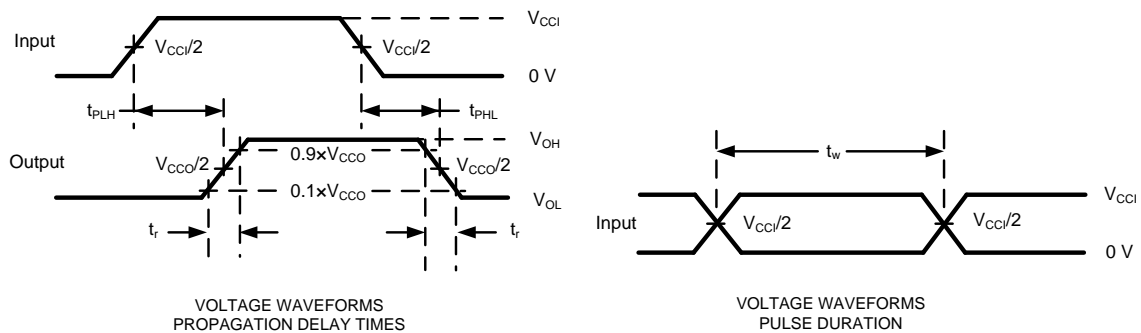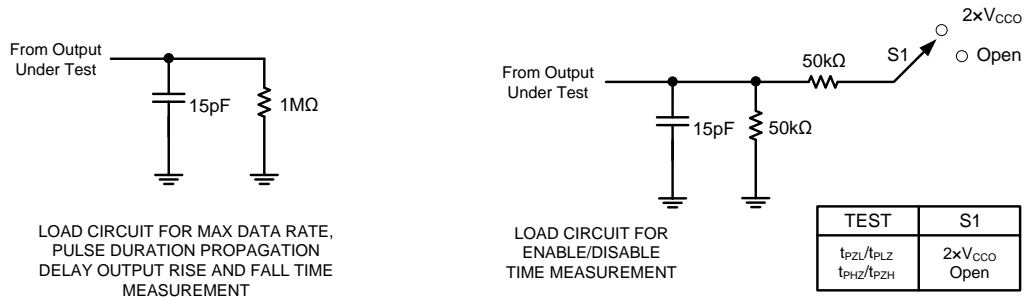
Typical Operating Circuit

Figure 3 Typical operating circuit

Test Circuits

LOAD CIRCUIT FOR MAX DATA RATE, PULSE DURATION PROPAGATION DELAY OUTPUT RISE AND FALL TIME MEASUREMENT

LOAD CIRCUIT FOR ENABLE/DISABLE TIME MEASUREMENT

TEST	S1
t_{PZL}/t_{PLZ}	$2 \times V_{CCO}$
t_{PHZ}/t_{PZH}	Open

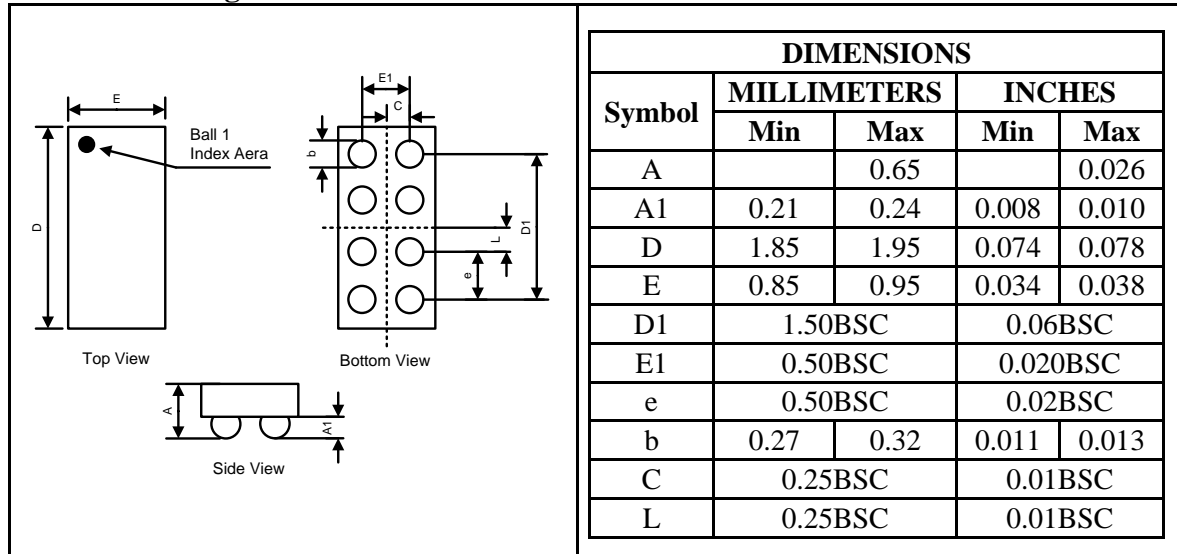VOLTAGE WAVEFORMS PROPAGATION DELAY TIMES

VOLTAGE WAVEFORMS PULSE DURATION

A. C_L includes probe and jig capacitance.
B. All input pulses are supplied by generators having the following characteristics: PRR≤10MHz, Z_O=50Ω, dv/dt≥1V/ns.
C. The outputs are measured one at a time, with one transition per measurement.
D. t_{PLH} and t_{PHL} are the same as t_{pd}.
E. V_{CCI} is the V_{CC} associated with the input port.
F. V_{CCO} is the V_{CC} associated with the output port.
G. All parameters and waveforms are not applicable to all devices.

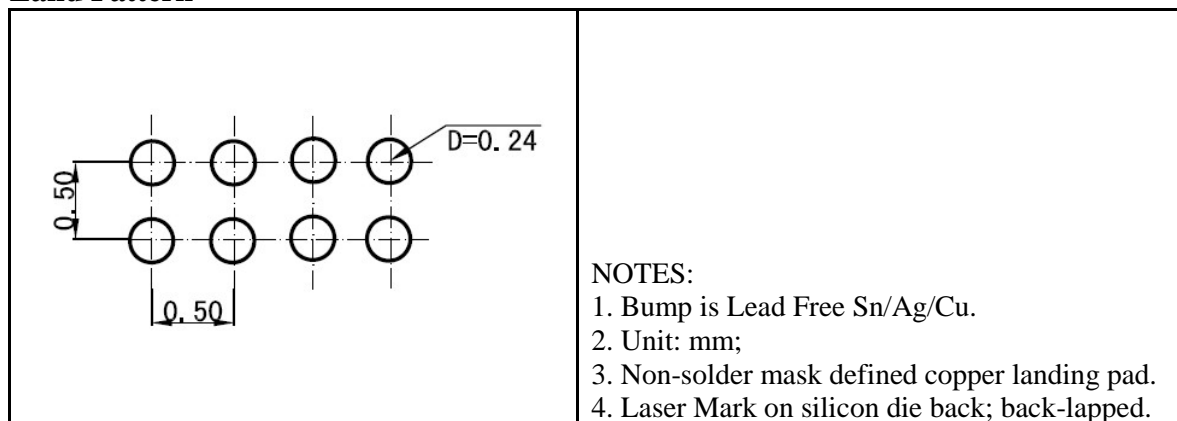**Figure 4 Load Circuits and Voltage Waveforms**

Package Information

UM3302H: CSP8 1.90×0.90

Outline Drawing

DIMENSIONS				
Symbol	**MILLIMETERS**		**INCHES**	
	Min	**Max**	**Min**	**Max**
A		0.65		0.026
A1	0.21	0.24	0.008	0.010
D	1.85	1.95	0.074	0.078
E	0.85	0.95	0.034	0.038
D1	1.50BSC		0.06BSC	
E1	0.50BSC		0.020BSC	
e	0.50BSC		0.02BSC	
b	0.27	0.32	0.011	0.013
C	0.25BSC		0.01BSC	
L	0.25BSC		0.01BSC	

Land Pattern

NOTES:
1. Bump is Lead Free Sn/Ag/Cu.
2. Unit: mm;
3. Non-solder mask defined copper landing pad.
4. Laser Mark on silicon die back; back-lapped.

Tape and Reel Orientation

UM3302Q: QFN8 1.40×1.20

Outline Drawing

	DIMENSIONS			
Symbol	**MILLIMETERS**		**INCHES**	
	Min	**Max**	**Min**	**Max**
A	0.45	0.55	0.018	0.022
A1	0.00	0.05	0.000	0.002
A3	0.13REF		0.005REF	
D	1.40BSC		0.056BSC	
E	1.20BSC		0.048BSC	
e	0.40BSC		0.016BSC	
b	0.15	0.25	0.006	0.010
L	0.20	0.40	0.008	0.016
L2	0.30	0.50	0.012	0.020

Land Pattern

NOTES:
1. Compound dimension: 1.40×1.20 ;
2. Unit: mm;
3. General tolerance ±0.05mm unless otherwise specified;
4. The layout is just for reference.
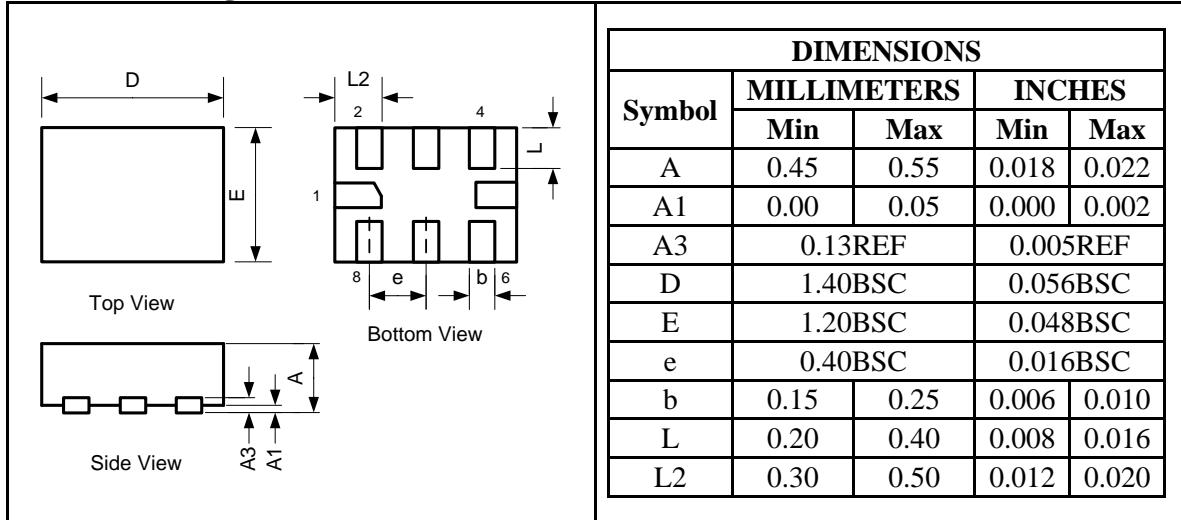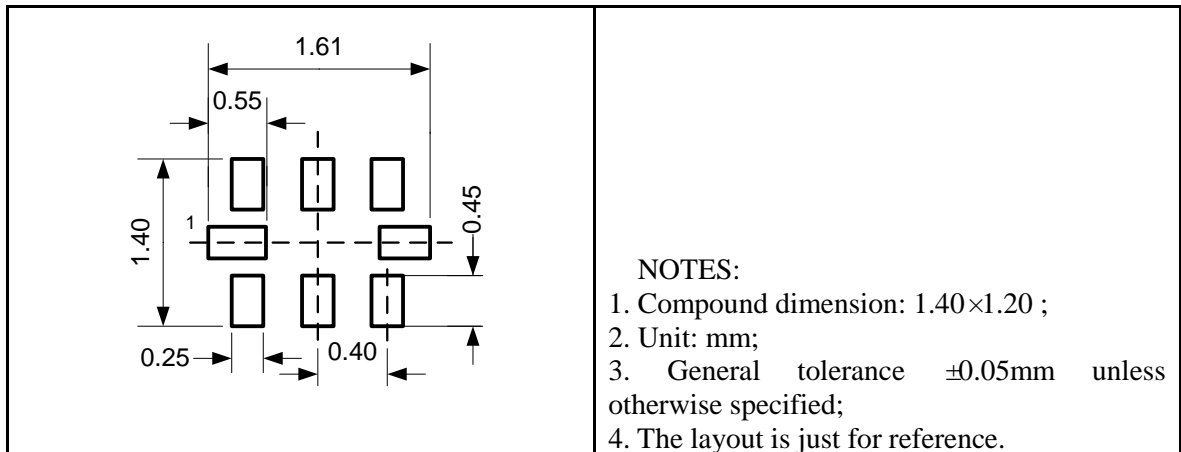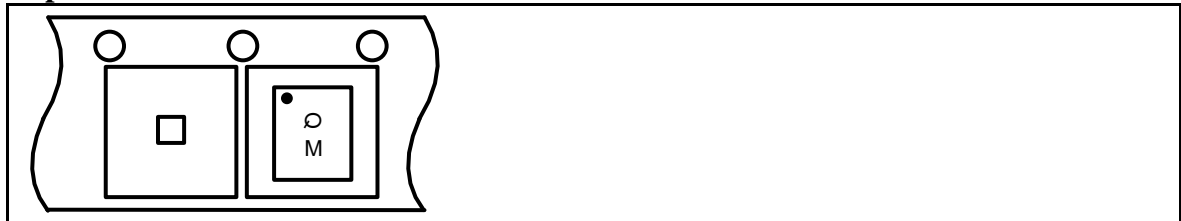
Tape and Reel Orientation

IMPORTANT NOTICE

The information in this document has been carefully reviewed and is believed to be accurate. Nonetheless, this document is subject to change without notice. Union assumes no responsibility for any inaccuracies that may be contained in this document, and makes no commitment to update or to keep current the contained information, or to notify a person or organization of any update. Union reserves the right to make changes, at any time, in order to improve reliability, function or design and to attempt to supply the best product possible.

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