# MSS40, MSS50

#### Back to back SCR module

Table 1. Main features

Symbol	Value	Unit
I <sub>T(RMS)</sub>	55 and 70	Α
V <sub>DRM</sub> /V <sub>RRM</sub>	800 and 1200	V
I <sub>GT</sub>	50	mA

#### **Description**

Packaged in ISOTOP modules, the MSS40 / MSS50 Series is based on two back-to-back SCR configurations, providing high noise immunity. They are suitable for high power applications such as solid state relays, heating control systems, welding equipment, motor control circuits...

The compactness of the ISOTOP package allows high power density and optimized power bus connections. Thanks to their internal ceramic pad, they provide high voltage insulation (2500  $V_{RMS}$ ), complying with UL standards (File ref: E81734).

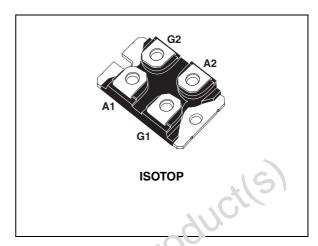
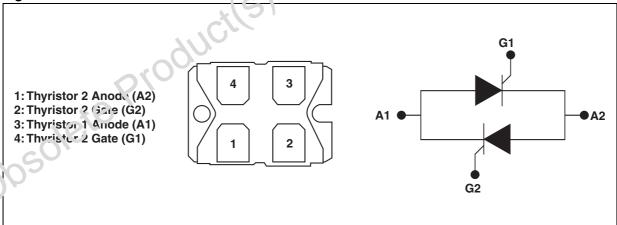


Table 2. Order coass

Part numbers	Marking
MSS412-1200	MSS40-1200
M3S50-800	MSS50-800
MSS50-1200	MSS50-1200

Figure 1. Pin connections



Characteristics MSS40, MSS50

### 1 Characteristics

Table 3. Absolute ratings (limiting values)

Symbol	Parameter			Value		Unit
Syllibol	Faramete	MSS40	MSS50	Jill		
V <sub>DRM</sub> /V <sub>RRM</sub>	Repetitive peak off-state voltage			1200	800 1200	V
ı	DMC on state surrent		$T_c = 80^{\circ} \text{ C}$	55		Α
I <sub>T(RMS)</sub> RMS on-state current	nivio ori-state current		$T_c = 85^{\circ} C$		70	А
	Non repetitive surge peak on-state	$t_p = 16.7 \text{ ms}$	T <sub>j</sub> = 25° C	420	630	
I <sub>TSM</sub> cur	current	$t_p = 20 \text{ ms}$		400	600	Α
l <sup>2</sup> t	I <sup>2</sup> t Value for fusing	t <sub>p</sub> = 10 ms	$T_j = 25^{\circ} C$	800	1800	A <sup>2</sup> s
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ , $t_r \le 100 \text{ ns}$ $F = 120 \text{ Hz}$ $T$		T <sub>j</sub> = 125° C	5	0	A/µs
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$ $T_j = 125^{\circ} C$		4	4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 125^{\circ} C$		1		W	
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range		•		+ 150 + 125	° C
$V_{RGM}$	Maximum peak reverse gate voltage		0	( i	5	V

Table 4. Electrical characteristics ( $T_j = 25^{\circ}$  C, unless otherwise specified)

Symbol	Test Condition	7/8,	Va	lue	Unit	
Symbol	rest Condition	0.	MSS40	MSS50	Oliit	
1			MIN.	Ę	5	mA
I <sub>GT</sub>	$V_D = 12 \text{ V}$ $R_L = 33 \Omega$		MAX.	5	0	IIIA
V <sub>GT</sub>	16		MAX.	1.	.3	V
$V_{GD}$	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$	T <sub>j</sub> = 125° C	MIN.	0.2		V
I <sub>H</sub>	I <sub>T</sub> = 500 mA Gate open	MAX.	80		mA	
ΙL	I <sub>G</sub> = 1.2 I <sub>GT</sub>	MAX.	120		mA	
dV/dt	$V_D = 67 \% V_{DRM}$ Gate open $T_j = 125^{\circ} C$		MIN.	10	00	V/µs
V <sub>TM</sub>	$I_{TM} = 80 \text{ A}$ $t_p = 380 \text{ µs}$ $T_i = 25^{\circ} \text{ C}$		MAX.	1.7		V
V TM	$I_{TM} = 100 \text{ A}$ $t_p = 380  \mu\text{s}$	1	WIAX.		1.7	V
V <sub>t0</sub>	Threshold voltage $T_j = 125^{\circ} C$		MAX.	0.8	85	V
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 125° C	MAX.	11	7	mΩ
I <sub>DRM</sub>	$T_j = 25^{\circ} \text{ C}$		MAX.	20		μΑ
I <sub>RRM</sub>	$V_{DRM} = V_{RRM}$	T <sub>j</sub> = 125° C	IVI/ T/X.	10		mA

Table 5. Thermal reistances

Symbol	Parameter			Unit
D. Jungtion to a		MSS40	0.6	° C/W
R <sub>th(j-c)</sub>	Junction to case (AC)	MSS50	0.45	C/VV

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MSS40, MSS50 Characteristics

Figure 2. Maximum average power dissipation versus average on-state current

Figure 3. Average and DC on-state current versus case temperature

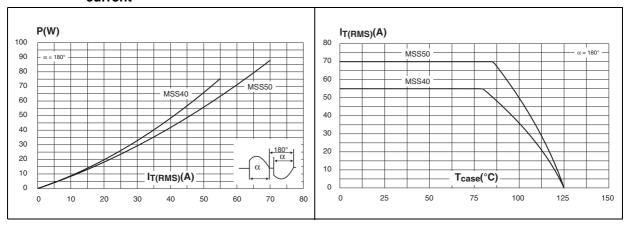


Figure 4. Relative variation of thermal impedance versus pulse duration

Figure 5. Relative variation of gate trigger current and holding current versus junction temperature

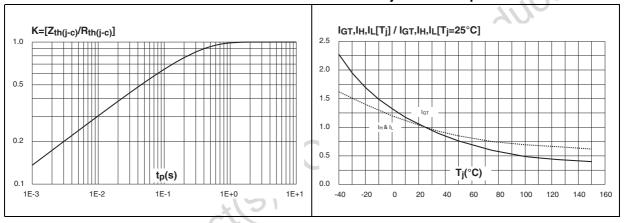
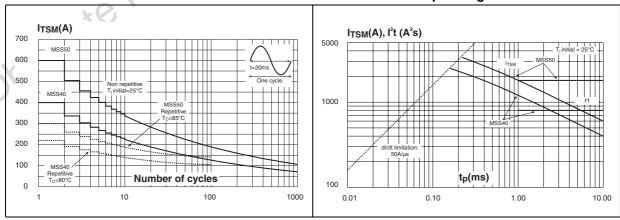


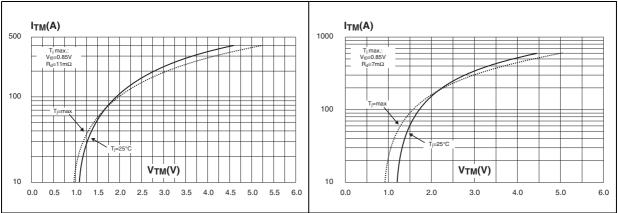
Figure 6. Surge peak on-state current versus Figure 7. number of cycles

Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10$  ms, and corresponding values of  $I^2t$ 



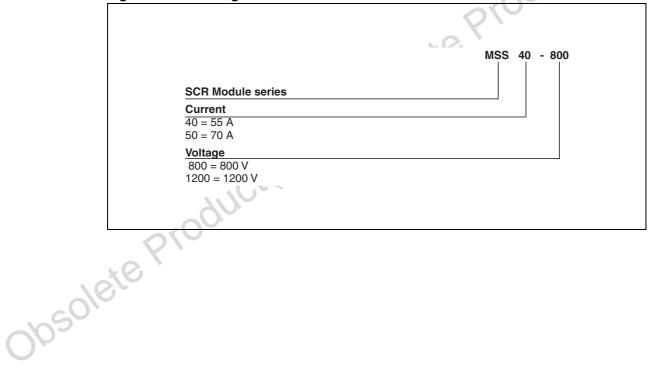
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Figure 8. On-state characteristics (maximum Figure 9. On-state characteristics (maximum values) (MSS40) values) (MSS50)



### 2 Ordering information scheme

Figure 10. Ordering information scheme



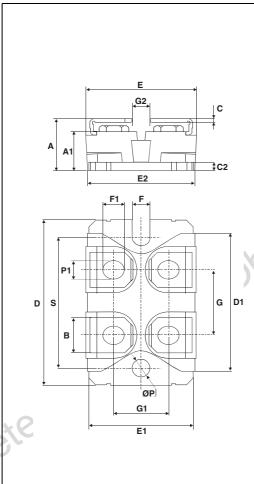
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MSS40, MSS50 Package information

#### 3 Package information

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- Recommended torque value: 0.9 Nm (max. 1.2 Nm) for the 6 x M4 screws (2 x M4 screws recommended for mounting the package on the heatsink and the 4 provided screws).
- The screws supplied with the package are adapted for mounting on a board (or other types of terminals) with a thickness of 0.6 mm min. and 2.2 mm max.

Table 6. ISOTOP dimensions



	Dimensions				
Ref.	Millim	neters	Inches		
	Min.	Max.	Min.	Max.	
Α	11.80	12.20	0.465	0.480	
A1	8.90	9.10	0.350	0.358	
В	7.8	8.20	0.307	0.323	
С	0.75	0.85	0.030	0.033	
C2	1.95	2.05	0.077	0.081	
D C	37.80	38.20	1.488	1.504	
D1	31.50	31.70	1.240	1.248	
Е	25.15	25.50	0.990	1.004	
E1	23.85	24.15	0.939	0.951	
E2	24.80	) typ.	0.976 typ.		
G	14.90	15.10	0.587	0.594	
G1	12.60	12.80	0.496	0.504	
G2	3.50	4.30	0.138	0.169	
F	4.10	4.30	0.161	0.169	
F1	4.60	5.00	0.181	0.197	
Р	4.00	4.30	0.157	0.69	
P1	4.00	4.40	0.157	0.173	
S	30.10	30.30	1.185	1.193	

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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Ordering information MSS40, MSS50

# 4 Ordering information

Table 7. Ordering information

Part number	Marking	Package	Weight	Base qty	Delivery mode
MSS40-1200	MSS40-1200		0.7	40	
MSS50-800	MSS50-800	ISOTOP	27 g (without screws)	10 (with screws)	Tube
MSS50-1200	MSS50-1200		(	(	

## 5 Revision history

Table 8. Revision history

Γ	Date	Revision	Changes
	Sep-2000	3	Last release.
	11-Jul-2007	4	Reformated to current standards. Removed MSS40-800 product.
Obsole	ePro	ducil	S). Obsolete Pro

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