

Bi-directional ESD Protection Diode

FEATURES

- Meet IEC61000-4-2 (ESD) $\pm 15\text{kV}$ (air), $\pm 8\text{kV}$ (contact)
- Meet IEC61000-4-4 (EFT) rating. 40A (5/50ns)
- Protects one Bi-directional I/O line
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS directive 2011/65/EU and in accordance to WEEE 2002/96/EC

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
P_{PPM}	100	W
I_{PP}	3	A
V_{RWM}	5	V
V_C at $I_{PP} = 3\text{ A}$	15	V
Package	SOD-523F	

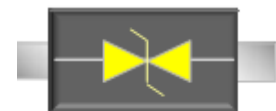
APPLICATIONS

- Cell Phone Handsets and Accessories
- Notebooks, Desktops, and Servers
- Keypads, Side Keys, USB 2.0, LCD Displays
- Portable Instrumentation
- Microprocessor Based Equipment



MECHANICAL DATA

- Case: SOD-523F
- Molding compound meets UL 94 V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Weight: 2 mg (approximately)
- Marking code on the device: DT



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TESDD5V0	UNIT
Rated random recurring peak Impulse power dissipation (tp=8/20 μs waveform)	P_{PPM}	100	W
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	± 15 ± 8	KV
Junction temperature range	T_J	-55 to +150	$^\circ\text{C}$
Storage temperature range	T_{STG}	-55 to +150	$^\circ\text{C}$

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Reverse breakdown voltage ⁽¹⁾	$I_R = 1 \text{ mA}$	$V_{(BR)}$	6	-	-	V
Rated working standoff voltage		V_{WM}	-	-	5	V
Reverse current ⁽¹⁾	$V_R = 5 \text{ V}$	I_R	-	-	1	μA
Clamping voltage ⁽²⁾	$I_{PP} = 1 \text{ A}$	V_C	-	-	9.8	V
Clamping voltage ⁽²⁾	$I_{PP} = 3 \text{ A}$	V_C	-	-	15	V
Junction capacitance	1 MHz, $V_R = 0\text{V}$	C_J	-	13	-	pF

Notes:

1. Pulse test with $PW = 30 \text{ ms}$
2. $t_p = 8/20\mu\text{s}$ waveform

ORDERING INFORMATION		
ORDERING CODE	PACKAGE	PACKING
TESDD5V0 RK	SOD-523F	3K / 7" Reel
TESDD5V0 RKG	SOD-523F	3K / 7" Reel

Note: "G" means green compound (halogen)

CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Fig. 1 Non-Repetitive Peak Pulse Power vs. Pulse Time

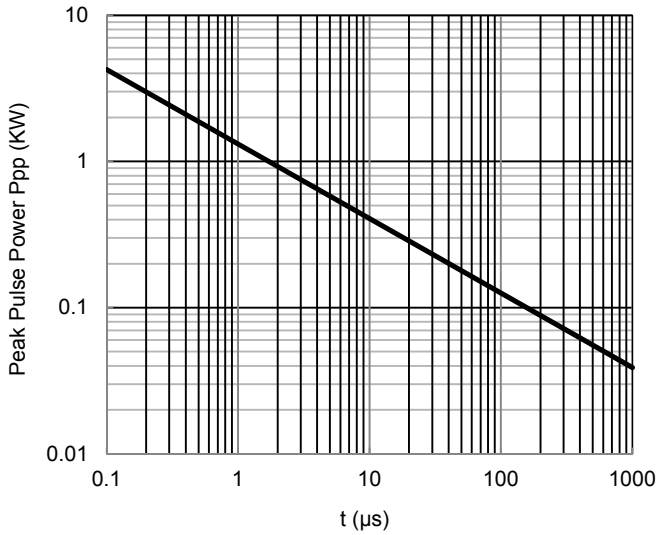


Fig. 2 8/20 μs pulse waveform according to IEC 61000-4-5

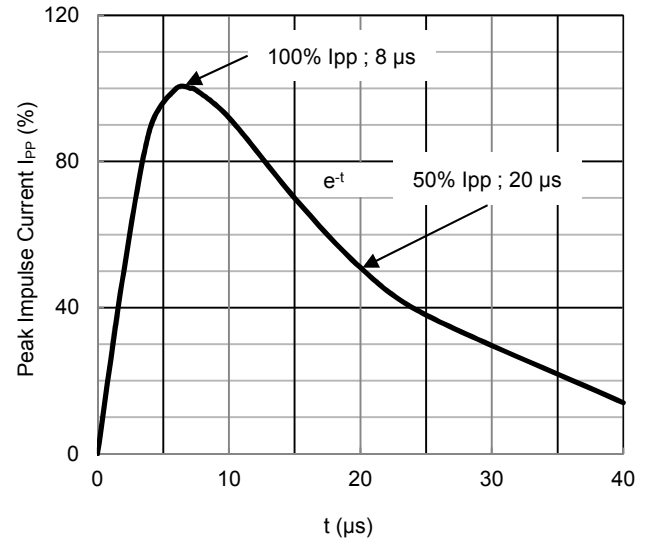


Fig. 3 Admissible Power Dissipation Curve

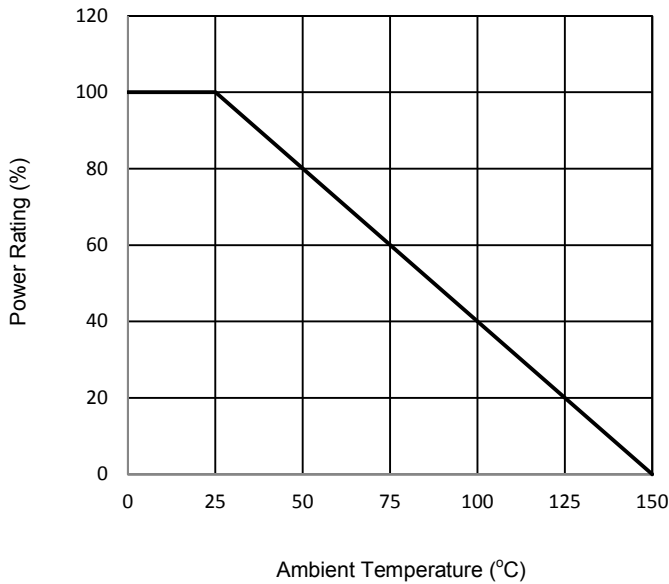
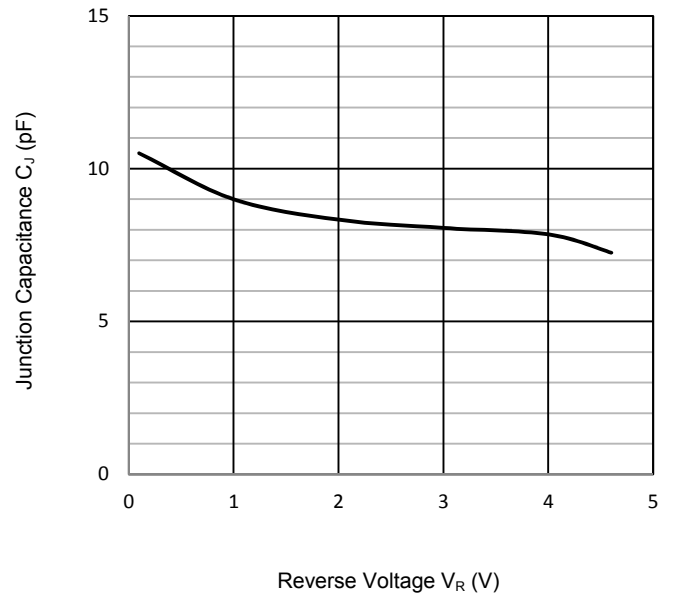


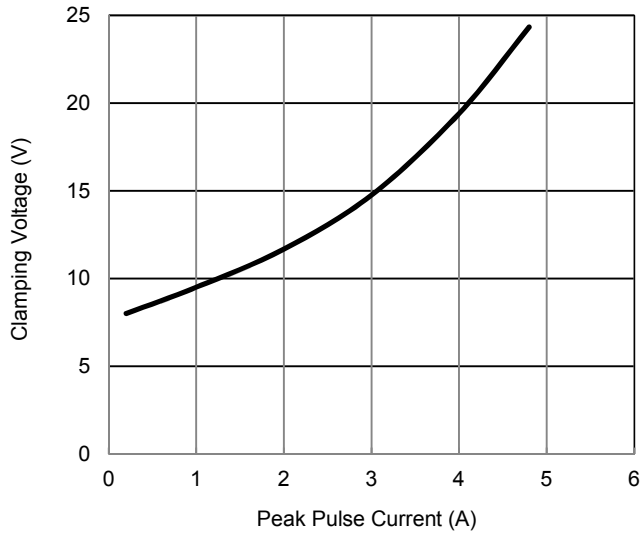
Fig. 4 Typical Junction Capacitance



CHARACTERISTICS CURVES

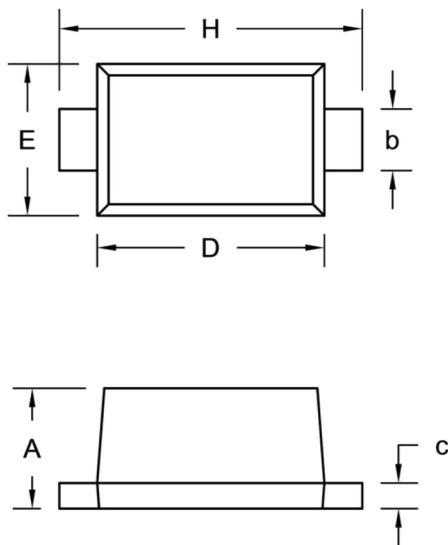
($T_A = 25^\circ\text{C}$ unless otherwise noted)

**Fig. 5 Clamping Voltage vs.
Peak Pulse Current**



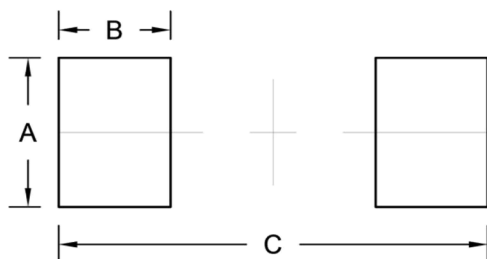
PACKAGE OUTLINE DIMENSION

SOD-523F



DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
A	0.50	0.77	0.020	0.030
b	0.25	0.40	0.010	0.016
c	0.07	0.20	0.003	0.008
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
H	1.50	1.70	0.059	0.067

SUGGEST PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	0.80	0.031
B	0.60	0.024
C	2.30	0.091

Note: The suggested land pattern dimensions have been provided for reference only, as actual pad layouts may vary depending on application.

APPLICATION INFORMATION

- Designed to protect one data, I/O, or power supply line
- Designed to protect sensitive electronics from damage or latch-up due to ESD
- Designed to replace multilayer varistors (MLVs) in portable applications
- Offers superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs
- The combination of small size and high ESD surge capability makes them ideal for use in portable applications

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

- Good circuit board layout is critical for the suppression of ESD induced transients
- Place the ESD Protection Diode near the input terminals or connectors to restrict transient coupling
- Minimize the path length between the ESD Protection Diode and the protected line
- Minimize all conductive loops including power and ground loops
- The ESD transient return path to ground should be kept as short as possible

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