



PJSD05LCTM

BI-DIRECTIONAL ESD PROTECTION DIODE

This Penta has been designed to Protect Sensitive Equipment against ESD and to prevent Latch-Up events in CMOS circuitry operating at 5Vdc and below. This offers an integrated solution to protect up to data line where the board space is a premium.

SPECIFICATION FEATURES

- 100W Power Dippsipation (8/20µs Waveform)
- Low Leakage Current, Maximum of 1μA@5Vdc
- · Very low Clamping voltage
- IEC 61000-4-2 ESD 15kV air, 8kV Contact Compliance
- In compliance with EU RoHS 2002/95/EC directivess

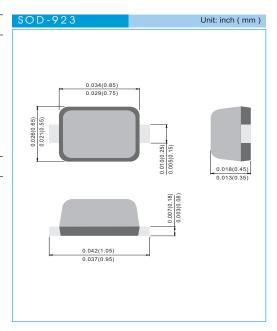
APPLICATIONS

- Video I/O ports protection
- Set Top Boxes
- · Portable Instrumentation
- Case : SOD-923, Plastic

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0004gram

· Marking: X



MAXIMUM RATINGS

Rating	Symbol	Value	Units
Peak Pulse Power (8/20 μs Waveform)	Ppp	100	w
Peak Pulse Current (8/20 μs Waveform)	І РР	1	А
ESD Voltage (HBM)	V _{ESD}	>25	kV
Operating Junction and Storage Temperature Range	TJ, TSTG	-55 to 150	°C

ELECTRICAL CHARACTERISTICS (TA=25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-Off Voltage	Vwrm		-	-	5.0	V
Reverse Breakdown Voltage	VBR	I BR=1mA	5.78	-	7.82	V
Reverse Leakage Current	IR	V _R =5V	-	-	1.0	μΑ
Clamping Voltage (8/20μs)	Vc	I pp=1A	-	-	12.5	V
Off State Junction Capacitance	Сл	0 Vdc Bias f=1MHz	-	-	15	pF

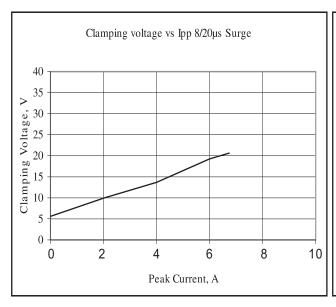
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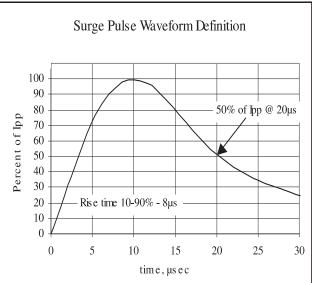


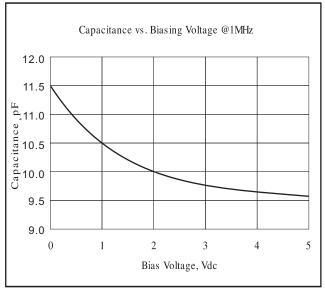


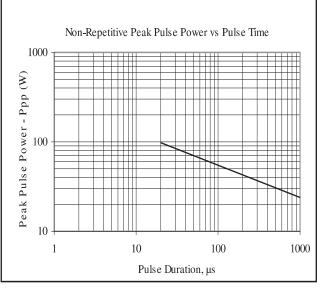
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TYPICAL CHARACTERISTICS









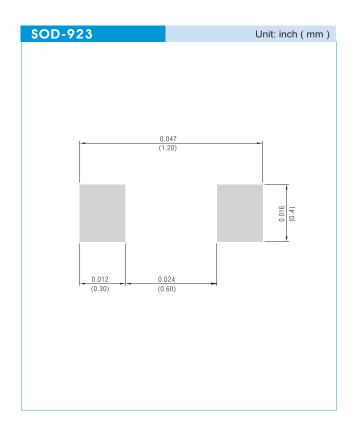
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MOUNTING PAD LAYOUT



ORDER INFORMATION

Packing information
T/R - 8K per 7" plastic Reel

LEGAL STATEMENT

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