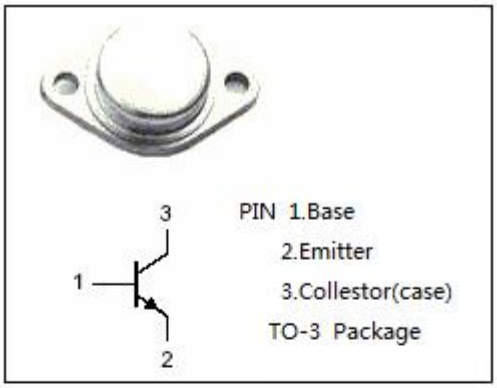
isc Silicon NPN Power Transistor 2N4395

DESCRIPTION

- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- The device employs the popular JEDEC TO-3
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

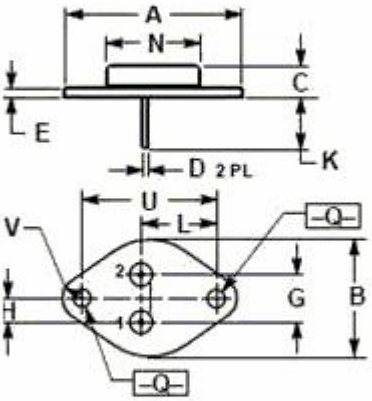


APPLICATIONS

- High voltage high current power transistors

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	60	V
V_{CEO}	Collector-Emitter Voltage	40	V
V_{EBO}	Emitter-Base Voltage	4	V
I_C	Collector Current-Continuous	5	A
P_C	Collector Power Dissipation@T_C=25℃	62	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature	-65~150	℃



DIM	mm MIN	MAX
A	39.00	
B	25.30	26.67
C	7.60	8.50
D	0.90	1.10
E	1.40	1.60
G	10.92	
H	5.46	
K	11.30	13.50
L	16.75	17.05
N	19.40	19.62
Q	4.00	4.20
U	30.00	30.20
V	4.30	4.50

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j\text{-}c}$	Thermal Resistance,Junction to Case	2	℃/W

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CEO(SUS)}$*	Collector-Emitter Sustaining Voltage	I_C=30mA; I_B= 0	40		V
I_{CEO}	Collector Cutoff Current	V_{CE}=40V;I_B= 0		200	mA
I_{EBO}	Emitter Cutoff Current	V_{EB}= 4V; I_C= 0		0.1	mA
h_{FE}	DC Current Gain	I_C=2A; V_{CE}= 1V	50	170	

*:Pulse test:Pulse width=300us,duty cycle≤2%