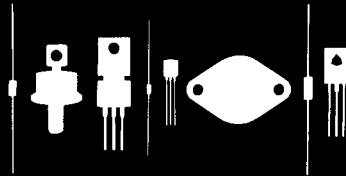


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145 Adams Avenue  
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2N5555  
N-CHANNEL FET  
JEDEC TO-92 CASE

### DESCRIPTION

The CENTRAL SEMICONDUCTOR 2N5555 type is an Silicon N Channel Junction FET designed for switching, RF amplifier and mixer applications where low capacitance is desired.

MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

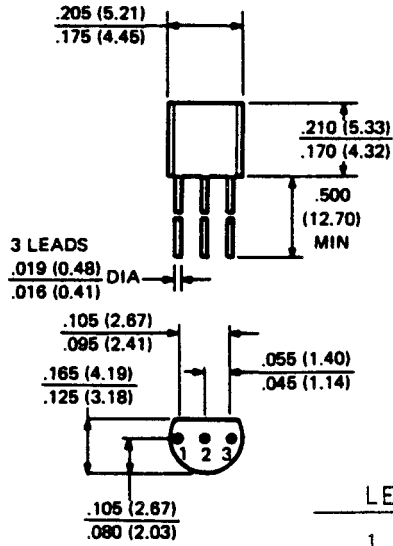
	<u>SYMBOL</u>		<u>UNIT</u>
Gate-Source Voltage	$V_{GS}$	25	V
Drain-Source Voltage	$V_{DS}$	25	V
Drain-Gate Voltage	$V_{DG}$	25	V
Gate Current	$I_G$	10	mA
Power Dissipation	$P_D$	310	mW
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 TO +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

<u>SYMBOL</u>	<u>TEST CONDITIONS</u>	<u>MIN</u>	<u>MAX</u>	<u>UNIT</u>
$I_{GSS}$	$V_{GS}=15\text{V}$		1.0	nA
$I_{DSS}$	$V_{DS}=15\text{V}$	15		mA
$BV_{GSS}$	$I_G=10\mu\text{A}$	25		V
$V_{GS}(f)$	$I_G(f)=1.0\text{mA}$		1.0	V
$I_D(\text{OFF})$	$V_{DS}=12\text{V}, V_{GS}=10\text{V}$		10	nA
$I_D(\text{OFF})$	$V_{DS}=12\text{V}, V_{GS}=10\text{V}, T_A=100^\circ\text{C}$		2.0	$\mu\text{A}$
$V_{DS}(\text{ON})$	$I_D=7.0\text{mA}$		1.5	V
$r_{DS}(\text{ON})$	$I_D=0.1\text{mA}$		150	$\Omega$
$r_{ds}(\text{ON})$	$V_{GS}=0, I_D=0, f=1.0\text{kHz}$		150	$\Omega$
$C_{iss}$	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		5.0	pF
$C_{rss}$	$V_{GS}=10\text{V}, V_{DS}=0, f=1.0\text{MHz}$		1.2	pF
$t_d(\text{ON})$	$V_{DD}=10\text{V}, V_{GS}(\text{ON})=0, V_{GS}(\text{OFF})=10\text{V}, I_D(\text{ON})=7.0\text{mA}$		5.0	ns
$t_r$	$V_{DD}=10\text{V}, V_{GS}(\text{ON})=0, V_{GS}(\text{OFF})=10\text{V}, I_D(\text{ON})=7.0\text{mA}$		5.0	ns
$t_d(\text{OFF})$	$V_{DD}=10\text{V}, V_{GS}(\text{ON})=0, V_{GS}(\text{OFF})=10\text{V}, I_D(\text{ON})=7.0\text{mA}$		15	ns
$t_f$	$V_{DD}=10\text{V}, V_{GS}(\text{ON})=0, V_{GS}(\text{OFF})=10\text{V}, I_D(\text{ON})=7.0\text{mA}$		10	ns

SEE REVERSE FOR OUTLINE DRAWING

OUTLINE DRAWING



LEAD CODE

- 1. DRAIN
- 2. SOURCE
- 3. GATE

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