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2N6117

2N6118

Silicon Programmable Unijunction Transistor

Applications Include:

- SCR Trigger
- Pulse and Timing Circuits
- Oscillators
- Sensing Circuits
- Sweep Circuits

absolute maximum ratings: (25°C)

Voltage

- Gate-Cathode Forward Voltage
- Gate-Cathode Reverse Voltage
- Gate-Anode Reverse Voltage
- Anode-Cathode Voltage

+40 V
- 5 V
+40 V
+40 V

Current

- DC Anode Current
- Peak Anode, Recurrent Forward
(100 μ sec pulse width, 1% duty cycle)
- (20 μ sec pulse width, 1% duty cycle)
- Peak Anode, Non-recurrent Forward
(10 μ sec)
- Gate Current

200 mA
1 A
2 A
5 A
 ± 20 mA

Power

- Total Average Power

250 mW

Temperature

- Operating Junction Temperature range, T_J
- Storage temperature range, T_{STG}

-55 to +125 °C
-65 to +200 °C

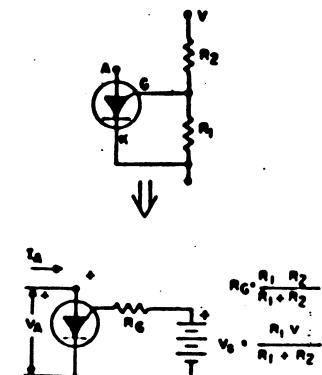
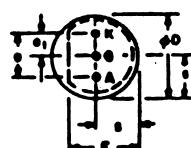
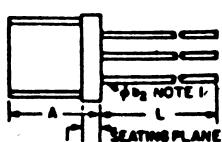
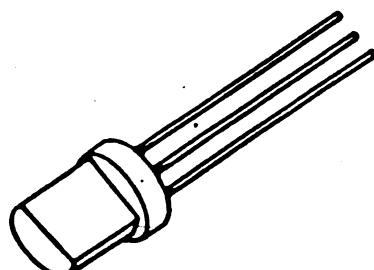


Figure 1



SYMBOL	INCHES		MILLIMETERS	
	MIN.	MAX.	MIN.	MAX.
A	.170	.266	4.38	6.78
ϕ_{d2}	.016	.019	.406	.493
ϕ_D	.165	.206	4.19	5.21
E	.110	.158	2.79	3.94
ϕ	.098	.105	2.41	2.67
ϕ_1	.045	.058	1.14	1.40
L	.500		12.70	
ϕ_2		.078		1.90
ϕ_3	.000	.116	2.03	2.92

NOTE 1: LEAD DIAMETER IS CONTROLLED IN THE ZONE BETWEEN .070 AND .230 FROM THE SEATING PLANE. BETWEEN .290 AND END OF LEAD A MAX. OF .021 IS HELD.

electrical characteristics: (25°C) (unless otherwise specified)

		Fig. No. 1	2N6117		2N6118	
			Min.	Max.	Min.	Max.
*Peak Current ($V_s = 10$ Volts) ($R_G = 1$ Meg) ($R_G = 10k$)	I_P		—	0.3	—	0.15 μA
*Offset Voltage ($V_s = 10$ Volts) ($R_G = 1$ Meg) ($R_G = 10k$)	V_T	1	0.2	0.6	0.2	0.6 μA
*Valley Current ($V_s = 10$ Volts) ($R_G = 1$ Meg) ($R_G = 10k$)	I_V	1	— 50	50 —	— 50	25 μA
*Anode Gate-Anode Leakage Current ($V_s = 40$ Volts, $T = 25^\circ C$) ($T = 75^\circ C$)	I_{GAO}	2	—	5	—	5 nA
*Gate to Cathode Leakage Current ($V_s = 40$ Volts, Anode-Cathode short)	I_{GKS}	3	—	50	—	50 nA
*Forward Voltage ($I_F = 50$ mA)	V_F		—	1.5	—	1.5 Volts
*Pulse Output Voltage	V_O	4	6	—	6	— Volts
*Pulse Voltage Rate of Rise	t_r	4	—	80	—	80 nsec

*JEDEC registered data

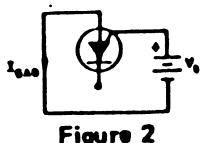


Figure 2

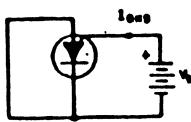


Figure 3

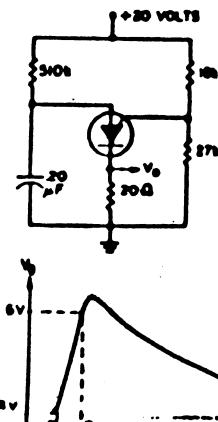


Figure 4

