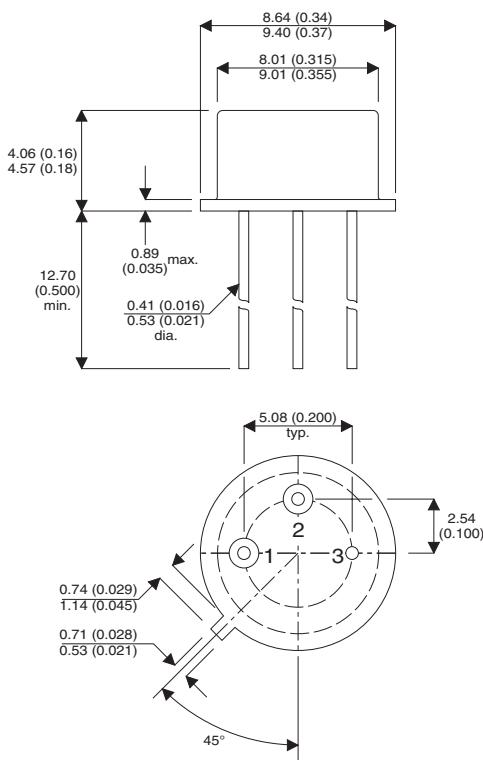


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**2N6790
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MECHANICAL DATA

Dimensions in mm (inches)



TO39 Package (TO-205AF)

Underside View

Pin 1 - Source

Pin 2 - Gate

Pin 3 - Drain and Case

N-CHANNEL POWER MOSFET ENHANCEMENT MODE

FEATURES

- REPETITIVE AVALANCHE RATING
- SIMPLE DRIVE REQUIREMENTS
- HERMETICALLY SEALED

APPLICATIONS

- FAST SWITCHING
- MOTOR CONTROLS
- POWER SUPPLIES

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^\circ\text{C}$ unless otherwise stated)

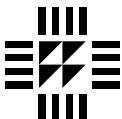
V_{DS}	Drain Source Voltage	200V
$I_D @ T_{case} = 25^\circ\text{C}$	Continuous Drain Current	3.5A
$I_D @ T_{case} = 100^\circ\text{C}$	Continuous Drain Current	2.25A
I_{DM}	Pulsed Drain Current ¹	14A
V_{GS}	Gate Source Voltage	$\pm 20\text{V}$
$P_D @ T_{case} = 25^\circ\text{C}$	Maximum Power Dissipation	20W
$R_{\theta J-C}$	Thermal Resistance Junction To Case	6.25°C/W
$R_{\theta J-A}$	Thermal Resistance Junction To Ambient	175°C/W
T_J, T_{stg}	Operating and Storage Temperature Range	-55 to +150°C
Lead Temperature	(1.6mm from case for 10 secs)	300°C

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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ELECTRICAL CHARACTERISTICS ($T_{case} = 25^\circ C$ unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
STATIC ELECTRICAL RATINGS					
BV_{DSS}	Drain – Source Breakdown Voltage	$V_{GS} = 0$	$I_D = 1.0\text{mA}$	200	V
$V_{GS(th)}^*$	Gate Threshold Voltage	$V_{DS} = V_{GS}$	$I_D = 250\mu\text{A}$	2.0	4.0
I_{GSSF}	Gate Body Leakage Forward	$V_{GS} = 20\text{V}$		100	nA
I_{GSSR}	Gate Body Leakage Reverse	$V_{GS} = -20\text{V}$		-100	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 160\text{V}$	$V_{GS} = 0$ $T_C = 125^\circ C$	25 250	μA
$R_{DS(on)}$ *	Static Drain Source On-State Resistance	$V_{GS} = 10\text{V}$	$I_D = 2.25\text{A}$	0.80	Ω
		$V_{GS} = 10\text{V}$	$I_D = 3.5\text{A}$	0.92	
gfs^*	Forward Transconductance	$V_{DS} = 15\text{V}$	$I_{DS} = 2.25\text{A}$	1.5	S (Ω)
DYNAMIC CHARACTERISTICS					
C_{iss}	Input Capacitance	$V_{GS} = 0$	$V_{DS} = 25\text{V}$	260	pF
C_{oss}	Output Capacitance	$f = 1.0\text{MHz}$		100	
C_{rss}	Reverse Transfer Capacitance			30	
$t_{d(on)}$	Turn-On Delay Time			40	
t_r	Rise Time	$V_{DD} = 100\text{V}$	$I_D = 3.5\text{A}$	50	ns
$t_{d(off)}$	Turn-Off Delay Time	(MOSFET switching times are essentially independent of operating temperature.)		50	
t_f	Fall Time			50	
Q_g	Total Gate Charge	$V_{GS} = 10\text{V}$	$I_D = 3.5\text{A}$	8.0	nC
Q_{gs}	Gate To Source Charge	$V_{DS} = 100\text{V}$		0.9	
Q_{gd}	Gate To Drain ("Miller") Charge			2.3	
BODY– DRAIN DIODE RATINGS & CHARACTERISTICS					
I_s	Continuous Source Current (Body Diode)	Modified MOS POWER symbol showing the integral P-N junction rectifier.		3.5	A
I_{SM}	Source Current (Body Diode)			14	
V_{SD}	Diode Forward Voltage*	$I_S = 3.5\text{A}$	$V_{GS} = 0$	1.5	V
t_{rr}	Reverse Recovery Time	$I_F = 3.5\text{A}$	$T_J = 25^\circ C$	400	ns
Q_{RR}	Reverse Recovery Charge	$d_i / d_t = 100\text{A}/\mu\text{s}$	$V_{DD} = 50\text{V}$	4.3	μC

Notes

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, $\delta \leq 2\%$

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