

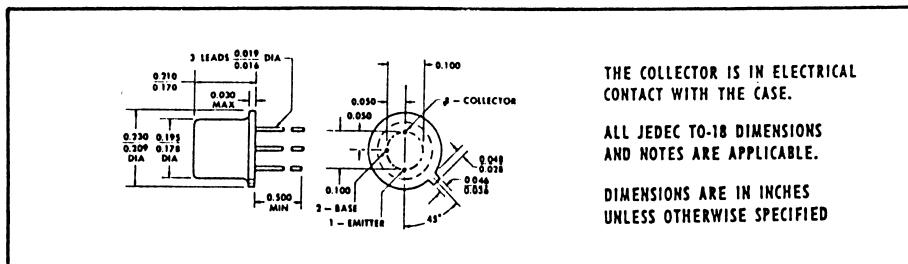
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## 2N709 N-P-N EPITAXIAL PLANAR SILICON TRANSISTOR

### mechanical data



### absolute maximum ratings at 25°C free-air temperature (unless otherwise noted)

Collector-Base Voltage . . . . .	15 v
Collector-Emitter Voltage (See Note 1) . . . . .	6 v
Emitter-Base Voltage . . . . .	4 v
Continuous Device Dissipation at (or below) 25°C Free-Air Temperature (See Note 2) . . . . .	0.3 w
Continuous Device Dissipation at (or below) 100°C Case Temperature (See Note 3) . . . . .	0.5 w
Storage Temperature Range . . . . .	-65°C to +200°C
Lead Temperature $\frac{1}{16}$ Inch from Case for 10 Seconds . . . . .	300°C

### electrical characteristics at 25°C free-air temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	MAX	UNIT
$V_{(BR)CBO}$ Collector-Base Breakdown Voltage	$I_C = 10 \mu\text{A}, I_E = 0$	15		v
$V_{(BR)CEO}$ Collector-Emitter Breakdown Voltage	$I_C = 10 \text{ mA}, I_E = 0$ , See Note 4	6		v
$V_{(BR)EBO}$ Emitter-Base Breakdown Voltage	$I_E = 10 \mu\text{A}, I_C = 0$	4		v
$I_{CBO}$ Collector Cutoff Current	$V_{CB} = 5 \text{ v}, I_E = 0$	50		na
	$V_{CB} = 5 \text{ v}, I_E = 0, T_A = 125^\circ\text{C}$	5		$\mu\text{A}$
$h_{FE}$ Static Forward Current Transfer Ratio	$V_{CE} = 0.5 \text{ v}, I_C = 10 \text{ mA}$ , See Note 4	30	90	
	$V_{CE} = 1 \text{ v}, I_C = 30 \text{ mA}$ , See Note 4	15		
	$V_{CE} = 0.5 \text{ v}, I_C = 10 \text{ mA}, T_A = -55^\circ\text{C}$ , See Note 4	10		
	$V_{CE} = 3 \text{ v}, I_C = 100 \mu\text{A}$	45		
$V_{BE}$ Base-Emitter Voltage	$I_B = 0.15 \text{ mA}, I_C = 3 \text{ mA}$	0.7	0.85	v
$V_{CE(sat)}$ Collector-Emitter Saturation Voltage	$I_B = 0.15 \text{ mA}, I_C = 3 \text{ mA}$		0.3	v
$f_T$ Transition Frequency	$V_{CE} = 4 \text{ v}, I_C = 5 \text{ mA}$ , See Note 5	600		Mc
$C_{obo}$ Common-Base Open-Circuit Output Capacitance	$V_{CB} = 5 \text{ v}, I_E = 0, f = 140 \text{ kc}$		3	pf
$C_{ibo}$ Common-Base Open-Circuit Input Capacitance	$V_{EB} = 0.5 \text{ v}, I_C = 0, f = 140 \text{ kc}$		2	pf

NOTES: 1. This value applies when the base-emitter diode is open circuited.

2. Derate linearly to 200°C free-air temperature at the rate of 1.71 mw/C°.

3. Derate linearly to 200°C case temperature at the rate of 5 mw/C°.

4. These parameters must be measured using pulse techniques. PW = 300  $\mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

5. To obtain  $f_T$ , the  $|h_{fe}|$  response with frequency is extrapolated at the rate of -6db per octave from  $f = 100 \text{ Mc}$  to the frequency at which  $|h_{fe}| = 1$ .



Quality Semi-Conductors