**PNP/NPN Epitaxial Planar Silicon Transistors** 



## 2SA1415/2SC3645

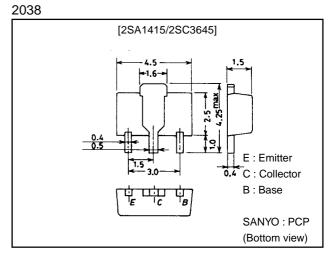
# High-Voltage Switching, **Predriver Applications**

#### **Features**

- · Adoption of FBET process.
- · High breakdown voltage ( $V_{CEO}$ =160V).
- $\cdot$  Excellent linearity of  $h_{FE}$  and small Cob.
- · Fast switching speed.
- · Very small size marking it easy to provide highdensity, small-sized hybrid ICs.

## **Package Dimensions**

unit:mm



(): 2SA1415

### **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		(–)180	V
Collector-to-Emitter Voltage	VCEO		(–)160	V
Emitter-to-Base Voltage	V <sub>EBO</sub>		(–)5	V
Collector Current	ι <sub>C</sub>		(–)140	mA
Collector Current (Pulse)	I <sub>CP</sub>		(-)200	mA
Collector Dissipation	P <sub>C</sub> 1		500	mW
	P <sub>C</sub> 2	Moutned on ceramic board (250mm <sup>2</sup> ×0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

Symbol	Conditions	Ratings			Unit
		min	typ	max	Unit
ICBO	V <sub>CB</sub> =(-)80V, I <sub>E</sub> =0			(–)100	nA
IEBO	V <sub>EB</sub> =(-)4V, I <sub>C</sub> =0			(–)100	nA
hFE	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)10mA	100*		400*	
fT	V <sub>CE</sub> =(-)10V, I <sub>C</sub> =(-)10mA		150		MHz
Cob	V <sub>CB</sub> =(-)10V, f=1MHz		(4.0)		pF
			3.0		pF
V <sub>CE(sat)</sub>	I <sub>C</sub> =(-)50mA, I <sub>B</sub> =(-)5mA		(-0.14)	(-0.4)	V
			0.07	0.3	V
ton	See sepcified Test Circuit.		0.1		μs
<sup>t</sup> stg	See sepcified Test Circuit.		1.5		μs
t <sub>f</sub>	See sepcified Test Circuit.		0.1		μs
	ICBO IEBO hFE fT Cob VCE(sat) ton tstg	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	Imin min   ICBO VCB=(-)80V, IE=0 Imin   IEBO VEB=(-)4V, IC=0 Imin   hFE VCE=(-)5V, IC=(-)10mA 100*   fT VCE=(-)10V, IC=(-)10mA Imin   Cob VCB=(-)10V, IC=(-)10mA Imin   VCB VCB=(-)10V, IC=(-)10mA Imin   VCE(sat) IC=(-)50mA, IB=(-)5mA Imin   ton See sepcified Test Circuit. Imin   tstg See sepcified Test Circuit. Imin	$\begin{tabular}{ c c c c c } \hline Symbol & Conditions & \hline min & typ \\ \hline I_{CBO} & V_{CB}=(-)80V, I_E=0 & & & & \\ \hline I_{EBO} & V_{EB}=(-)4V, I_C=0 & & & & \\ \hline I_{EBO} & V_{CB}=(-)5V, I_C=(-)10mA & 100^* & & \\ \hline f_T & V_{CE}=(-)10V, I_C=(-)10mA & & & 150 & \\ \hline C_{Ob} & V_{CB}=(-)10V, f=1MHz & & & & (4.0) & & \\ \hline & & & & & & & \\ \hline C_{C}(sat) & I_{C}=(-)50mA, I_{B}=(-)5mA & & & & & & \\ \hline V_{CE}(sat) & I_{C}=(-)50mA, I_{B}=(-)5mA & & & & & & & \\ \hline & & & & & & & & & \\ \hline V_{CE}(sat) & I_{C}=(-)50mA, I_{B}=(-)5mA & & & & & & & \\ \hline & & & & & & & & & \\ \hline & & & &$	$\begin{tabular}{ c c c c c } \hline & C & C & \hline & min & typ & max \\ \hline min & tstg & see sepcified Test Circuit. \\ \hline min & tstg & see sepcified Test Circuit. \\ \hline min & tstg & term & typ & max \\ \hline min & tstg & term & term & typ & max \\ \hline min & tstg & term & ter$

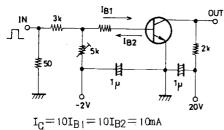
 $\ast$  : The 2SA1415/2SC3645 are classified by 10mA  $h_{FE}$  as follows :

100 R 200 140 S 280 200 T 400 Marking 2SA1415 : AA h<sub>FE</sub> rank : R, S, T

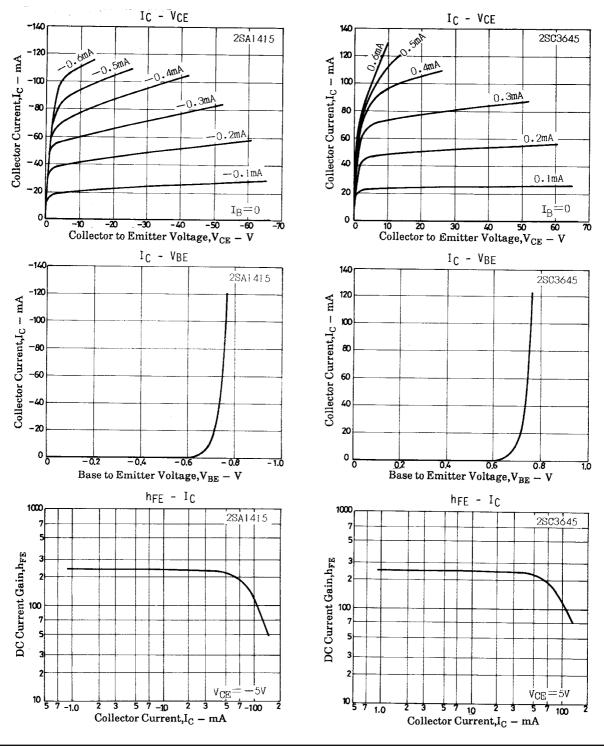
2SC3645 : CA

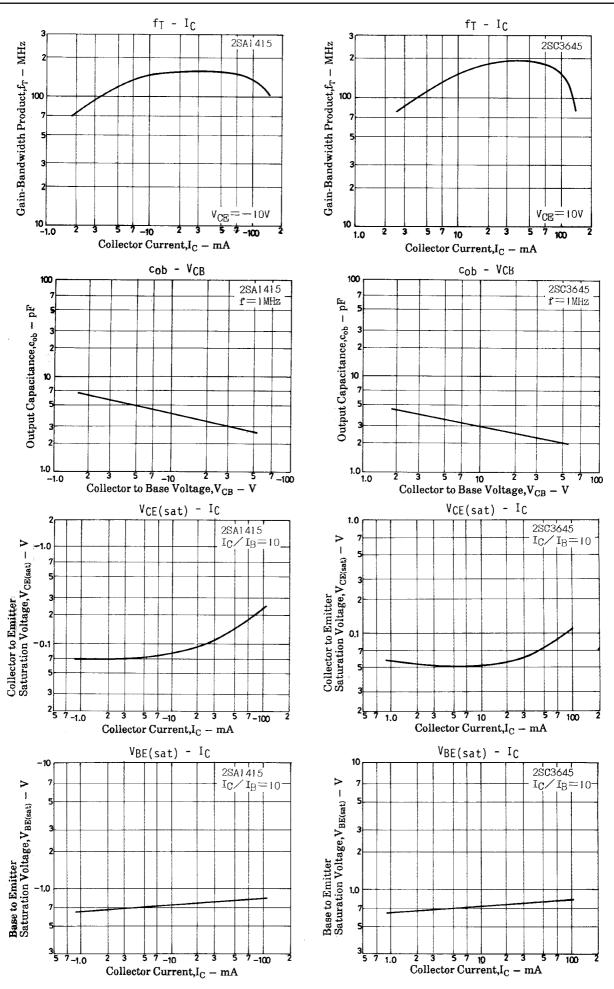
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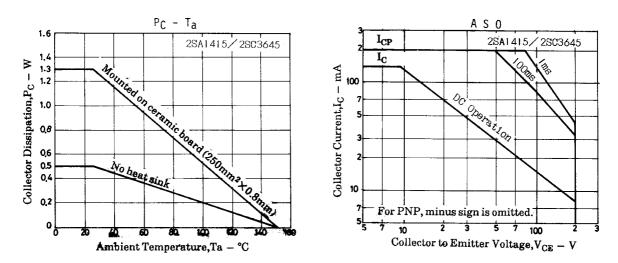
#### **Switching Time Test Circuit**



(For PNP, the polarity is reversed) Unit (resistance :  $\Omega$ , capacitance : F)







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