ZASYALUUS IN				T-27-	/5
COBK III					
2033	2003A	PNP Epitaxial P			
		General-I Switchin			
)334 1		Switchin	y Appi	Ications	
eatures					
. The 2SA608 is classified i The NP type is subclassifi . The 2SC536 is available a	ed into 2	subtypes accor	ding to th	e breakdown	voltage.
bsolute Maximum Ratings at Ta=2	5°C	2SA608SPA	2SA608NP	2SA608KNP	unit
Collector to Base Voltage	VCBO	-40	-40	-55	V
Collector to Emitter Voltage	-	-30	-30	-50	V
Emitter to Base Voltage	VEBO		-5 -100	-5 -100	V mA
Collector Current Peak Collector Current	IC icp	-100 300	300	300	ma i ma
Collector Dissipation	PC	300	400	400	mW
Junction Temperature	-c Tj	125	125	125	°C
Storage Temperature	Tstg	-55 to +125 -	55 to +125	-55 to +125	°C
lectrical Characteristics at Ta	=25°C		min	typ max	unit
Collector Cutoff Current	ICBO	$V_{CB}=-25V, I_{E}=0$		-1	uA
Emitter Cutoff Current	IEBO	$v_{EB}=-4V$, $I_C=0$		-1	uA
DC current Gain	hFE	$V_{CE} = -6V, I_C = -1m$)* 150 560*	
Gain-Bandwidth Product	fT	V _{CE} =-6V,I _C =-10 V _{CB} =-6V,f=1MHz		180 7	MHZ pF
Output Capacitance C-E Saturation Voltage	Cob VCE(cat)	$I_C = -50 \text{ mA}$, $I_B = -5$	mA	, -0.5	V
Turn-On Time	ton	VIN=-12V,VBB=+	3V,at	50	ns
		appointed circ	uit.	•	
Turn-Off Time	toff	VIN=+12V,VBB=-		210	ns
		appointed circ	uit.		
2SA608 is graded as follows by	h _{FE} at lm	·····	7	0	10
60 D 120 100 E 200	160 F 32	0 280 G 560		[<u>f</u> -1	⊢ 0
				₹220	<u>- 1007</u>
				7 220	
			3.3k		
		ç	3.3k		
		C			
				3.3k	
		-		3.3k 51	
		-		3.3k	
Case Outline 2033		Sw Case Outline	₩ ₩ ₩ ₩ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ¥ \$50 \$ ¥	3.3k 51	
(unit:mm)		- Sw	₩ ₩ ₩ ₩ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ¥ \$50 \$ ¥	3.3k 51	
	2	Sw Case Outline	₩ ₩ ₩ ₩ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ¥ \$50 \$ ¥	3.3k 51	
(unit:mm)		Sw Case Outline	₩ ₩ ₩ ₩ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ¥ \$50 \$ ¥	3.3k 51	
(unit:mm)		Sw Case Outline	₩ ₩ ₩ ₩ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ¥ \$50 \$ ¥	3.3k 51	
(unit:mm)		Sw Case Outline	₩ ₩ ₩ ₩ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ₩ \$50 \$ ₩ ¥ \$50 \$ ¥	3.3k 51	
(unit:mm)		Case Outline (unit:mm)	i i i i i i i i i i	3.3k	
(unit:mm)		Case Outline (unit:mm)	14.0	3.3k 51	
(unit:mm)		Case Outline (unit:mm)	14.0	0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0.44 9 0 0 0 0 0 0 0 0 0 0 0 0 0	
(unit:mm)		Sw Case Outline (unit:mm) JEDEC: TO- EIAJ : SC- SANYO: NP	$ \frac{1}{10000000000000000000000000000000000$	a.3k	rcuit
(unit:mm)	be discontin	Sw Case Outline (unit:mm) JEDEC: TO- EIAJ : SC- SANYO: NP ued soon. Use the	itching T 2003A 0.45 -0.5 -0.45 -	a.sk	rcuit
(unit:mm)	be discontin	Sw Case Outline (unit:mm) JEDEC: TO- EIAJ : SC- SANYO: NP ued soon. Use the	itching T 2003A 0.45 -0.5 -0.45 -	a.sk	rcuit

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SANYO SEMICONDUCTOR CORP 32E D 📰 7997076 0008715 7 🛤

T-9/-20CASE OUTLINES OF LEAD FORMED SMALL SIGNAL TRANSISTORS

All of Sanyo lead formed small signal transistor case outlines are illustrated below.All dimensions are in mm, and dimensions which are not followed by min. or max.

- are represented by typical values.
- •No marking is indicated.

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