

Silicon PNP Power Transistors

2SA740

**DESCRIPTION**

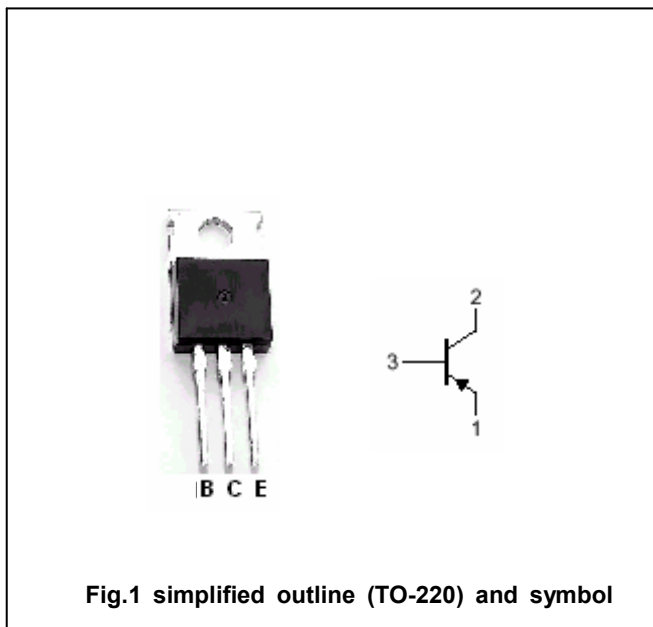
- With TO-220 package
- Complement to type 2SC1448

**APPLICATIONS**

- Power amplifier applications
- Vertical output applications

**PINNING**

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



**Absolute maximum ratings(Ta=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	-150	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	-150	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-5	V
I <sub>C</sub>	Collector current		-1.5	A
I <sub>E</sub>	Emitter current		1.5	A
P <sub>C</sub>	Collector power dissipation	T <sub>a</sub> =25°C	1.5	W
		T <sub>C</sub> =25°C	25	
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-10mA, I <sub>B</sub> =0	-150			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-500mA; I <sub>B</sub> =-50mA			-1.5	V
V <sub>BE</sub>	Base-emitter on voltage	I <sub>C</sub> =-500mA; V <sub>CE</sub> =-10V			-1.0	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-100V; I <sub>E</sub> =0			-20	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-5V; I <sub>C</sub> =0			-10	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-500mA; V <sub>CE</sub> =-10V	40		140	
C <sub>OB</sub>	Output capacitance	I <sub>E</sub> =0; V <sub>CB</sub> =-10V; f=1MHz		90		pF
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-500mA; V <sub>CE</sub> =-10V		8		MHz

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PACKAGE OUTLINE



Fig.2 Outline dimensions(unindicated tolerance:±0.10 mm)