



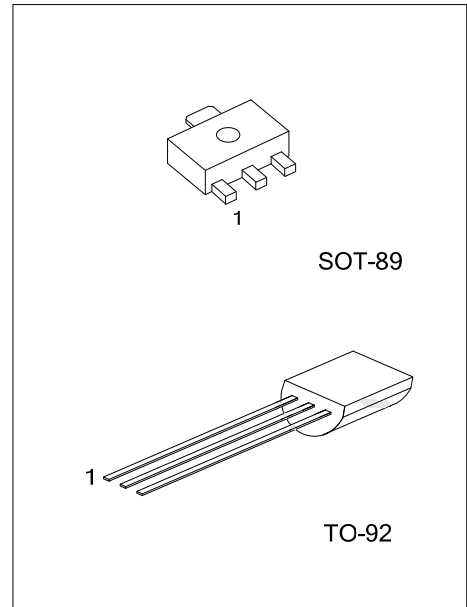
# 2SB1116/A

## PNP SILICON TRANSISTOR

### PNP EPITAXIAL SILICON TRANSISTOR

■ DESCRIPTION

Complement to UTC **2SD1616/A**



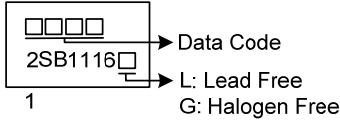
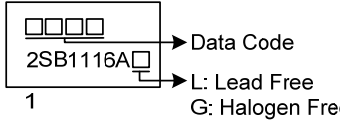
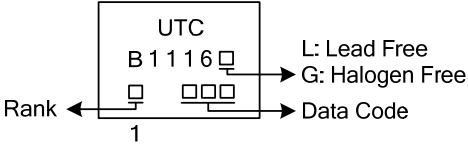
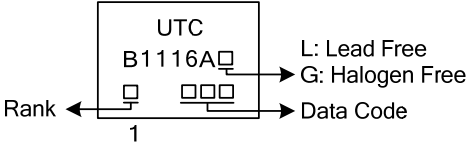
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
2SB1116L-x-AB3-B	2SB1116G-x-AB3-B	SOT-89	B	C	E	Tape Reel
2SB1116L-x-T92-B	2SB1116G-x-T92-B	TO-92	E	C	B	Tape Box
2SB1116L-x-T92-K	2SB1116G-x-T92-K	TO-92	E	C	B	Bulk
2SB1116AL-x-AB3-B	2SB1116AG-x-AB3-B	SOT-89	B	C	E	Tape Reel
2SB1116AL-x-T92-B	2SB1116AG-x-T92-B	TO-92	E	C	B	Tape Box
2SB1116AL-x-T92-K	2SB1116AG-x-T92-K	TO-92	E	C	B	Bulk
2SB1116AL-x-T92-C-B	2SB1116AG-x-T92-C-B	TO-92	E	B	C	Tape Box
2SB1116AL-x-T92-C-K	2SB1116AG-x-T92-C-K	TO-92	E	B	C	Bulk

Note: Pin Assignment: B: Base C: Collector E: Emitter

<p>2SB1116AG-x-T92-C-B</p>	<p>(1) B: Tape Box, K: Bulk, R: Tape Reel                  (2) refer to Pin Assignment                  (3) AB3: SOT-89, T92: TO-92                  (4) x: refer to Classification of <math>h_{FE1}</math>                  (5) G: Halogen Free and Lead Free, L: Lead Free</p>
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### MARKING

Package	2SB1116	2SB1116A
SOT-89	 <p>Diagram showing SOT-89 marking for 2SB1116. The marking includes a data code (three squares), the part number '2SB1116', and a lead-free/halogen-free symbol (square with 'L' and 'G'). The pin 1 location is indicated by a '1' below the symbol.</p>	 <p>Diagram showing SOT-89 marking for 2SB1116A. The marking includes a data code (three squares), the part number '2SB1116A', and a lead-free/halogen-free symbol (square with 'L' and 'G'). The pin 1 location is indicated by a '1' below the symbol.</p>
TO-92	 <p>Diagram showing TO-92 marking for 2SB1116. The marking includes 'UTC', 'B 1 1 1 6', a lead-free/halogen-free symbol (square with 'L' and 'G'), and a data code (three squares). The pin 1 location is indicated by a '1' below the symbol and an arrow labeled 'Rank' pointing to the left.</p>	 <p>Diagram showing TO-92 marking for 2SB1116A. The marking includes 'UTC', 'B 1 1 1 6 A', a lead-free/halogen-free symbol (square with 'L' and 'G'), and a data code (three squares). The pin 1 location is indicated by a '1' below the symbol and an arrow labeled 'Rank' pointing to the left.</p>

# 2SB1116/A

## PNP SILICON TRANSISTOR

### ■ ABSOLUTE MAXIMUM RATINGS ( $T_A=25^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector to Base Voltage	2SB1116	$V_{CBO}$	-60	V
	2SB1116A		-80	
Collector to Emitter Voltage	2SB1116	$V_{CEO}$	-50	V
	2SB1116A		-60	
Emitter to Base Voltage		$V_{EBO}$	-6	V
Collector Current	DC	$I_C$	-1	A
	Pulse(Note2)	$I_{CM}$	-2	A
Total Power Dissipation	SOT-89	$P_C$	500	mW
	TO-92		750	mW
Junction Temperature		$T_J$	+150	$^{\circ}\text{C}$
Operating Temperature		$T_{OPR}$	-20 ~ +85	$^{\circ}\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse width  $\leq 10\text{ms}$ , Duty cycle  $\leq 50\%$

### ■ ELECTRICAL CHARACTERISTICS ( $T_A=25^{\circ}\text{C}$ , unless otherwise specified)

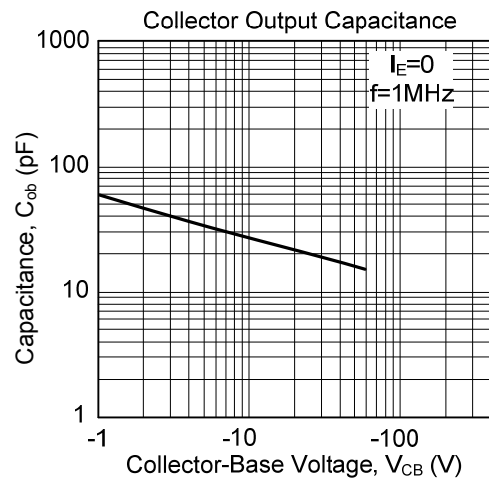
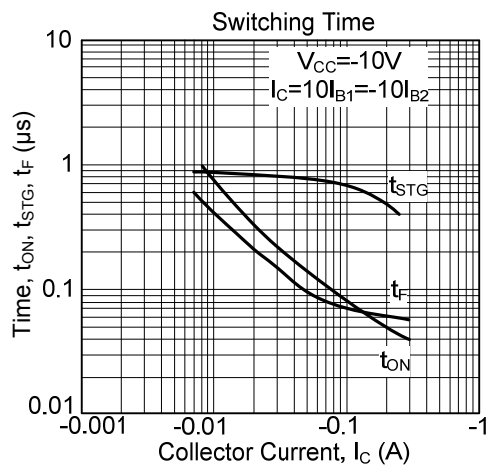
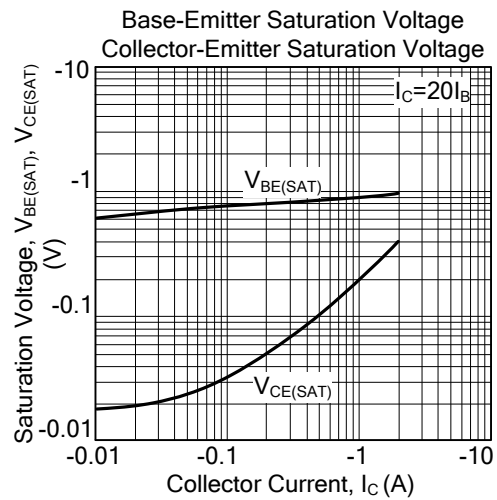
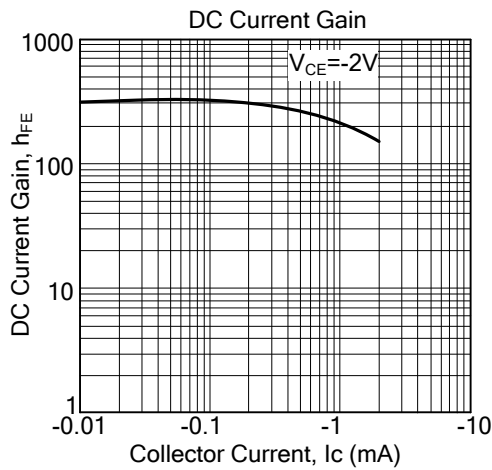
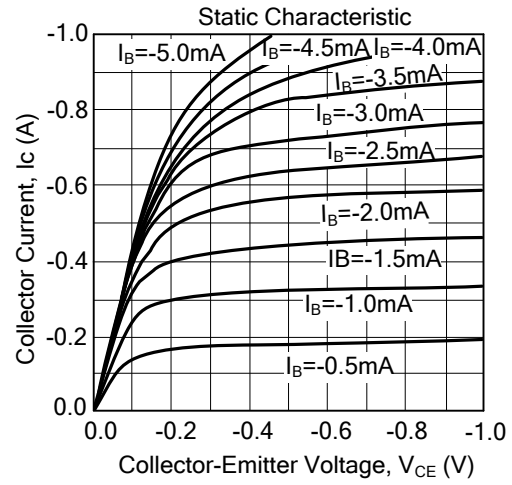
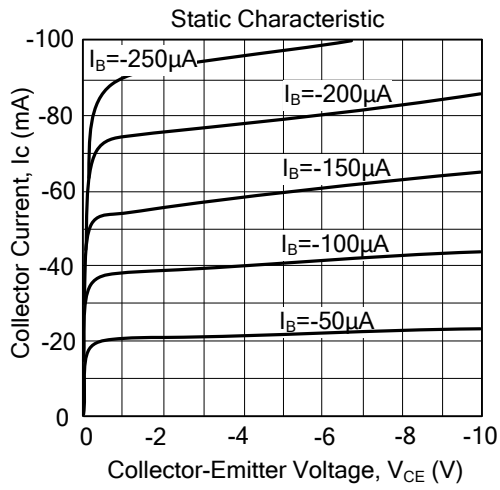
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Saturation Voltage(Note)	$V_{CE(SAT)}$	$I_C=-1\text{A}$ , $I_B=-50\text{mA}$		-0.2	-0.3	V
Base-Emitter Saturation Voltage(Note)	$V_{BE(SAT)}$	$I_C=-1\text{A}$ , $I_B=-50\text{mA}$		-0.9	-1.2	V
Base Emitter On Voltage(Note)	$V_{BE(ON)}$	$V_{CE}=-2\text{V}$ , $I_C=-50\text{mA}$	-600	-650	-700	mV
Collector Cut-Off Current	$I_{CBO}$	$V_{CB}=-60\text{V}$ , $I_E=0$			-100	nA
Emitter Cut-Off Current	$I_{EBO}$	$V_{EB}=-6\text{V}$ , $I_C=0$			-100	nA
DC Current Gain(Note)	$h_{FE1}$	$V_{CE}=-2\text{V}$ , $I_C=-100\text{mA}$	2SB1116	135	600	
			2SB1116A	135	400	
	$h_{FE2}$	$V_{CE}=-2\text{V}$ , $I_C=-1\text{A}$	81			
Transition Frequency	$f_T$	$V_{CE}=-2\text{V}$ , $I_C=-100\text{mA}$	70	120		MHz
Output Capacitance	$C_{OB}$	$V_{CB}=-10\text{V}$ , $I_E=0$ , $f=1\text{MHz}$		25		pF
Turn On Time	$t_{ON}$	$V_{CC}=-10\text{V}$ , $I_C=-100\text{mA}$ $I_{B1}=-I_{B2}=-10\text{mA}$ , $V_{BE(OFF)}=2 \sim 3\text{V}$		0.07		$\mu\text{s}$
Storage Time	$t_{STG}$			0.7		$\mu\text{s}$
Fall Time	$t_F$			0.07		$\mu\text{s}$

Note: Pulse Test: Pulse width  $\leq 350\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

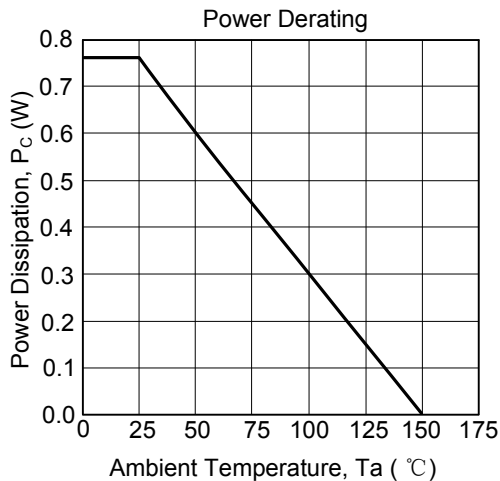
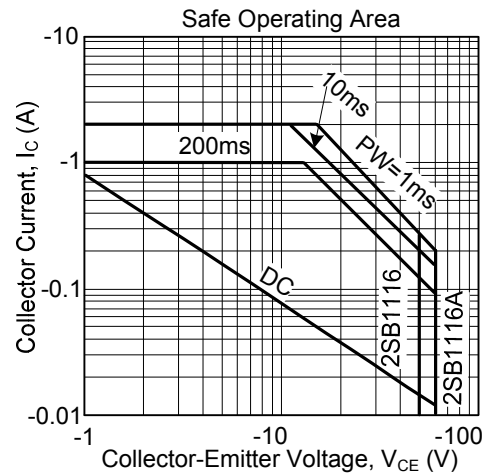
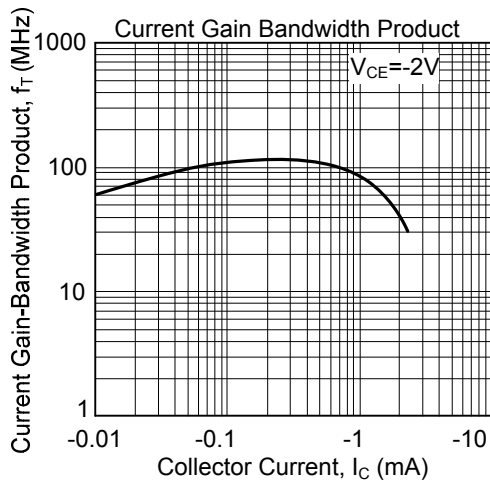
### ■ CLASSIFICATION OF $h_{FE1}$

RANK	Y	G	L
$h_{FE1}$	135 ~ 270	200 ~ 400	300 ~ 600

### TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)



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