

T-29-27

2029A

NPN Epitaxial Planar Silicon Composite Transistor

# Differential Amp Applications

©974B

## Applications

- . Differential amp, current mirror.

## Features

- . Excellent in thermal equilibrium and suited for use in first-stage differential amp.
- . Low noise.
- . Matched pair capability.

## Absolute Maximum Ratings at Ta=25°C

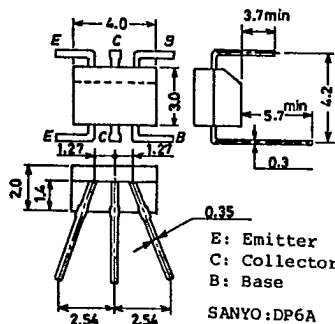
Parameter	Symbol	Value	unit
Collector to Base Voltage	V <sub>CB0</sub>	55	V
Collector to Emitter Voltage	V <sub>CE0</sub>	50	V
Emitter to Base Current	V <sub>EBO</sub>	5	V
Collector Current	I <sub>C</sub>	150	mA
Peak Collector Current	i <sub>cp</sub>	300	mA
Collector Dissipation	P <sub>C</sub>	200	mW
Total Dissipation	P <sub>T</sub>	400	mW
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

## Electrical Characteristics at Ta=25°C

Parameter	Symbol	Test Conditions	min	typ	max	unit
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =35V, I <sub>E</sub> =0			0.1	uA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =4V, I <sub>C</sub> =0			0.1	uA
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =6V, I <sub>C</sub> =1mA	100*		960*	
DC Current Gain Ratio	h <sub>FE</sub> (small/large)	V <sub>CE</sub> =6V, I <sub>C</sub> =1mA	0.85	0.98		
Base to Emitter Voltage Drop	V <sub>BE</sub> (large-small)	V <sub>CE</sub> =6V, I <sub>C</sub> =1mA		1.0	10	mV
Collector to Emitter Saturation Voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA			0.5	V
Gain-Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> =6V, I <sub>C</sub> =1mA		100		MHz
Output Capacitance	c <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz		2.5		pF
Collector to Base Breakdown Voltage	V(BR)CBO	I <sub>C</sub> =10uA, I <sub>E</sub> =0	55			V

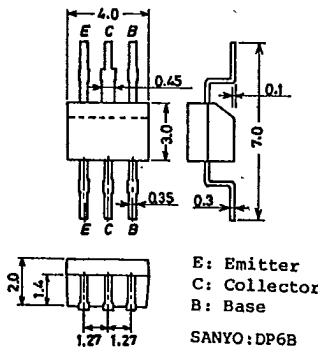
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Case Outline 2029A (unit:mm)



The 2SC3065 is provided with a surface mounted package.

Case Outline 2030A (unit:mm)

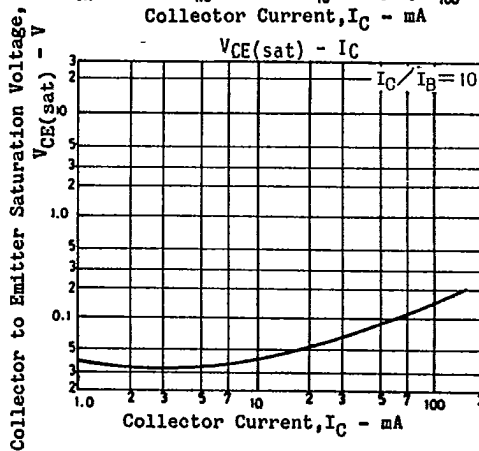
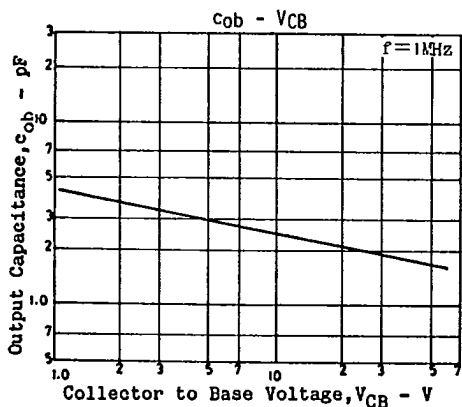
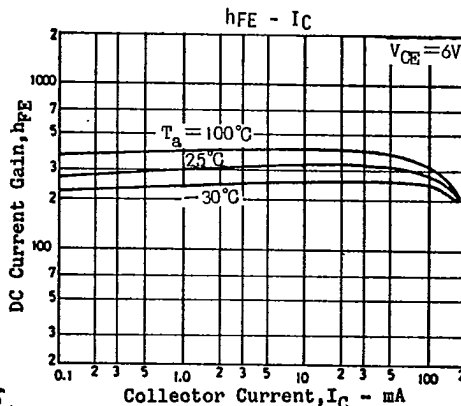
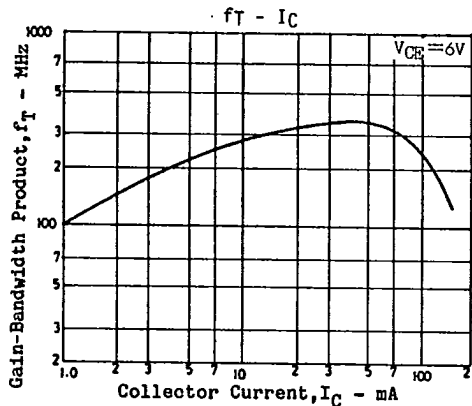
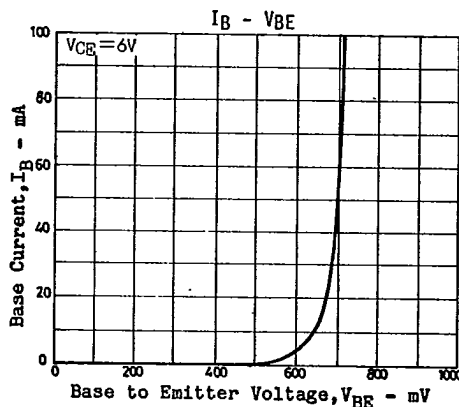
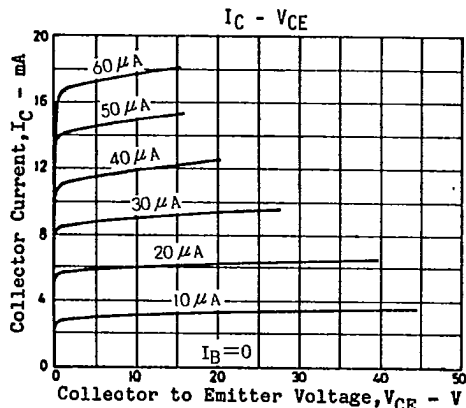


\*The 2SC3065 is classified by h<sub>FE</sub>(small) as follows:

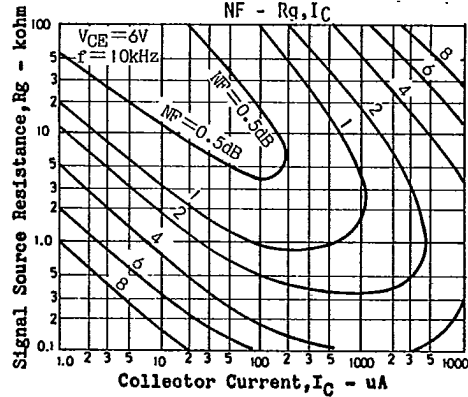
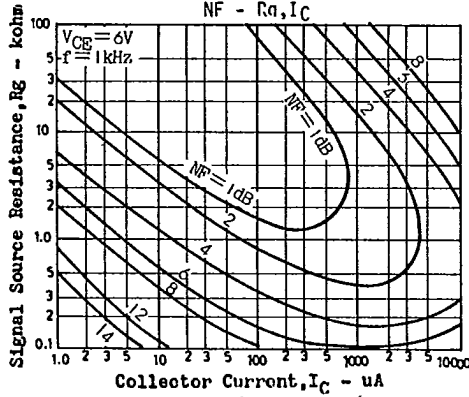
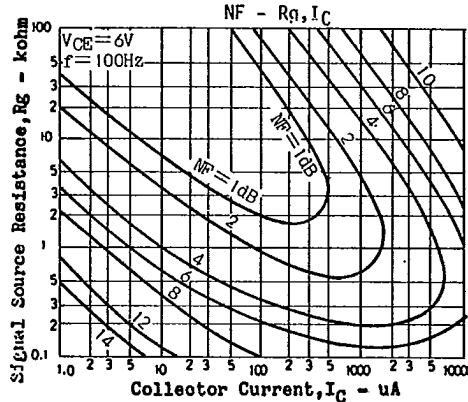
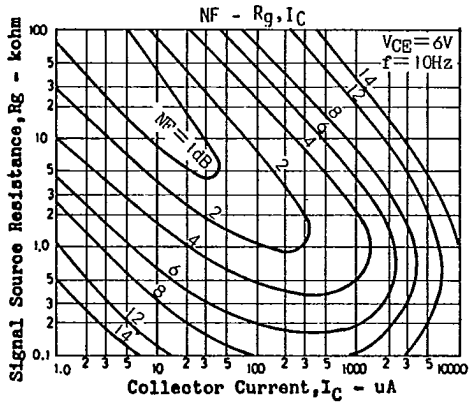
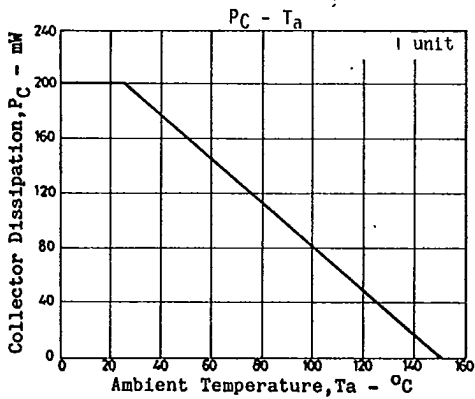
100	E	200
160	F	320
280	G	560
480	H	960

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			min	typ	max	unit
Collector to Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0$	55			V
Collector to Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	50			V
Emitter to Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	5			V
Noise Level	$V_{NO(ave)}$	$V_{CC}=30V, I_C=1mA, R_g=56k\Omega, V_G=77dB/1kHz$			35	mV
Noise Peak Level	$V_{NO(peak)}$	$V_{CC}=30V, I_C=1mA, R_g=56k\Omega, V_G=77dB/1kHz$			200	mV



T-29-27

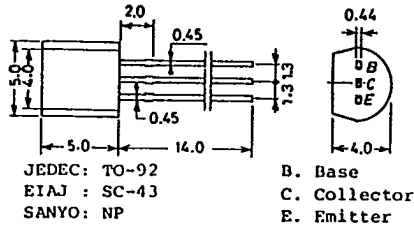


T-91-20

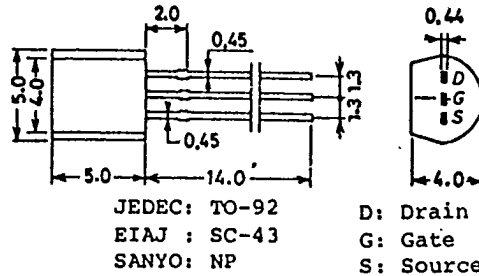
# CASE OUTLINES OF LEAD FORMED SMALL SIGNAL TRANSISTORS

- All of Sanyo lead formed small signal transistor case outlines are illustrated below.
- All dimensions are in mm, and dimensions which are not followed by min. or max. are represented by typical values.
- No marking is indicated.

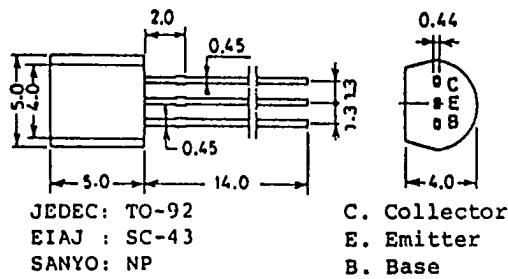
Case Outline-[2003A] unit: mm



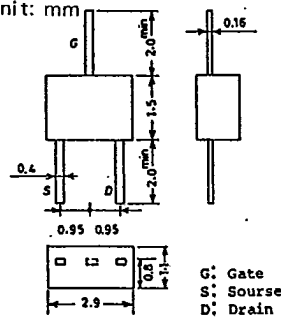
Case Outline-[2019A] unit: mm



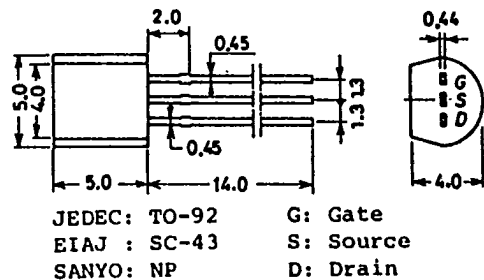
Case Outline-[2004A] unit: mm



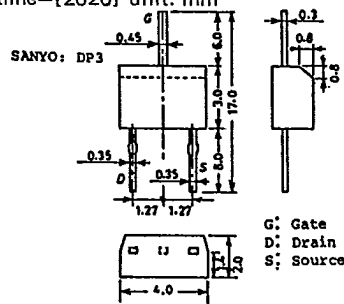
Case Outline-[2025] unit: mm



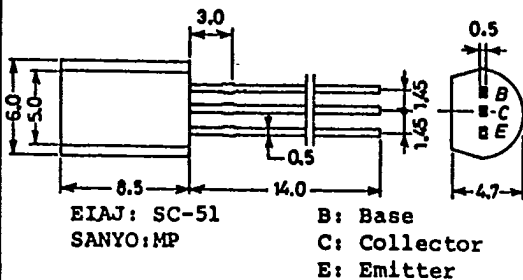
Case Outline-[2005A] unit: mm



Case Outline-[2026] unit: mm



Case Outline-[2006A] unit: mm



Case Outline-[2027A] unit: mm

