

Silicon NPN Power Transistors

2SC4131

DESCRIPTION

- With TO-3PML package
- Low collector saturation voltage

APPLICATIONS

- For chopper regulator, switch and general purpose applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

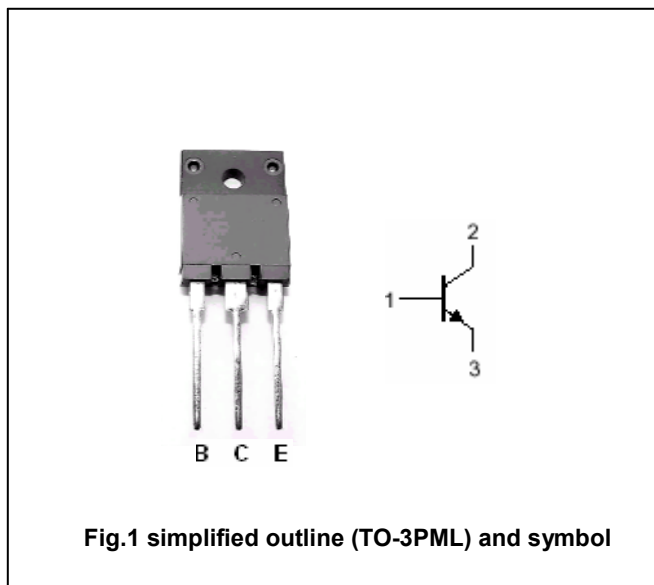


Fig.1 simplified outline (TO-3PML) and symbol

Absolute maximum ratings(Tc=25□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	100	V
V_{CEO}	Collector-emitter voltage	Open base	50	V
V_{EBO}	Emitter-base voltage	Open collector	15	V
I_C	Collector current		15	A
I_{CM}	Collector current-peak		25	A
I_B	Base current		4	A
P_C	Collector power dissipation	$T_c=25□$	60	W
T_j	Junction temperature		150	□
T_{stg}	Storage temperature		-55~150	□

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =25mA; I _B =0	50			V
V _{CE(sat)}	Collector-emitter saturation voltage	I _C =5 A; I _B =80m A			0.5	V
V _{BE(sat)}	Base-emitter saturation voltage	I _C =5 A; I _B =80m A			1.2	V
I _{CBO}	Collector cut-off current	V _{CB} =100V; I _E =0			10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =15V; I _C =0			10	μA
h _{FE}	DC current gain	I _C =5A ; V _{CE} =1V	50			
f _T	Transition frequency	I _C =1A ; V _{CE} =12V		18		MHz
C _{OB}	Output capacitance	I _E =0; V _{CB} =10V; f=1MHz		210		pF

Switching times

t _{on}	Turn-on time	I _C =5A; R _L =4Ω I _{B1} =-I _{B2} =80mA V _{CC} =20V		0.5		μs
t _s	Storage time			2.0		μs
t _f	Fall time			0.4		μs

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PACKAGE OUTLINE



Fig.2 Outline dimensions