TOSHIBA Transistor Silicon NPN Triple Diffused Type

# 2SC5549

High-Speed Switching Application for Inverter Lighting System

- Suitable for RCC circuits. (guaranteed small current hFE)
   hFE = 13 (min) (I<sub>C</sub> = 1 mA)
- High speed:  $t_r = 0.5 \mu s$  (max),  $t_f = 0.3 \mu s$  (max) ( $I_C = 0.24 A$ )
- High breakdown voltage: VCEO = 400 V

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	(Vinit
Collector-base voltage		V <sub>CBO</sub>	400	A
Collector-emitter voltage		V <sub>CEO</sub>	400	V
Emitter-base voltage		V <sub>EBO</sub>	7	> v
Collector current	DC	IC		Α
	Pulse	ICP	2	A
Base current		IB	0.5	//A
Collector power dissipation		PC	0.9	W
Junction temperature		Tj	)) 150	°C
Storage temperature range		Tstg	-55 to 150	∕ °C

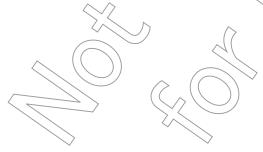
	Unit: mm
0.75MAX. 1.0MAX. 0.6MAX. 1.2. 1. EMITTER 2. COLLECT 3. BASE	4.1MAX.
JEDEC	TO-92MOD
JEITA	SC-65
TOSHIBA	2-5J1A

Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

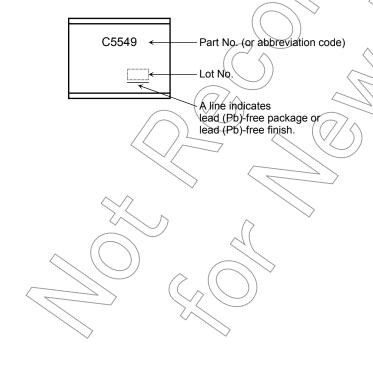
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



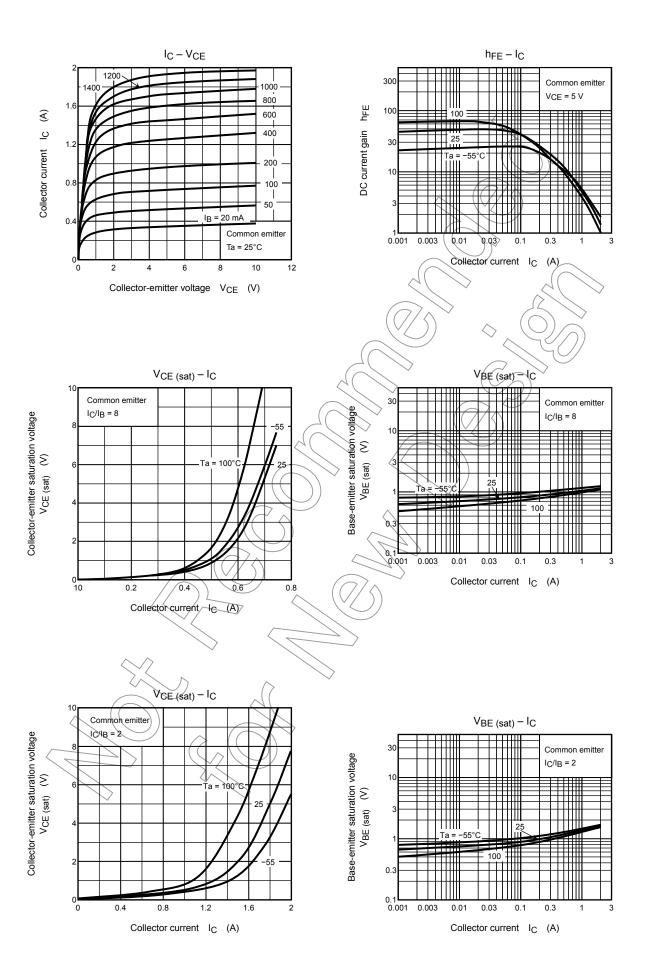
## Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off	current	I <sub>CBO</sub>	V <sub>CB</sub> = 320 V, I <sub>E</sub> = 0	_	_	100	μA
Emitter cut-off cu	rrent	I <sub>EBO</sub>	V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0	_	_	100	μΑ
Collector-base br	eakdown voltage	V (BR) CBO	I <sub>C</sub> = 1 mA, I <sub>E</sub> = 0	400	_	_	V
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	400	_	_	V
DC current gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 1 mA	13	) }_	_	
		h <sub>FE</sub> (2)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.04 A	20	_	65	
Collector-emitter saturation voltage V <sub>C</sub>		V <sub>CE (sat)</sub>	I <sub>C</sub> = 0.2 A, I <sub>B</sub> = 25 mA	$\bigcirc)$	_	1.0	V
Base-emitter saturation voltage V <sub>BE</sub> (sat)		V <sub>BE (sat)</sub>	I <sub>C</sub> = 0.2 A, I <sub>B</sub> = 25 mA	_	_	1.3	V
Switching time	Rise time	t <sub>r</sub>	V <sub>CC</sub> ≈ 200 V Q 20 μs	_		0.5	
	Storage time	t <sub>stg</sub>	Input B1 Output			5.0	μs
	Fall time	t <sub>f</sub>	I <sub>B1</sub> = 0.03 A, I <sub>B2</sub> = -0.06 A, Duty cycle ≤ 1%		_	0.3	

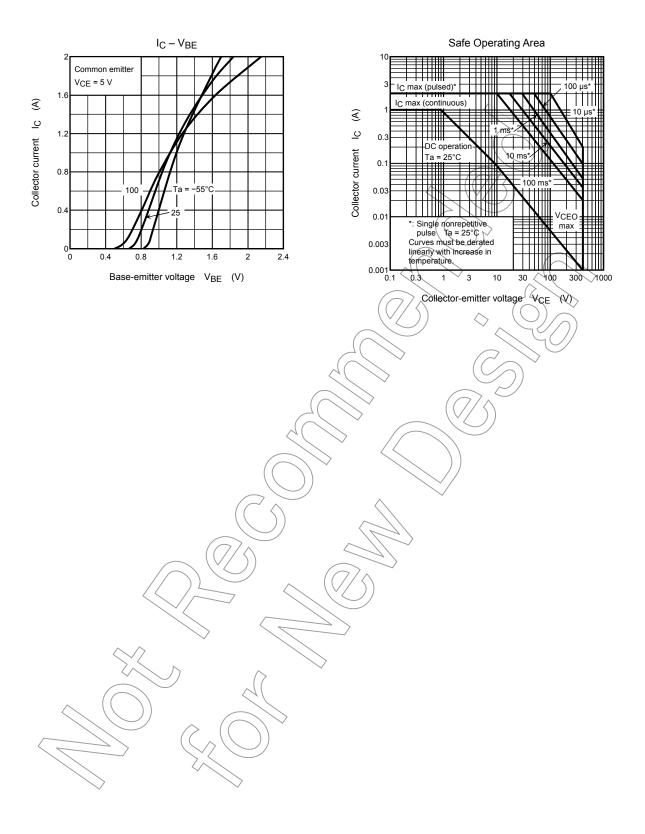
### Marking

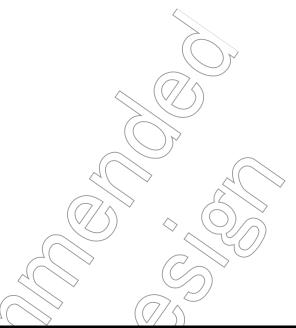


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