

isc Silicon NPN Power Transistor
2SC5586
DESCRIPTION

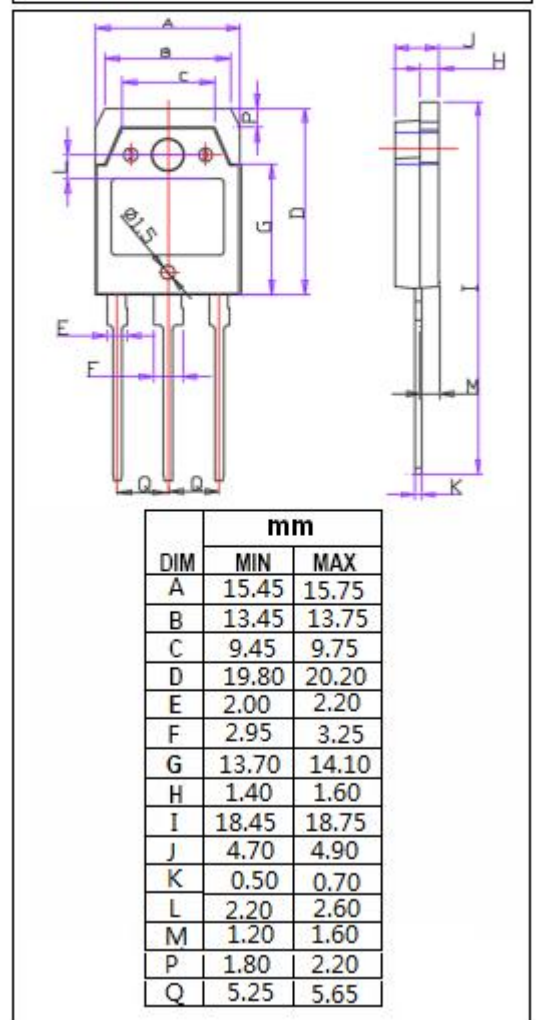
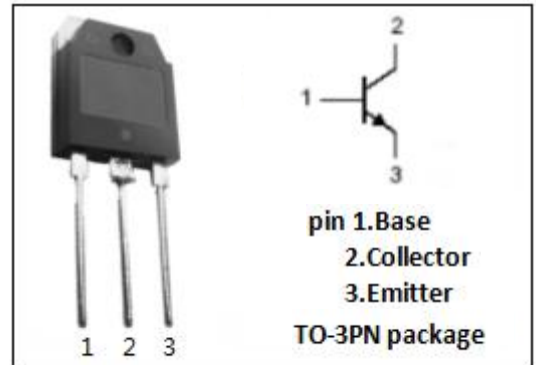
- High Collector-Base Voltage-
: $V_{CBO} = 900V(\text{Min})$
- High Switching Speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	900	V
V_{CEO}	Collector-Emitter Voltage	550	V
V_{EBO}	Emitter-Base Voltage	9	V
I_c	Collector Current-Continuous	5	A
P_c	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	70	W
T_j	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO}	Collector-Emitter Voltage	I _C = 30mA; I _B = 0	550			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1.8A; I _B =0.36A			0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 1.8A; I _B =0.36A			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 800V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 9V; I _C = 0			1	mA
h _{FE}	DC Current Gain	I _C = 1.8A; V _{CE} = 4V	10		40	

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