TOSHIBA Transistor Silicon NPN Epitaxial Type

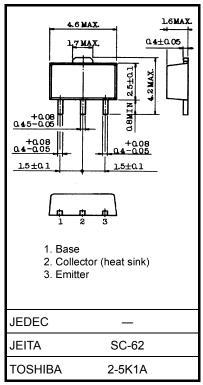
# 2SC5714

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain:  $h_{FE}$  = 400 to 1000 (I<sub>C</sub> = 0.5 A)
- Low collector-emitter saturation voltage: V<sub>CE (sat)</sub> = 0.15 V (max)
- High-speed switching: t<sub>f</sub> = 90 ns (typ.)

| Characteristics             |          | Symbol           | Rating     | Unit |  |
|-----------------------------|----------|------------------|------------|------|--|
| Collector-base voltage      |          | V <sub>CBO</sub> | 40         | V    |  |
| Collector-emitter voltage   |          | V <sub>CEX</sub> | 30         | V    |  |
| Collector-emitter voltage   |          | V <sub>CEO</sub> | 20         | V    |  |
| Emitter-base voltage        |          | V <sub>EBO</sub> | 7          | V    |  |
| Collector current           | DC       | IC               | 4          | А    |  |
|                             | Pulse    | I <sub>CP</sub>  | 7          |      |  |
| Base current                |          | Ι <sub>Β</sub>   | 400        | mA   |  |
| Collector power dissipation | DC       | PC               | 1.0        | w    |  |
|                             | t = 10 s | (Note 1)         | 2.5        |      |  |
| Junction temperature        |          | Тj               | 150        | °C   |  |
| Storage temperature range   |          | T <sub>stg</sub> | -55 to 150 | °C   |  |

#### Absolute Maximum Ratings (Ta = 25°C)



Weight: 0.05 g (typ.)

Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

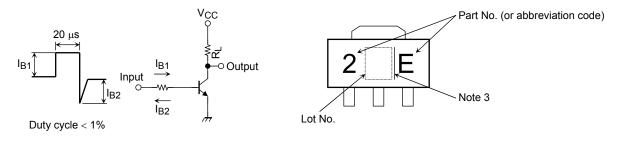
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit: mm

**Electrical Characteristics (Ta = 25°C)** 

| Characteristics                      |              | Symbol                | Test Condition                                      | Min | Тур. | Max  | Unit |
|--------------------------------------|--------------|-----------------------|---|-----|------|------|------|
| Collector cut-off current            |              | I <sub>CBO</sub>      | $V_{CB}=40~V,~I_{E}=0$                              |     |      | 100  | nA   |
| Emitter cut-off current              |              | I <sub>EBO</sub>      | $V_{EB} = 7 V, I_{C} = 0$                           | _   |      | 100  | nA   |
| Collector-emitter breakdown voltage  |              | V (BR) CEO            | $I_C = 10 \text{ mA}, I_B = 0$                      | 20  | _    | _    | V    |
| DC current gain                      |              | h <sub>FE</sub> (1)   | $V_{CE} = 2 V, I_C = 0.5 A$                         | 400 |      | 1000 |      |
|                                      |              | h <sub>FE</sub> (2)   | $V_{CE} = 2 V, I_C = 1.6 A$                         | 200 |      | _    |      |
| Collector-emitter saturation voltage |              | V <sub>CE (sat)</sub> | I <sub>C</sub> = 1.6 A, I <sub>B</sub> = 32 mA      | _   |      | 0.15 | V    |
| Base-emitter saturation voltage      |              | V <sub>BE (sat)</sub> | I <sub>C</sub> = 1.6 A, I <sub>B</sub> = 32 mA      | _   |      | 1.10 | V    |
| Collector output capacitance         |              | C <sub>ob</sub>       | $V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ | _   | 18   | _    | pF   |
| Switching time                       | Rise time    | tr                    | See Figure 1 circuit diagram.                       |     | 100  | _    | ns   |
|                                      | Storage time | t <sub>stg</sub>      | $V_{CC}\approx 12~V,~R_L=7.5~\Omega$                |     | 350  | _    |      |
|                                      | Fall time    | t <sub>f</sub>        | I <sub>B1</sub> = 53.3 mA,I <sub>B2</sub> = 53.3 mA |     | 90   | _    |      |

## Marking

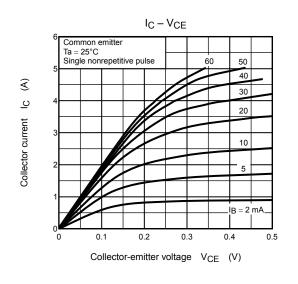


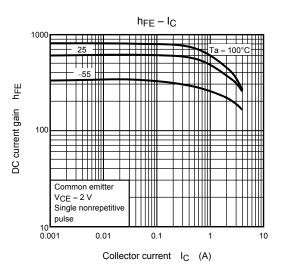
#### Figure 1 Switching Time Test Circuit & Timing Chart

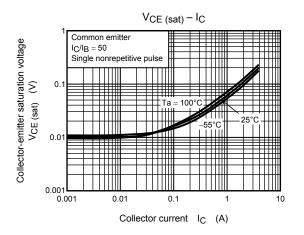
Note 3: A line beside a Lot No. identifies the indication of product Labels. Without a line: [[Pb]]/INCLUDES > MCV With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

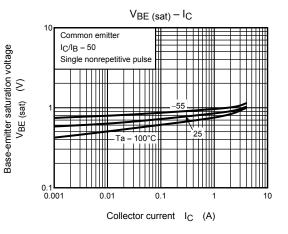
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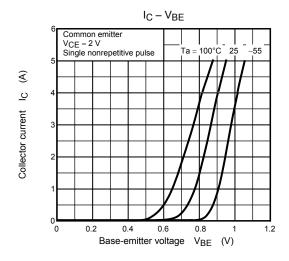
# **TOSHIBA**

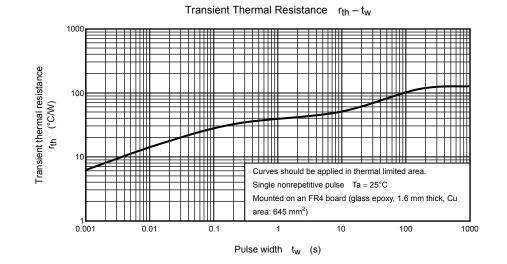


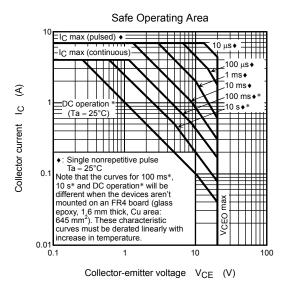












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