

Silicon NPN Power Transistors

2SD1264 2SD1264A

DESCRIPTION

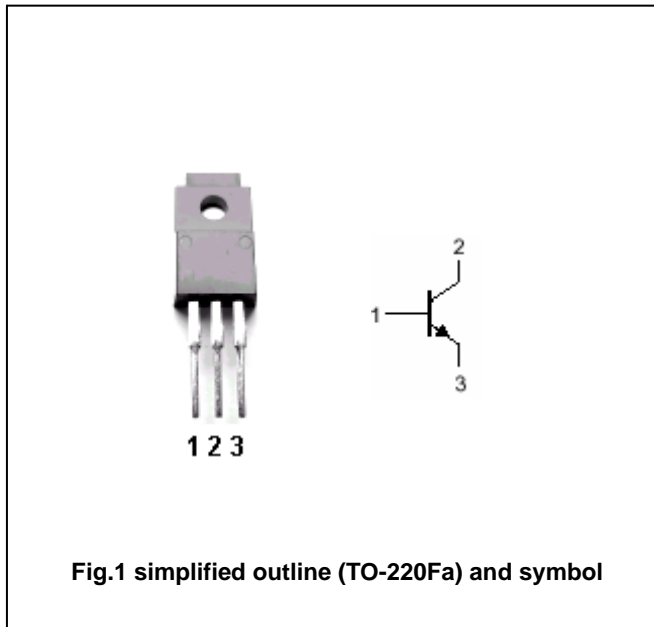
- With TO-220Fa package
- Complement to type 2SB940/940A
- High collector to emitter voltage V_{CEO}
- Large collector power dissipation P_C

APPLICATIONS

- For power amplification
- For TV vertical deflection output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	200	V
V_{CEO}	Collector-emitter voltage	2SD1264	150	V
		2SD1264A	180	
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		2	A
I_{CM}	Collector current-peak		3	A
P_C	Collector power dissipation	$T_a=25^\circ\text{C}$	2	W
		$T_C=25^\circ\text{C}$	30	
T_j	Junction temperature		150	°C
T_{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT	
V _{CEO}	Collector-emitter voltage	2SD1264	I _C =5mA, I _B =0	150			V
		2SD1264A		180			
V _{CBO}	Collector-base voltage	I _C =50 μA, I _E =0	200			V	
V _{EBO}	Emitter-base voltage	I _C =500 μA, I _C =0	6			V	
V _{CEsat}	Collector-emitter saturation voltage	I _C =0.5A, I _B =50mA			1.0	V	
V _{BE}	Base-emitter voltage	I _C =0.4A; V _{CE} =10V			1.0	V	
I _{EBO}	Emitter cut-off current	V _{EB} =4V; I _C =0			50	μA	
I _{CBO}	Collector cut-off current	V _{CB} =200V; I _E =0			50	μA	
h _{FE-1}	DC current gain	I _C =0.15A; V _{CE} =10V	60		240		
h _{FE-2}	DC current gain	I _C =0.4A; V _{CE} =10V	50				
f _T	Transition frequency	I _C =0.5A; V _{CE} =10V, f=10MHz		20		MHz	

◆ h_{FE-1} Classifications

Q	P
60-140	100-240

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PACKAGE OUTLINE

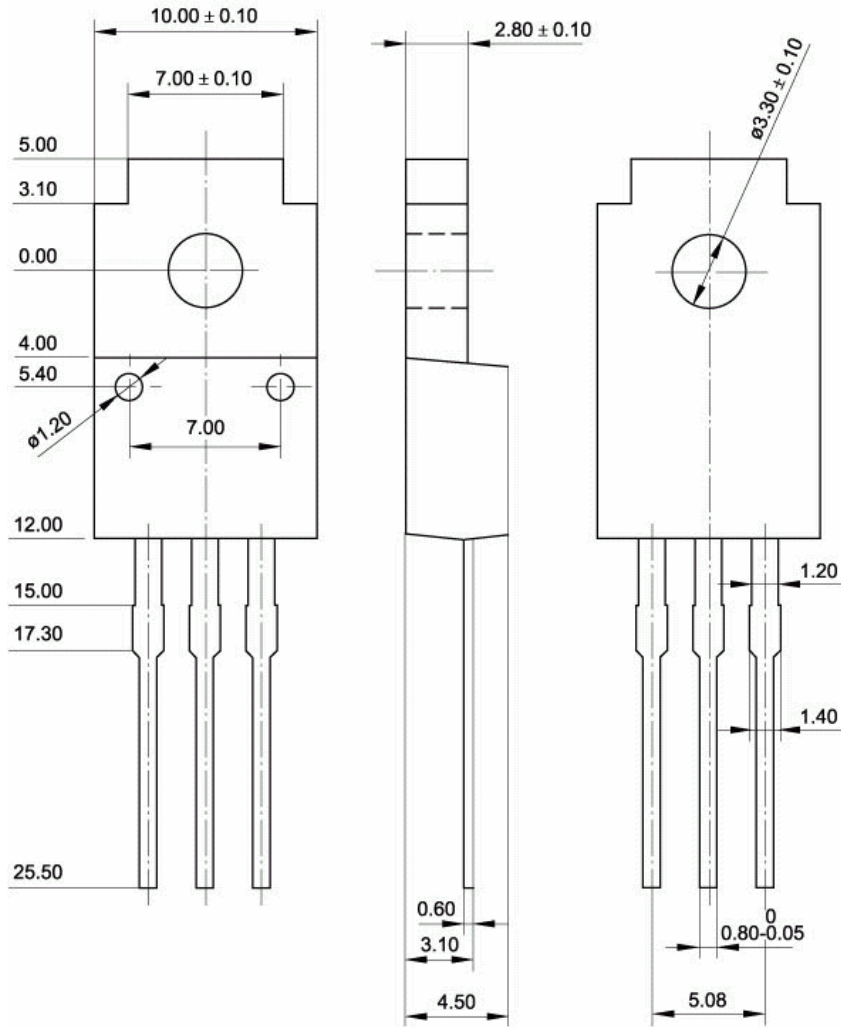


Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)

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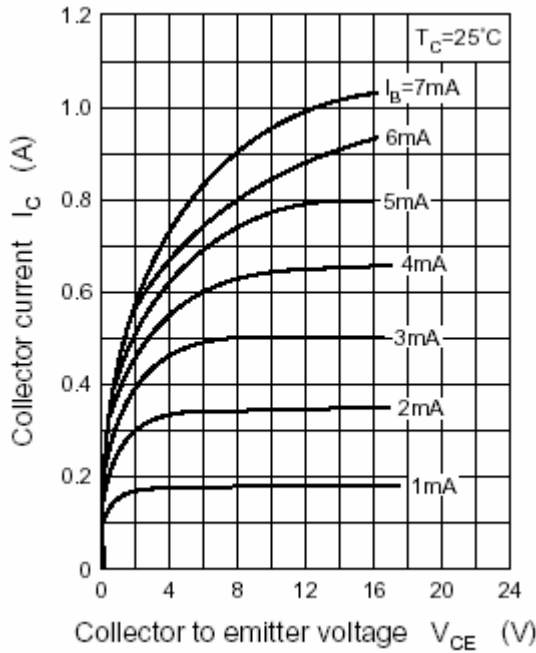


Fig.3 Static Characteristic

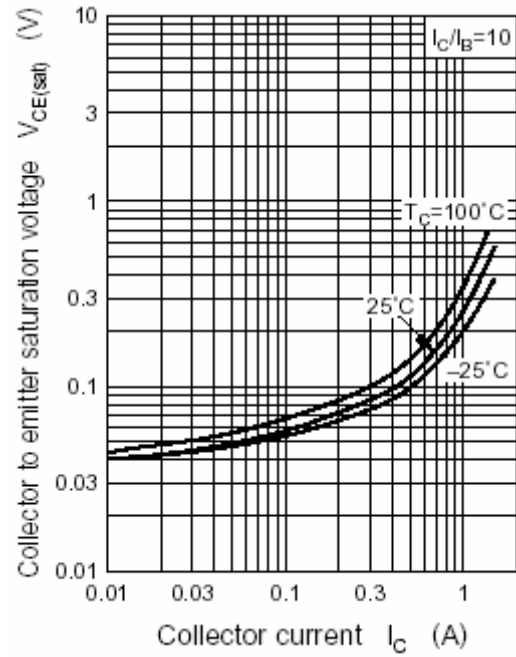


Fig.4 Collector-Emitter Saturation Voltage

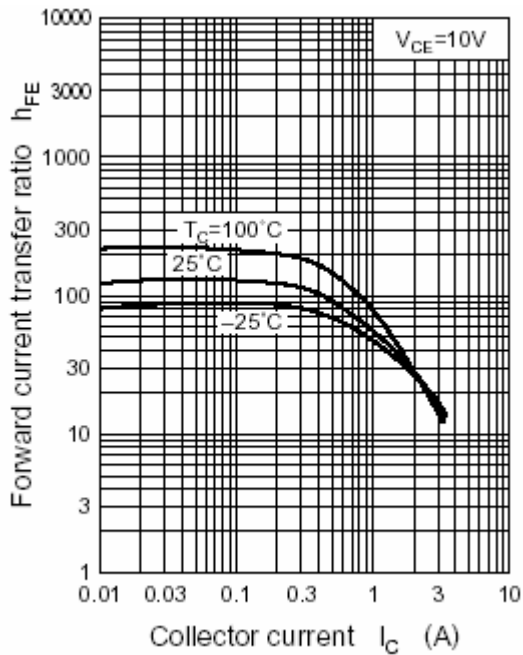


Fig.5 DC current Gain

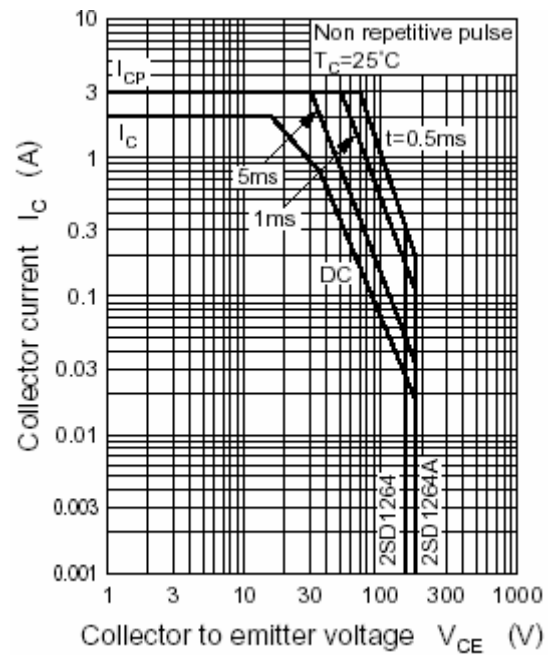


Fig.6 Safe Operating Area