

● Applications

- Strobe DC-DC converters, relay drivers, hammer drivers, lamp drivers, motor drovers

● Features

- Low saturation voltage
- Large current capacity
- Very small size making it easy to provide highdensity, small-sized hybrid IC's
- Halogen free compliance
- Highh FE

● Specifications

Absolute Maximum Ratings at Ta=25°C

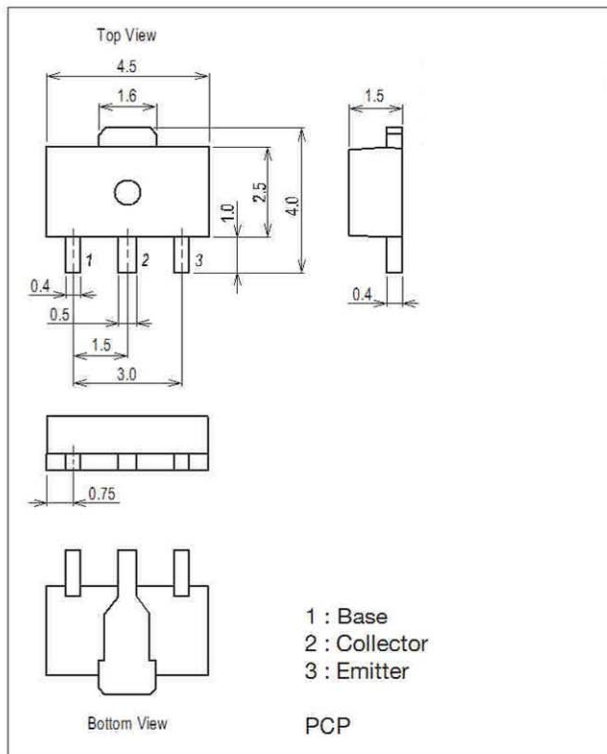
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCEO		20	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		5	A
Collector Current (Pulse)	ICP		8	A

Continued on next page.

● Package Dimensions

unit: mm (typ)

7007B-004



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Parameter	Symbol	Conditions	Ratings	Unit
Collector Dissipation	PC		500	mW
		When mounted on ceramic substrate (250mm ² ×0.8mm)	1.5	W
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

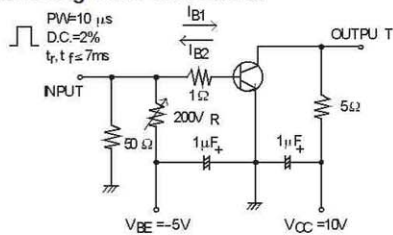
- Electrical Characteristics at T_a=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CBO}	V _{CB} =50V, I _E =0A			100	nA
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0A			100	nA
DC Current Gain	h _{FE1}	V _{CE} =2V, I _C =0.5A	120*		560*	
	h _{FE2}	V _{CE} =2V, I _C =3A	95			
Gain-Bandwidth Product	f _T	V _{CE} =10V, I _C =50mA		120		MHz
Output Capacitance	C _{ob}	V _{CB} =10V, f=1MHz		45		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =3A, I _B =60mA			500	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =3A, I _B =60mA			1.5	V
Turn-ON Time	t _{on}	See specified Test Circuit.		30		ns
Storage Time	t _{stg}			300		ns
Fall Time	t _f			40		ns

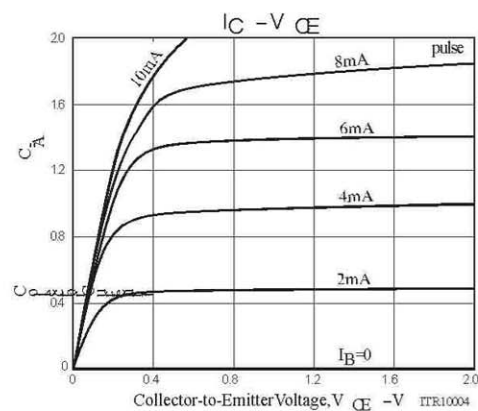
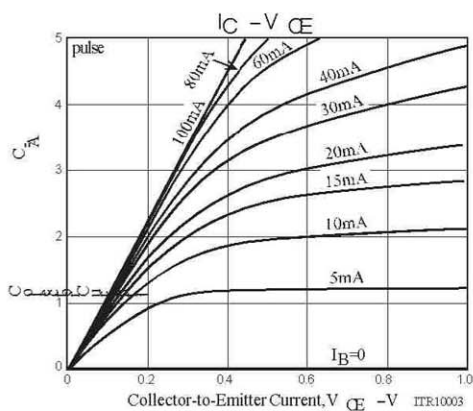
- * : The 2SD1628 is classified by 0.5A h_{FE} as follows:

Rank	E	F	G
h _{FE}	120 to 200	160 to 320	280 to 560

Switching Time Test Circuit



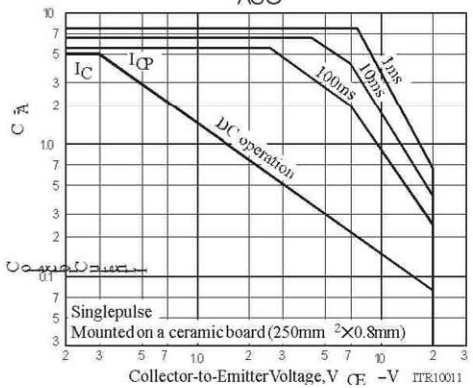
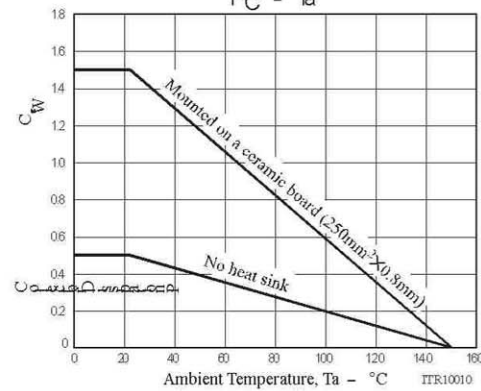
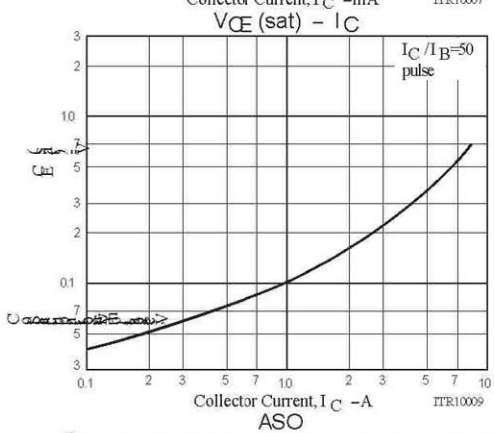
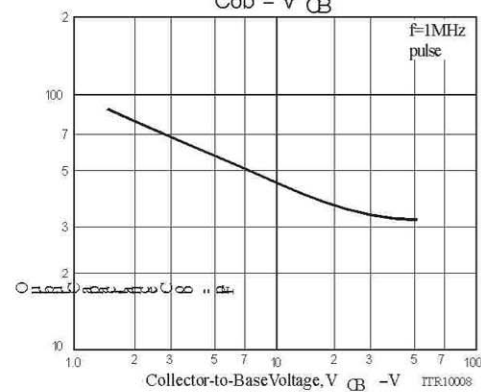
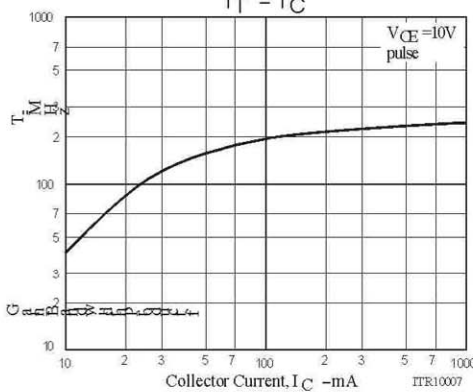
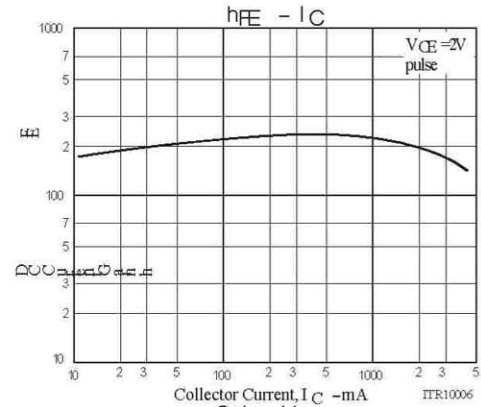
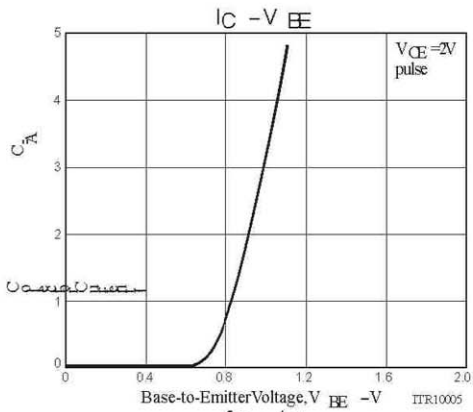
$$I_C = 10 I_{B1} = -10 I_{B2} = 2A$$



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