TOSHIBA Transistor Silicon NPN Triple Diffused Type (Darlington)

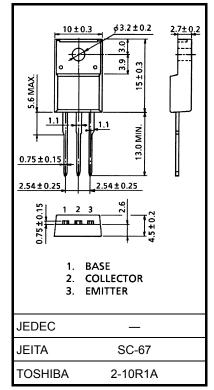
2SD2604

High-Power Switching Applications Hammer Drive, Pulse Motor Drive Applications

- High DC current gain: hFE = 2000 (min)
- Low saturation voltage: V_{CE} (sat) = 1.5 V (max)

Absolute Maximum Ratings (Tc = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	95	V	
Collector-emitter voltage		V _{CEO}	110 ± 15	V	
Emitter-base voltage		V _{EBO}	5	V	
Collector current	DC	Ι _C	5	A	
	Pulse	I _{CP}	10		
Base current		Ι _Β	0.7	А	
Collector power dissipation	Ta = 25°C	Pc	2.0	W	
	Tc = 25°C	ГC	20		
Junction temperature		Тј	150	°C	
Storage temperature range		T _{stg}	−55 to 150	°C	



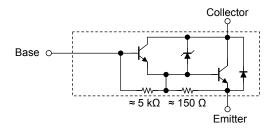
Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Equivalent Circuit

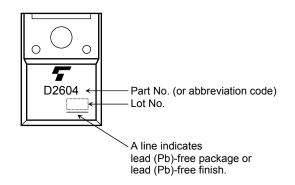


Unit: mm

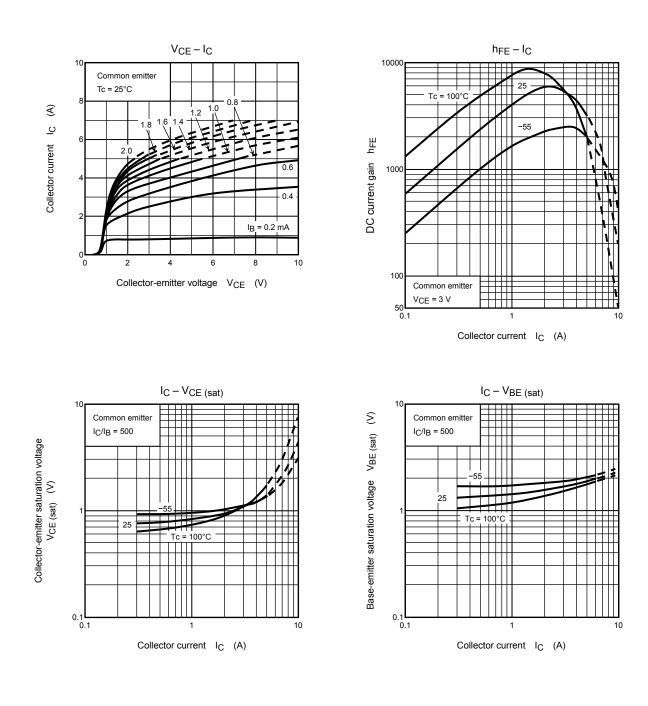
Electrical Characteristics (Tc = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off c	urrent	I _{CBO}	V _{CB} = 90 V, I _E = 0	_	_	100	μA
Emitter cut-off current		I _{EBO}	V _{EB} = 6 V, I _C = 0	0.75	_	3.0	μA
Collector-emitter	breakdown voltage	V (BR) CEO	I _C = 10 mA, I _B = 0	95	110	125	V
DC current gain		h _{FE (1)}	V _{CE} = 3 V, I _C = 2 A	2000	—	15000	
		h _{FE (2)}	V _{CE} = 3 V, I _C = 5 A	1000	—	_	
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = 2 A, I _B = 4 mA	_	0.9	1.5	V
Base-emitter saturation voltage		V _{BE (sat)}	I _C = 2 A, I _B = 4 mA	-	1.5	2.5	V
Switching time	Turn-on time	t _{on}	Output Input $IB1$ 1B1 1B1 1B1 1B2 1B2 $V_{CC} \approx 40$ $V_{CC} \approx 40$ $V_{CC} \approx 40$ $V_{CC} \approx 40$ IB1 IB1 IB1 IB1 IB1 IB1 $V_{CC} \approx 40$ V IB1 IB1 IB1 IB1 $V_{CC} \approx 40$ V IB1 IB1 IB1 $V_{CC} \approx 40$ V IB1 IB1 IB1 IB1 IB1 $V_{CC} \approx 40$ V IB1 IB1 IB1 IB1 $V_{CC} \approx 40$ V IB1 I	-	0.5	_	
	Storage time	t _{stg}			5.0	_	μs
	Fall time	t _f		_	0.7	_	

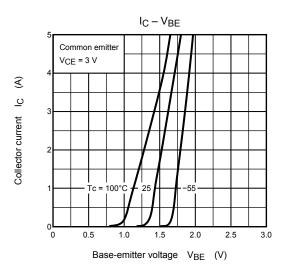
Marking

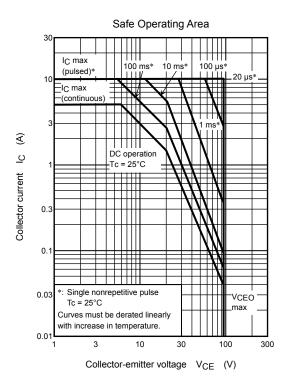


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