<u>TOSHIBA</u>

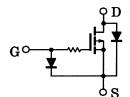
TOSHIBA Field Effect Transistor Silicon P Channel MOS Type

2SJ342

High Speed Switching Applications Analog Switch Applications

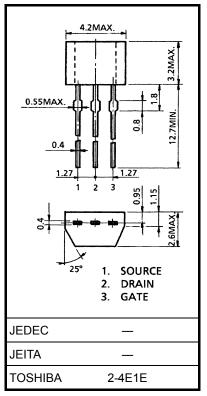
- Low threshold voltage: $V_{th} = -0.8 \sim -2.5 \text{ V}$
- High speed
- Enhancement-mode
- Small package
- Complementary to 2SK1825

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V _{DS}	-50	V
Gate-source voltage	V _{GSS}	-7	V
DC drain current	۱ _D	-50	mA
Drain power dissipation	PD	300	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55~150	°C



Weight: 0.13 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

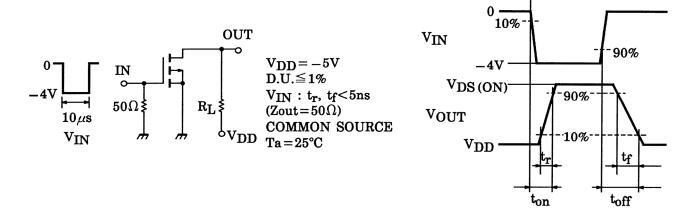
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = -7 V, V_{DS} = 0$		_	-1	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = -100 \ \mu A, \ V_{GS} = 0$	-50		_	V
Drain cut-off curre	nt	IDSS	$V_{DS} = -50 \text{ V}, V_{GS} = 0$			-1	μA
Gate threshould v	oltage	V _{th}	$V_{DS} = -5 \text{ V}, \text{ I}_{D} = -0.1 \text{ mA}$	-0.8		-2.5	V
Forward transfer admittance		Y _{fs}	$V_{DS} = -5 \text{ V}, \text{ I}_{D} = -10 \text{ mA}$	15		_	mS
Drain-source ON resistance		R _{DS (ON)}	$I_D = -10$ mA, $V_{GS} = -4$ V		20	50	Ω
Input capacitance		C _{iss}	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$		10.5	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$		1.9	_	pF
Output capacitance		C _{oss}	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$		7.2	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = -5 \text{ V}, \text{ I}_{D} = -10 \text{ mA}, V_{GS} = 0 \text{~} -4 \text{ V}$		0.15		
	Turn-off time	t _{off}			0.13	_	μS

Unit: mm

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Switching Time Test Circuit



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3L⊥ -0.5

-1

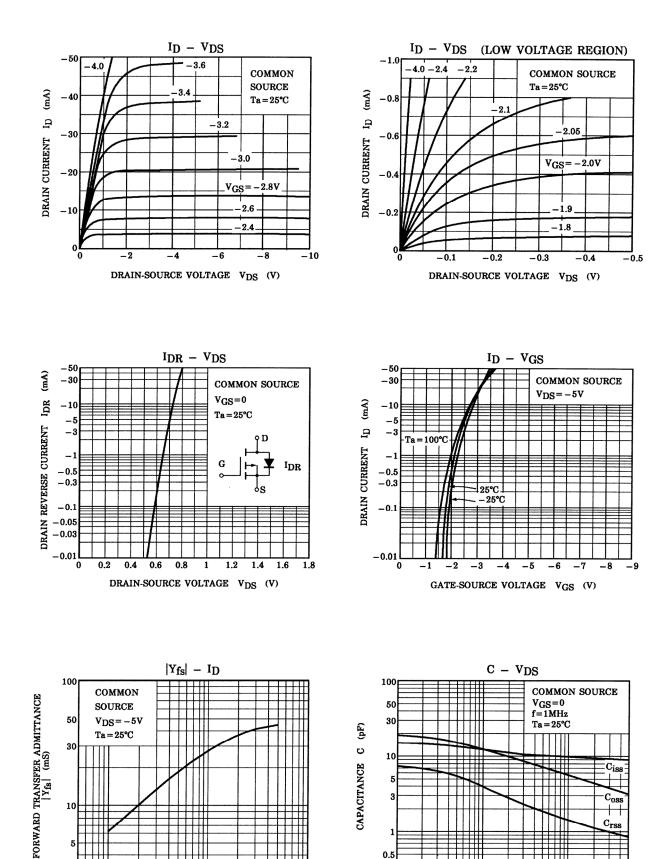
-3 -5

-10

DRAIN CURRENT ID (mA)

-30 -50

-100



-30 - 50

0.5 0.3 -0.1

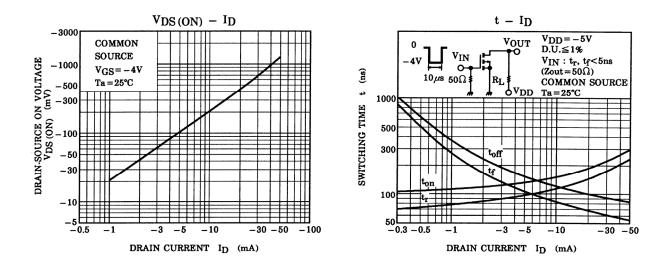
-1

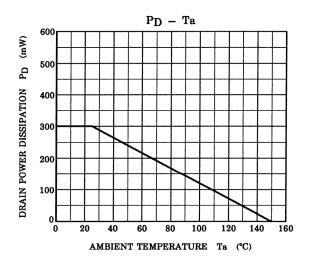
-3

DRAIN-SOURCE VOLTAGE VDS (V)

-5 -10

-0.3-0.5





RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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