# 2SJ483

# Silicon P Channel MOS FET High Speed Power Switching

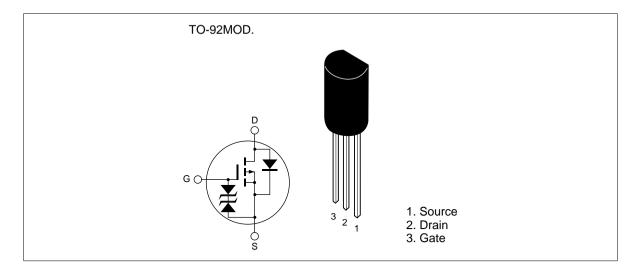
# **HITACHI**

ADE-208-519 1st. Edition

### **Features**

- Low on-resistance  $R_{\rm DS(on)} = 0.08\Omega \ typ \ (at \ V_{\rm GS} = -10 \ V, \ I_D = -2.5 \ A)$
- 4V gate drive devices.
- Large current capacitance  $I_D = -5 A$

### Outline





## 2SJ483

## **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{ exttt{DSS}}$	-30	V
Gate to source voltage	$V_{\sf GSS}$	±20	V
Drain current	I <sub>D</sub>	<b>–</b> 5	A
Drain peak current	Note1 D(pulse)	-20	A
Body to drain diode reverse drain current	I <sub>DR</sub>	<b>–</b> 5	A
Channel dissipation	Pch	0.9	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

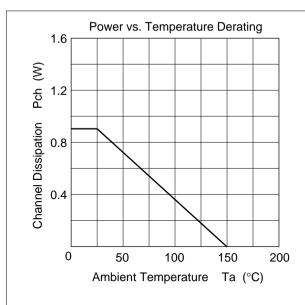
Note: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

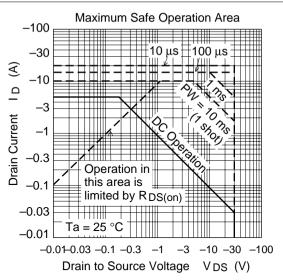
## **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

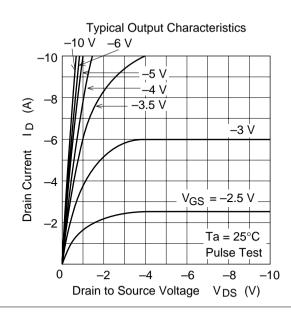
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_{D} = -10 \text{mA}, \ V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	-10	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{\rm GS(off)}$	-1.0	_	-2.0	V	$I_{D} = -1 \text{mA}, V_{DS} = -10 \text{V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	0.08	0.11	Ω	$I_D = -2.5A$ $V_{GS} = -10V^{*1}$
	R <sub>DS(on)</sub>	_	0.12	0.17	Ω	$I_{D} = -2.5A$ $V_{GS} = -4V^{*1}$
Forward transfer admittance	y <sub>fs</sub>	3	5	_	S	$I_D = -2.5A,$ $V_{DS} = -10V^{*1}$
Input capacitance	Ciss	_	630	_	pF	$V_{DS} = -10V$
Output capacitance	Coss	_	390	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	135	_	pF	f = 1MHz
Turn-on delay time	$t_{d(on)}$	_	15	_	ns	$V_{GS} = -10V, I_{D} = -2.5A$
Rise time	t <sub>r</sub>	_	70	_	ns	$R_L = 4\Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	65	_	ns	<del></del>
Fall time	t <sub>f</sub>	_	60	_	ns	_
Body to drain diode forward voltage	$V_{DF}$	_	-1.0	_	V	$I_{D} = -5A, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	60	_	ns	$I_F = -5A$ , $V_{GS} = 0$ diF/ dt = 20A/ $\mu$ s

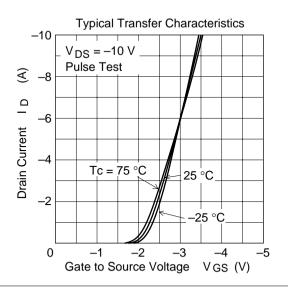
Note: 1. Pulse test

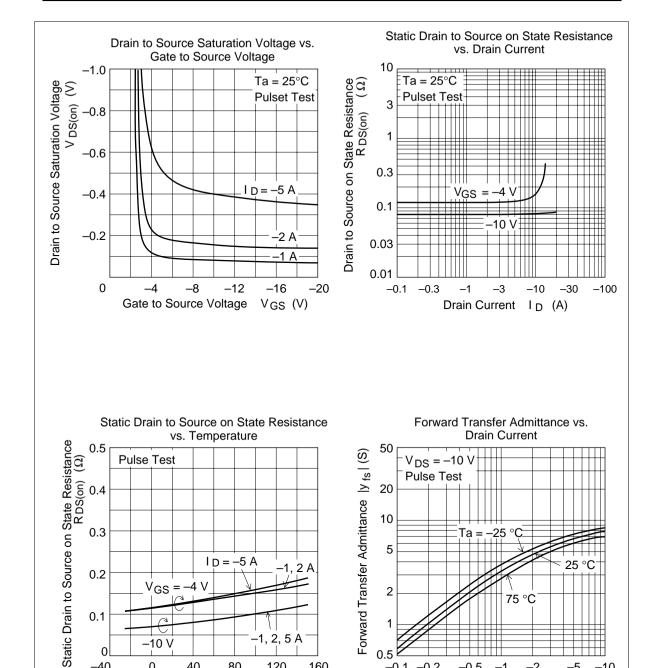
### **Main Characteristics**











–1, 2, 5 Å

120

Tc (°C)

160

0.1

0 -40 -10 V

40

Case Temperature

80

0

2

-0.1 -0.2

-0.5

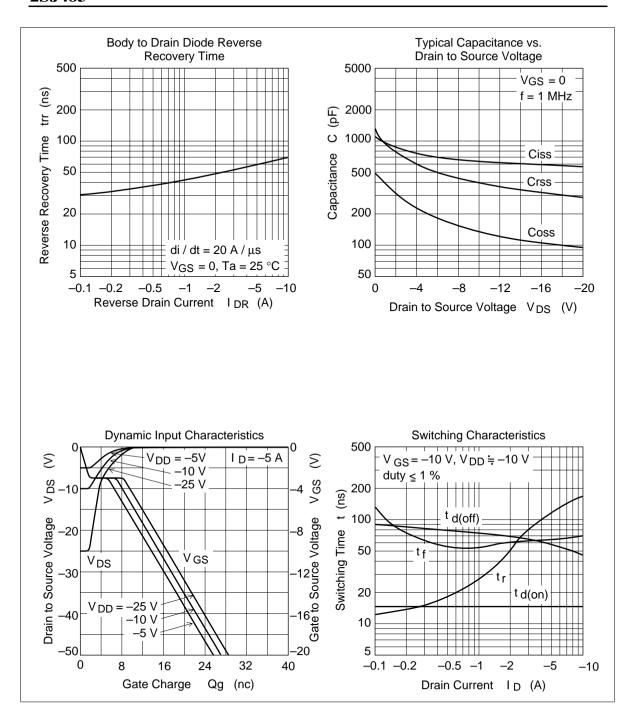
Drain Current ID (A)

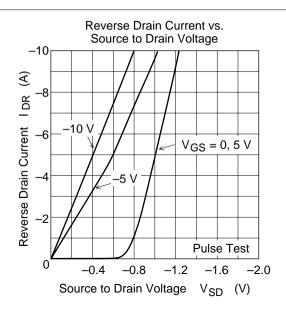
75 °C

-2

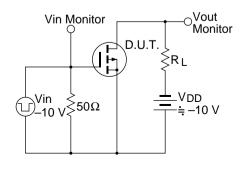
-5

-10

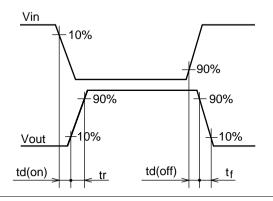




### Switching Time Test Circuit

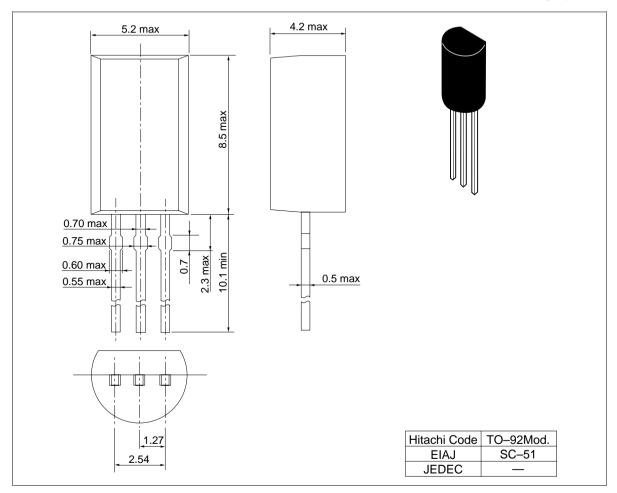


#### Switching Time Waveforms



## **Package Dimensions**

Unit: mm



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