

TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (π -MOSV)

2SJ567

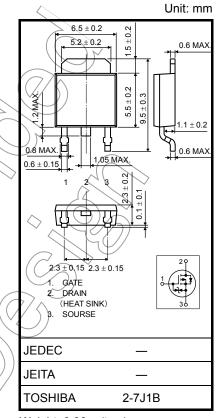
Switching Applications

Chopper Regulator, DC/DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: R_{DS (ON)} = 1.6 Ω (typ.)
- High forward transfer admittance: $|Y_{fS}| = 2.0 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -200 \ V)$
- Enhancement model: V_{th} = -1.5 to -3.5 V (V_{DS} = -10 V, I_D = -1 mA)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristic | | Symbol | Rating | Unit | | |
|--|-------|------------------|-------------------|---|--------|---|
| Drain-source voltage | | V _{DSS} | -200 | V | | |
| Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$) | | V _{DGR} | -200- | X | | |
| Gate-source voltage | | | V _{GSS} | <u></u> | | |
| Drain current | DC | (Note 1) | I _D | | А | |
| | Pulse | (Note 1) | I _{DP} | -10 | A | |
| Drain power dissipation (Tc = 25° C) | | | P _D < | 20 | XV | / |
| Single-pulse avalanche energy (Note 2) | | | EAS | 97.5 | Cm | |
| Avalanche current | | LAR | -2.5 | A | \sim | |
| Repetitive avalanche energy (Note 3) | | | | 2.0 | mJ | |
| Channel temperature | | | Tch | 150 | 0°C | 1 |
| Storage temperature range | | | (T _{stg} | -55 to 150 | °C | |



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

| Characteristic | Symbol | Мах | Unit |
|--|------------------------|------|------|
| Thermal resistance, channel to case | Rth (ch-c) | 6.25 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 125 | °C/W |

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -50 V, Tch = 25 $^\circ\text{C}$ (initial), L = -25.2 mH, I_{AR} = -2.5 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

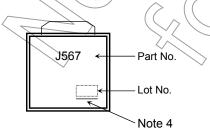
Electrical Characteristics (Ta = 25°C)

| Char | acteristic | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--|----------------|----------------------|---|------------|-------|------------|------|
| Gate leakage cur | rent | I _{GSS} | $V_{GS} = \pm 16$ V, $V_{DS} = 0$ V | _ | _ | ±10 | μA |
| Drain cutoff curre | ent | I _{DSS} | $V_{DS} = -200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | _ | | -100 | μA |
| Drain-source brea | akdown voltage | V (BR) DSS | $I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$ | -200 | | _ | V |
| Gate threshold vo | oltage | V _{th} | $V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$ | -1.5 | | -3.5 | V |
| Drain-source ON-resistance | | R _{DS (ON)} | $V_{GS} = -10 \text{ V}, \text{ I}_{D} = -1.5 \text{ A}$ | Æ |) M.6 | 2.0 | Ω |
| Forward transfer | admittance | Y _{fs} | $V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1.5 \text{ A}$ | 1.0 | 2.0 | | S |
| Input capacitance | | C _{iss} | | \bigcirc | 410 | _ | |
| Reverse transfer capacitance | | C _{rss} | $V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$ | _ | 40 | _ | pF |
| Output capacitance | | C _{oss} | | | 145 | _ | |
| Switching time | Rise time | tr | V_{GS} -10 V -10 V $R_L = 66.7$ | _ | 20 | \swarrow | ns |
| | Turn-on time | t _{on} | | -(C | 45 | > | |
| | Fall time | t _f | | | 15 | | |
| | Turn-off time | t _{off} | Duty $\leq 1\%$, t _w = 10 µs $V_{DD} \approx -100 V$ | Ð | 85 | | |
| Total gate charge (Gate source plus | | Qg | $V_{DD} \approx -160 \text{ V}, \text{ V}_{GS} \neq -10 \text{ V},$ |) — | 10 | _ | 0 |
| Gate-source charge | | Qgs | ID=-2.5 A | | 6 | — | nC |
| Gate-drain ("Miller") charge | | Qgq | | _ | 4 | _ | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristic | Symbol | Test Condition | Min | Тур. | Мах | Unit |
|---|-----------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | | — | _ | -2.5 | А |
| Pulse drain reverse current (Note 1) | IDRP | (\vee) – | _ | | -10 | А |
| Forward voltage (diode) | VDSF | 1 _{DR} = −2.5 A, V _{GS} = 0 V | _ | _ | 2.0 | V |
| Reverse recovery time | trr | I _{DR} = –2.5 A, V _{GS} = 0 V, | _ | 135 | _ | ns |
| Reverse recovery charge | Q _{rr} | dl _{DR} /dt = 100 A/µs | _ | 0.81 | _ | μC |

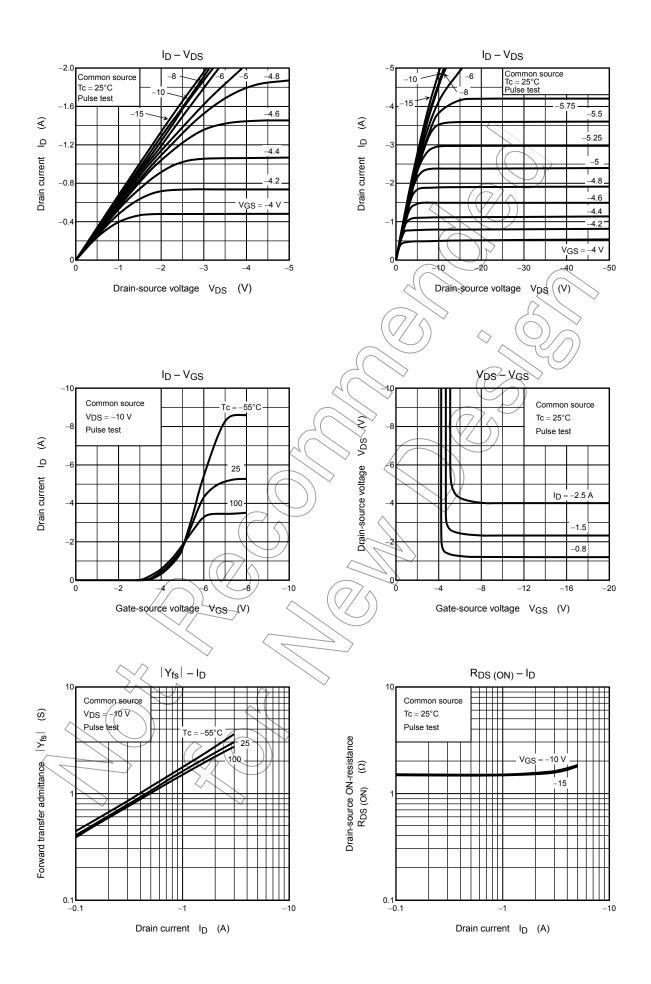
Marking



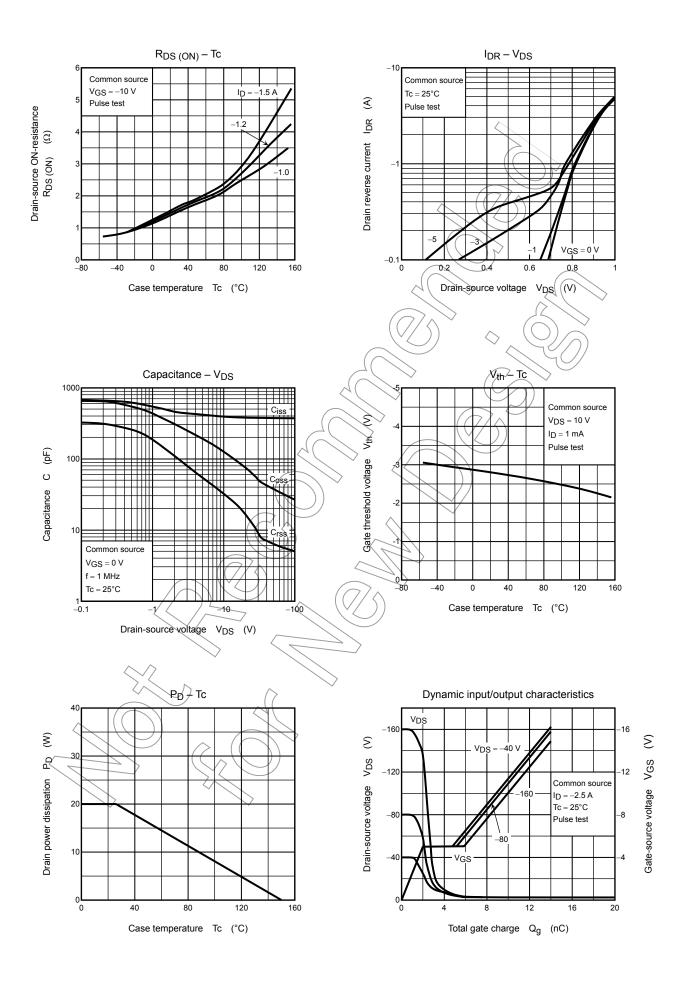
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

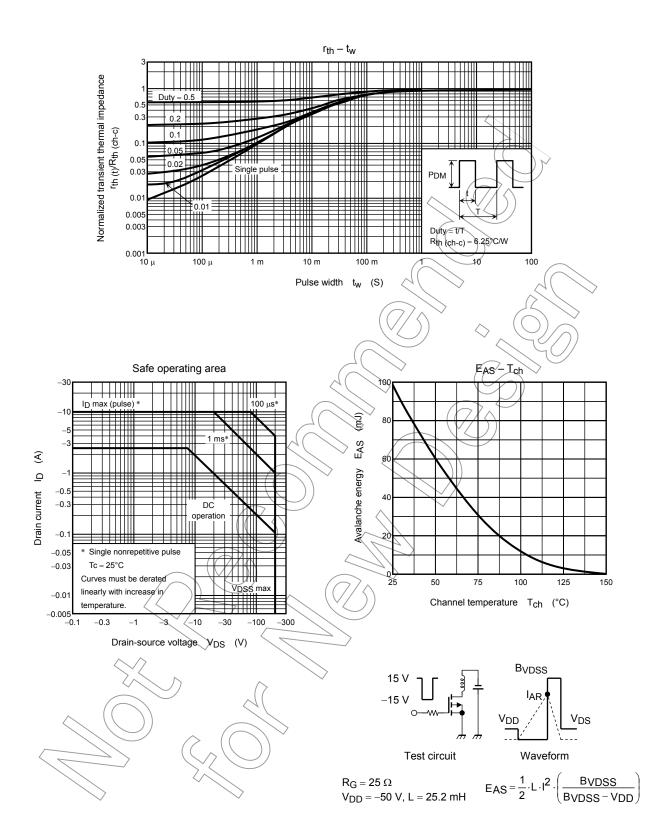
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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