

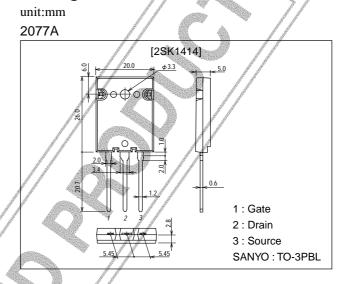
2SK1414

Ultrahigh-Speed Switching Applications

Features

- Low ON resistance, low input capacitance, Ultrahigh-speed switching.
- · High reliability (Adoption of HVP process).

Package Dimensions



Specifications

Absolute Maximum Ratings at Ta = $25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{D\$S}		1500	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	I I D		6	А
Drain Current (Pulse)	I _{DP} PW≤	10μs, duty cycle≤1%	12	А
Allowable Power Dissipation	PD To 2	> 11	3.5	W
	· D Tc=2	5°C	200	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		–55 to +150	°C

Electrical Characteristics at Ta = 25°C

Symbol	Conditions	Ratings			Unit
Gymbol	Conditions	min	typ	max	Onit
V _(BR) DSS	√ _D =1mA, V _{GS} =0	1500			V
I _{DSS}	V _{DS} =1200V, V _{GS} =0			100	μA
IGSS	V _{GS} =±20V, V _{DS} =0			±100	nA
VGS(off)	V _{DS} =10V, I _D =1mA	1.5		3.5	V
yfs	V _{DS} =20V, I _D =3A	1.0	3.0		S
R _{DS(on)}	I _D =3A, V _{GS} =10V		2.5	3.5	Ω
	IDSS IGSS VGS(off) / yfs	V(BR)DSS JD=1mA, VGS=0 IDSS VDS=1200V, VGS=0 IGSS VGS=±20V, VDS=0 VGS(off) VDS=10V, ID=1mA Vyfs VDS=20V, ID=3A	min V(BR)DSS /D=1mA, VGS=0 1500 IDSS VDS=1200V, VGS=0 1500 IGSS VGS=±20V, VDS=0 1500 VGS(off) VDS=10V, ID=1mA 1.5 Iyfs VDS=20V, ID=3A 1.0	Symbol Conditions min typ V(BR)DS\$ JD=1mA, VGS=0 1500 1500 IDS\$ VDS=1200V, VGS=0 1500 1000 IGS\$ VGS=±20V, VDS=0 1000 1000 VGS(off) VDS=10V, ID=1mA 1.5 1.0 3.0	Symbol Conditions min typ max V(BR)DSS JD=1mA, VGS=0 1500 100 IDSS VDS=1200V, VGS=0 100 100 IGSS VGS=±20V, VDS=0 ±100 ±100 VGS(off) VDS=10V, ID=1mA 1.5 3.5 I/yfs VDS=20V, ID=3A 1.0 3.0

(Note) Be careful in handling the 2SK1414 because it has no protection diode between gate and source.

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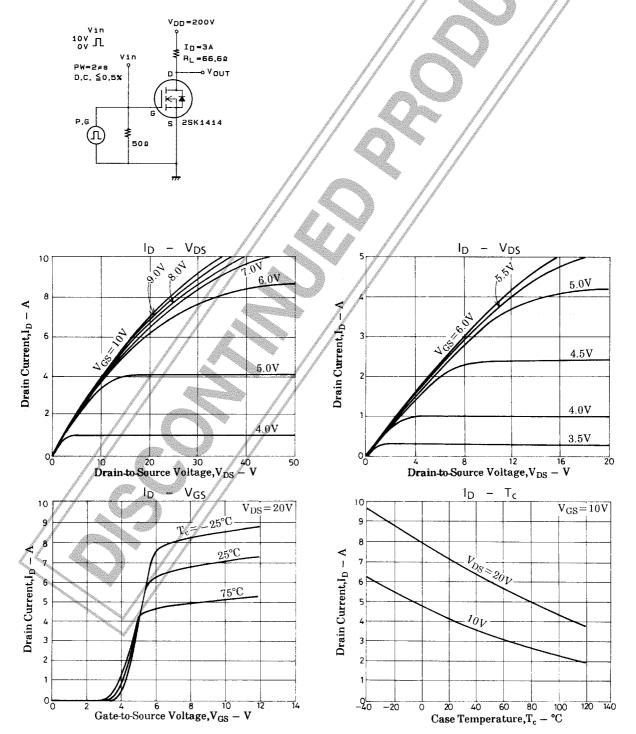
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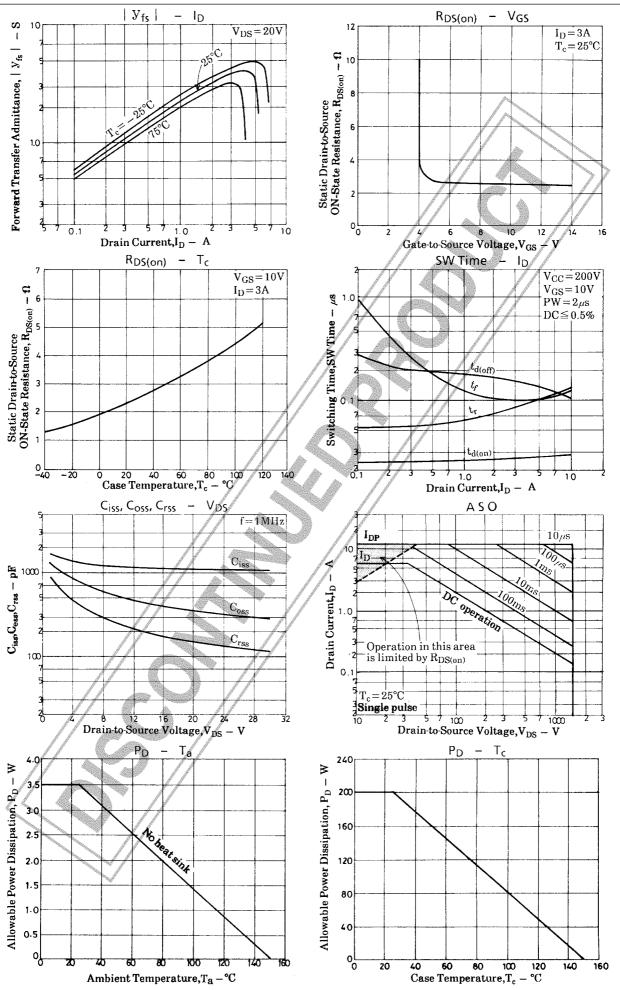
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Parameter	Symbol	Conditions		Ratings			Unit
	Symbol			min	typ	max	
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz			1100		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz			350		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz			150		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit			25		ns
Rise Time	tr	See specified Test Circuit	1		85	and the second second	ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit			155	Contraction of the owner owner owner owner own	ns
Fall Time	t _f	See specified Test Circuit			95	and a start of the second	ns
Diode Forward Voltage	V _{SD}	I _S =6A, V _{GS} =0			1.0	1.5	V

Switching Time Test Circuit





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