TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2$ - $\pi$ -MOSV)

# 2SK2507

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

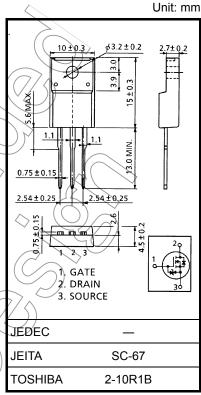
• Low drain-source ON resistance : RDS (ON) =  $0.034 \Omega$  (typ.)

• High forward transfer admittance  $: |Y_{fs}| = 16 \text{ S (typ.)}$ • Low leakage current  $: I_{DSS} = 100 \,\mu\text{A (max) (V}_{DS} = 50 \,\text{V)}$ 

• Enhancement mode :  $V_{th} = 0.8 \text{ to } 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	(V)nit
Drain-source voltage		$V_{DSS}$	50	<b>(</b>
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	$V_{DGR}$	50	V
Gate-source voltage		V <sub>GSS</sub>	±20	> v
Drain current	DC (Note 1)	ΙD	25	Α
Diam current	Pulse (Note 1)	I <sub>DP</sub>	75	(
Drain power dissipatio	n (Tc = 25°C)	PD	30	/\w
Single pulse avalanche	e energy (Note 2)	E <sub>A</sub> \$	138	É
Avalanche current		TAR	25	Α
Repetitive avalanche	energy (Note 3)	(EAR))	3	Lm/
Channel temperature		Tch	150	°C
Storage temperature r	ange	T <sub>stg</sub>	-55 to 150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	4.17	°C/W
Thermal resistance, channel to ambient Rth (ch-a)	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 272  $\mu$ H,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 25 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

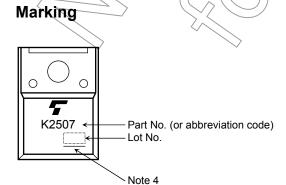
Please handle with caution.

## **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	50	_	_	V
Gate threshold v	oltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8		2.0	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 6 A	(F	0.058	0.08	Ω
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A	) /\	0.034	0.046	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 12 A	8.0	16	1	S
Input capacitance		C <sub>iss</sub>		\	900	1	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	130		pF
Output capacitance		Coss		_	370	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{Out}$ $V_{Out}$	- (	15	/> I _	
	Turn-on time	t <sub>on</sub>	$\begin{array}{c c} & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & &$	OV/	25	) _	ns
	Fall time	t <sub>f</sub>	$V_{DD} = 30 \text{ V}$	\ \ \ \	30	ı	113
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{w}} = 10 \mu \text{s}$		110	l	
Total gate charg plus gate-drain)		Qg		_	25	_	
Gate-source charge		Qgs	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 25 \text{ A}$	_	19	_	nC
Gate-drain ("miller") charge		Qgd		_	6	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	25	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	_	_	75	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 25 Å, V <sub>GS</sub> = 0 V	_	_	-1.6	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 50 A / μs		60	_	ns
Reverse recovery charge	Qrr	10K - 25 Λ, VGS - 0 V, αιρκ / αι - 30 Α / μs		45	_	μC

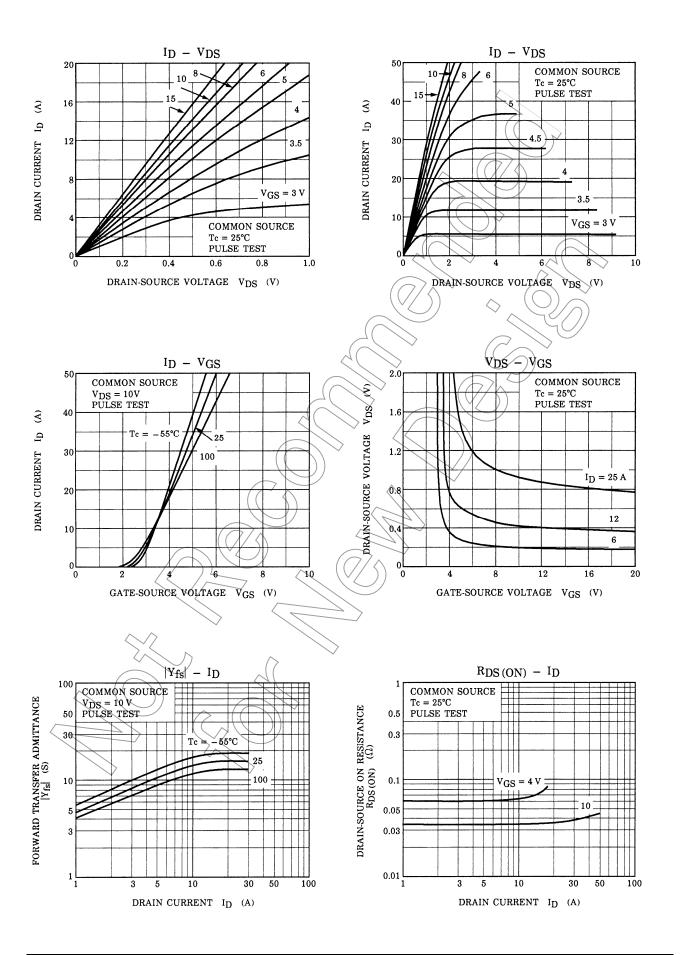


Note 4: A line under a Lot No. identifies the indication of product Labels.

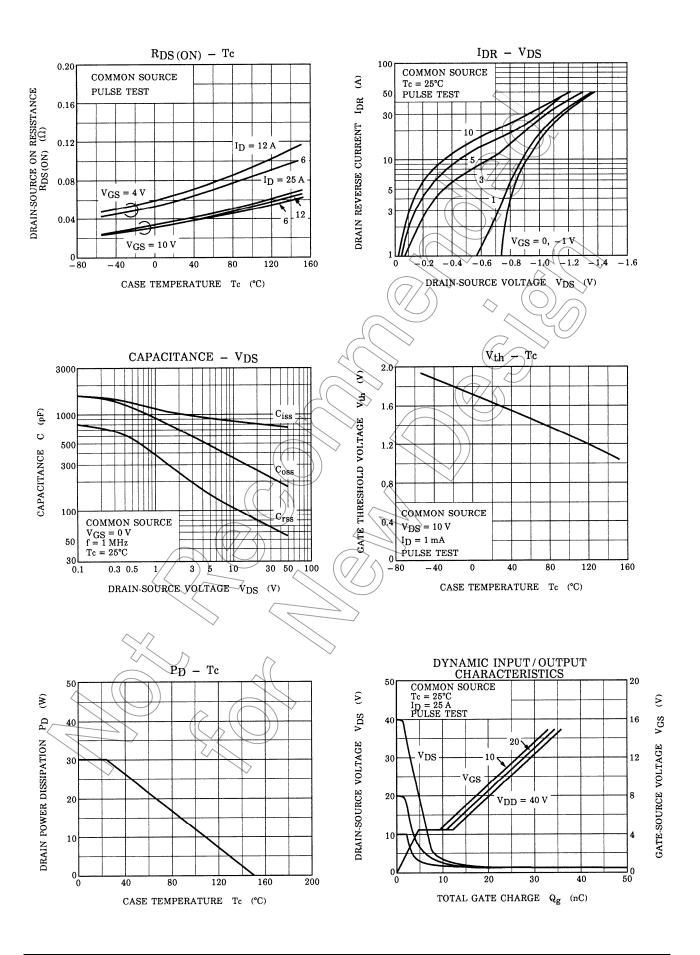
Not underlined: [[Pb]]/INCLUDES > MCV

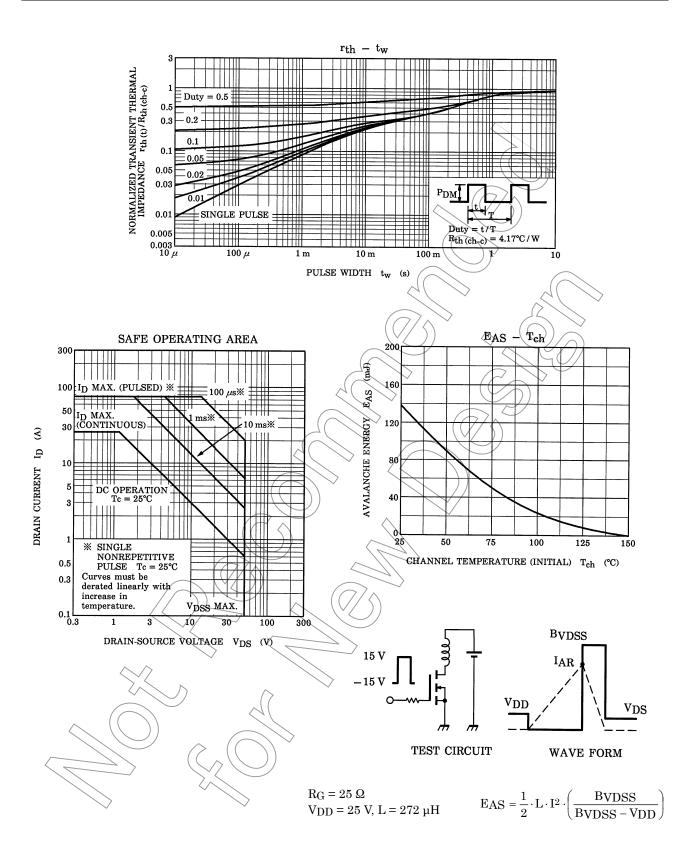
 $\label{thm:compatible} \mbox{Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$ 

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