TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (π-MOSIII)

2SK2605

HIGH SPEED, HIGH VOLTAGE SWITCHING APPLICATIONS

SWITCHING REGULATOR APPLICATIONS

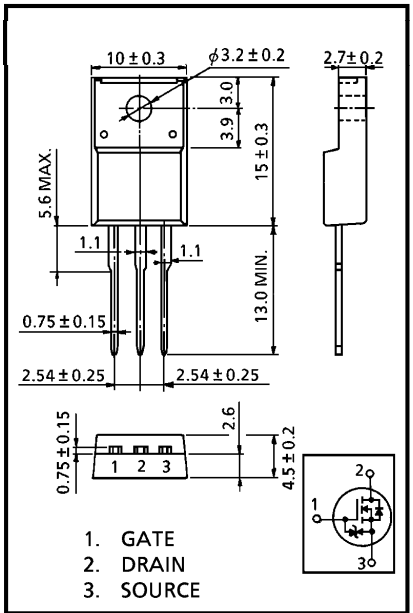
INDUSTRIAL APPLICATIONS

- Low Drain-Sorce ON Resistance : $R_{DS\,(ON)} = 1.9\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 3.8S$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100\mu A$ (Max.) ($V_{DS} = 640V$)
- Enhancement-Mode : $V_{th} = 2.0 \sim 4.0V$ ($V_{DS} = 10V$, $I_D = 1mA$)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)		V_{DGR}	800	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	DC	I_D	5	A
	Pulse	I_{DP}	15	A
Drain Power Dissipation (Tc = 25°C)		P_D	45	W
Single Pulse Avalanche Energy**		E_{AS}	370	mJ
Avalanche Current		I_{AR}	5	A
Repetitive Avalanche Energy*		E_{AR}	4.5	mJ
Channel Temperature		T_{ch}	150	°C
Storage Temperature Range		T_{stg}	−55~150	°C

Unit in mm



JEDEC	—
EIAJ	SC-67
TOSHIBA	2-10R1B

Weight : 1.9g

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th\,(ch-c)}$	2.78	°C / W
Thermal Resistance, Channel to Ambient	$R_{th\,(ch-a)}$	62.5	°C / W

Note ;

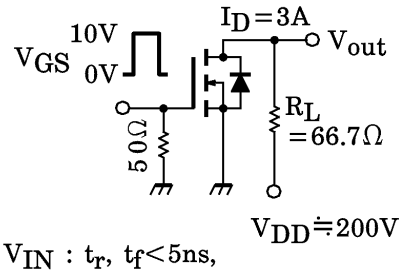
* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

** $V_{DD} = 90V$, Starting $T_{ch} = 25°C$, $L = 27mH$, $R_G = 25\Omega$, $I_{AR} = 5A$

This transistor is an electrostatic sensitive device.
Please handle with caution.

961001EAA2

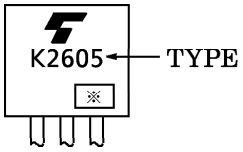
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I_{GSS}	$V_{GS}=\pm30V$, $V_{DS}=0V$	—	—	±10	μA
Gate-Source Breakdown Voltage		$V_{(BR)\,GSS}$	$I_G=\pm10\mu A$, $V_{DS}=0V$	±30	—	—	V
Drain Cut-off Current		I_{DSS}	$V_{DS}=640V$, $V_{GS}=0V$	—	—	100	μA
Drain-Source Breakdown Voltage		$V_{(BR)\,DSS}$	$I_D=10mA$, $V_{GS}=0V$	800	—	—	V
Gate Threshold Voltage		V_{th}	$V_{DS}=10V$, $I_D=1mA$	2.0	—	4.0	V
Drain-Source ON Resistance		$R_{DS\,(ON)}$	$V_{GS}=10V$, $I_D=3A$	—	1.9	2.2	Ω
Forward Transfer Admittance		$\|Y_{fs}\|$	$V_{DS}=15V$, $I_D=3A$	1.0	3.8	—	S
Input Capacitance		C_{iss}	$V_{DS}=25V$, $V_{GS}=0V$, f=1MHz	—	1080	—	pF
Reverse Transfer Capacitance		C_{rss}		—	16	—	
Output Capacitance		C_{oss}		—	105	—	
Switching Time	Rise Time	t_r		—	40	—	ns
	Turn-on Time	t_{on}		—	80	—	
	Fall Time	t_f		—	40	—	
	Turn-off Time	t_{off}		—	140	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q_g	$V_{DD}\fallingdotseq400V$, $V_{GS}=10V$, $I_D=5A$	—	34	—	nC
Gate-Source Charge		Q_{gs}		—	16	—	
Gate-Drain ("Miller") Charge		Q_{gd}		—	18	—	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	5	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	15	A
Diode Forward Voltage	V_{DSF}	$I_{DR}=5A$, $V_{GS}=0V$	—	—	−1.9	V
Reverse Recovery Time	t_{rr}	$I_{DR}=5A$, $V_{GS}=0V$	—	1000	—	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR}/dt=100A/\mu s$	—	7.5	—	μC

MARKING

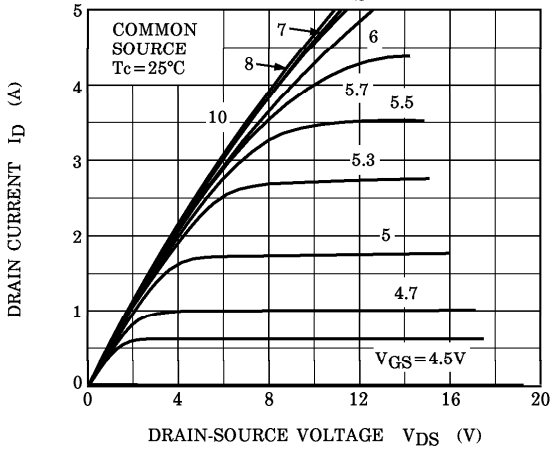


※ Lot Number

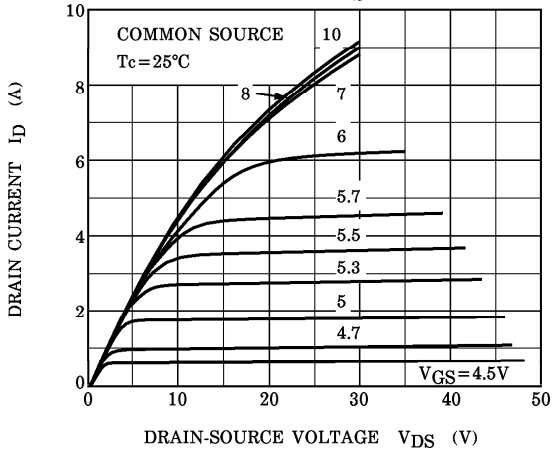
□□ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)

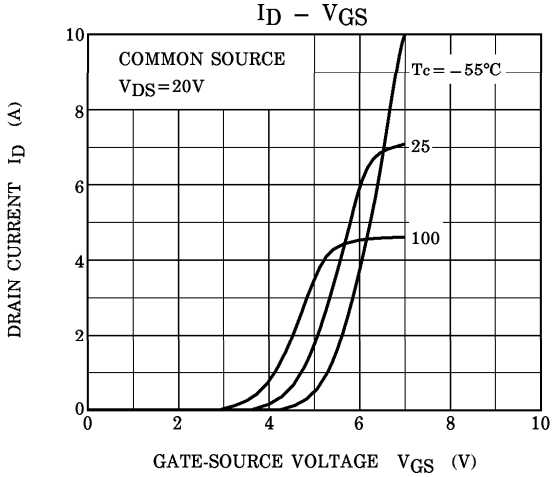
$I_D - V_{DS}$



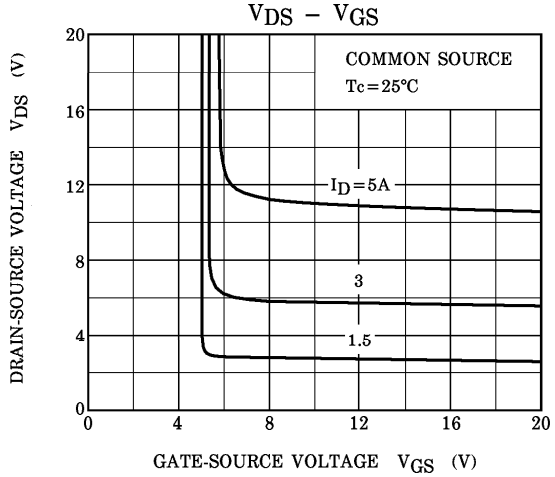
$I_D - V_{DS}$



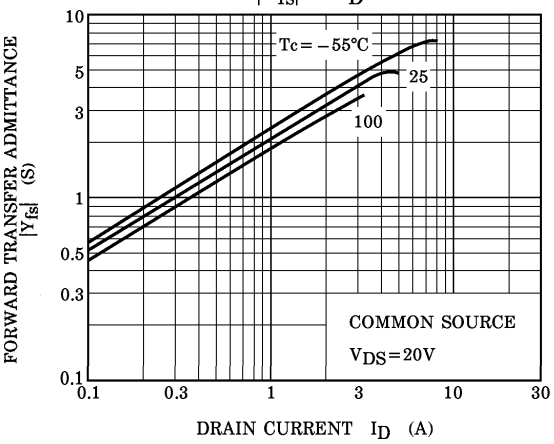
$I_D - V_{GS}$



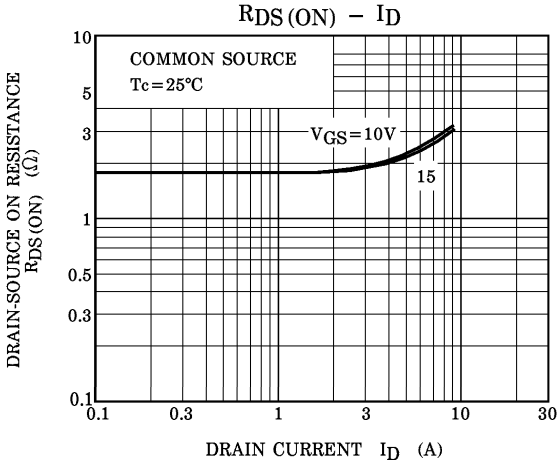
$V_{DS} - V_{GS}$



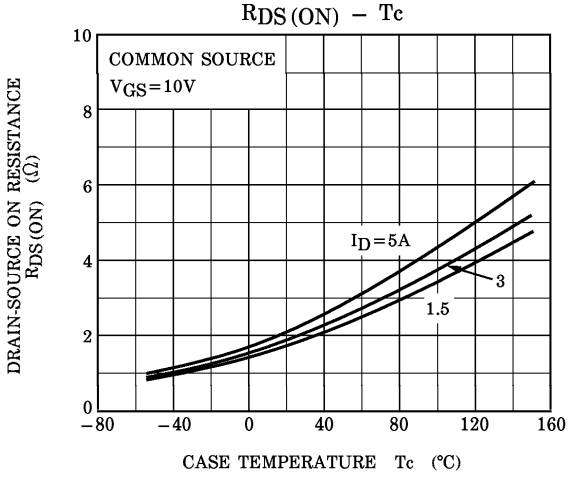
$|Y_{fs}| - I_D$



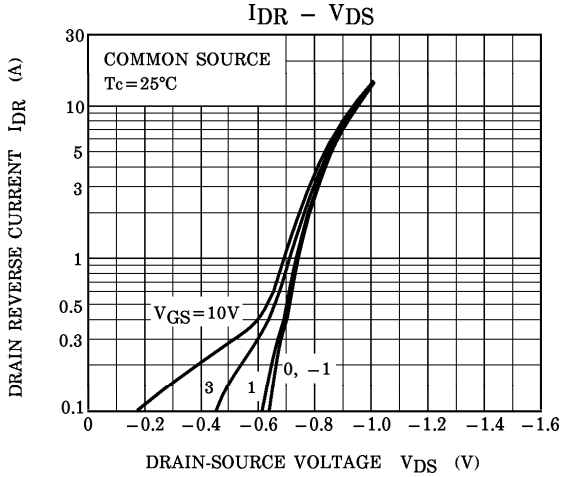
$R_{DS(ON)} - I_D$



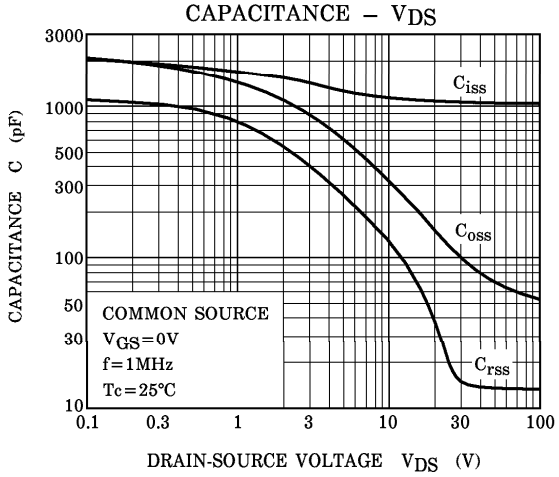
$R_{DS\,(ON)}$ – Tc



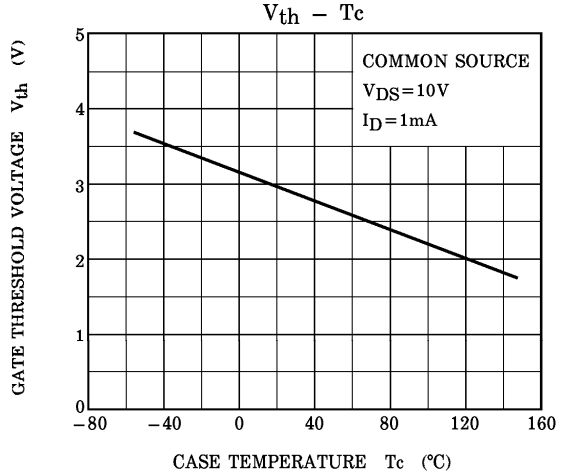
I_{DR} – V_{DS}



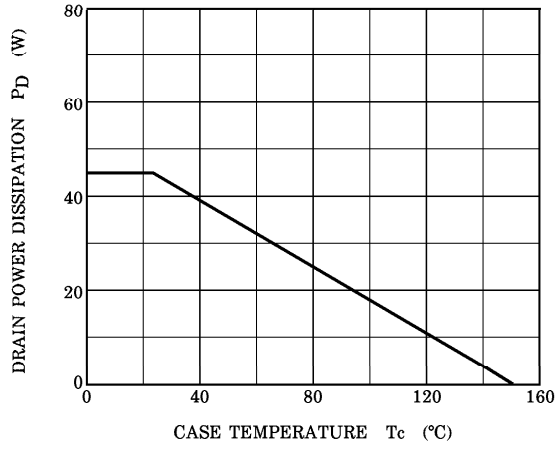
CAPACITANCE – V_{DS}



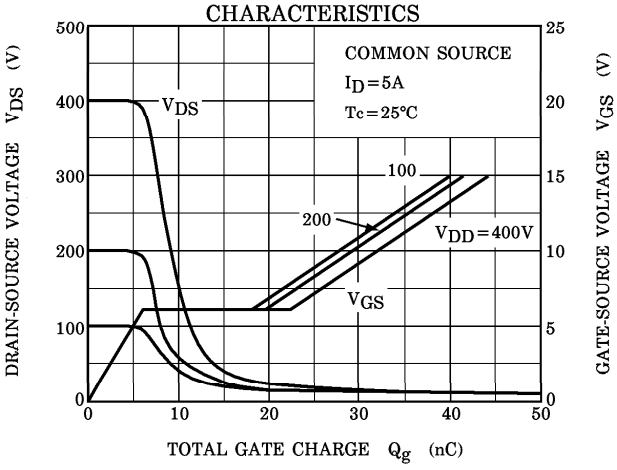
V_{th} – Tc



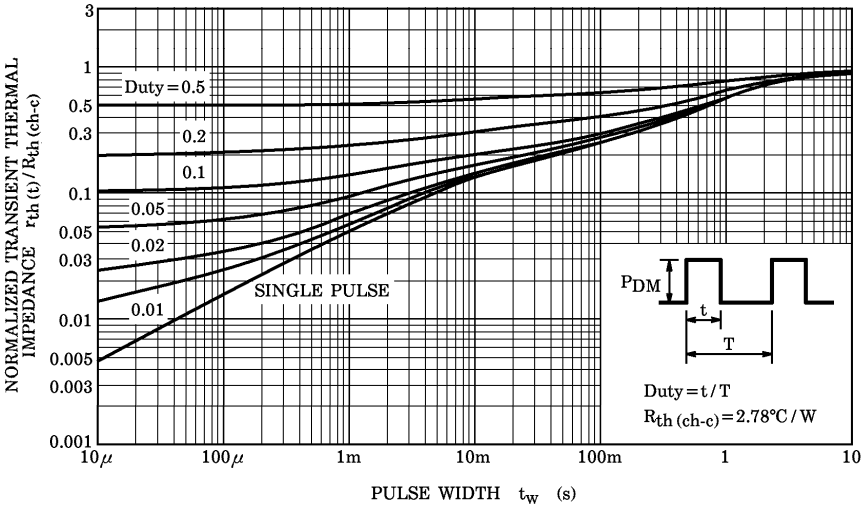
P_D – Tc



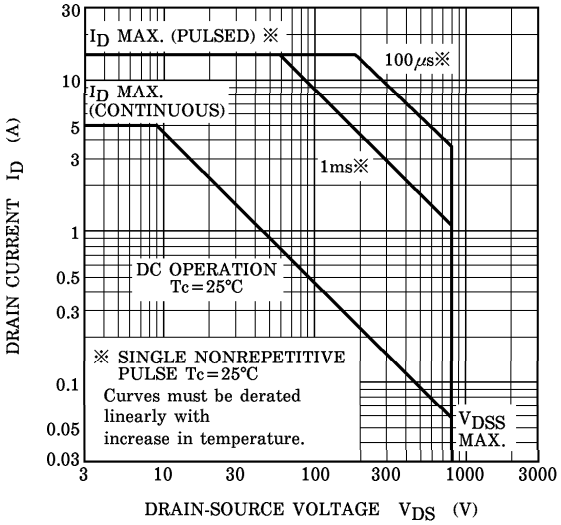
DYNAMIC INPUT / OUTPUT CHARACTERISTICS



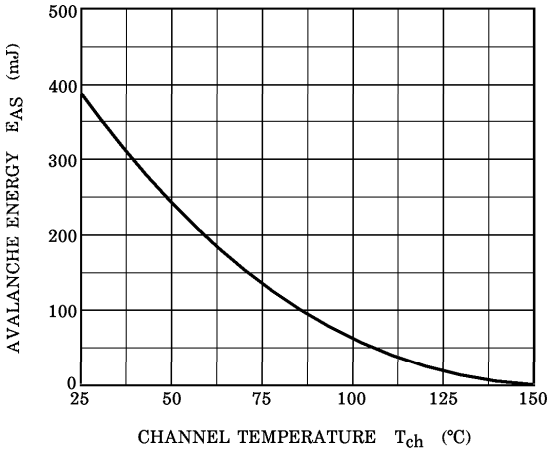
r_{th} – t_w

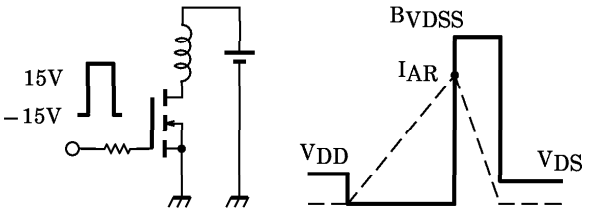


SAFE OPERATING AREA



E_{AS} – T_{ch}





TEST CIRCUIT WAVE FORM

Peak I_{AR} = 5A, R_G = 25Ω
V_{DD} = 90V, L = 27mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$