TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK2838

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

 $\begin{array}{ll} \bullet & Low\ drain-source\ ON\ resistance & : RDS\ (ON) = 0.84\ \Omega\ (typ.) \\ \bullet & High\ forward\ transfer\ admittance & : |Y_{fs}| = 4.4\ S\ (typ.) \\ \bullet & Low\ leakage\ current & : I_{DSS} = 100\ \mu A\ (max)\ (V_{DS} = 400\ V) \\ \bullet & Enhancement\ mode & : V_{th} = 2.0 \sim 4.0\ V\ (V_{DS} = 10\ V,\ I_{D} = 1\ mA) \\ \end{array}$ 

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	400	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	400	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	5.5	Α	
	Pulse (Note 1)	$I_{DP}$	22	Α	
Drain power dissipation (Tc = 25°C)		$P_{D}$	40	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	223	mJ	
Avalanche current		I <sub>AR</sub>	5.5	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

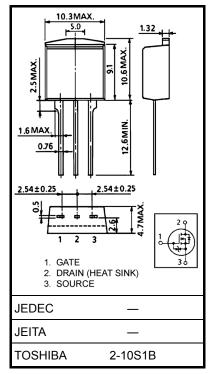
Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 12.0 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 5.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

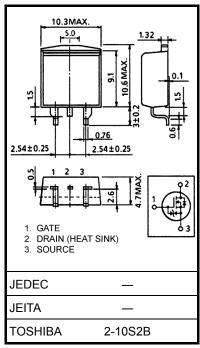
This transistor is an electrostatic-sensitive device.

Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



Weight: 1.5 g (typ.)

## Electrical Characteristics (Ta = 25°C)

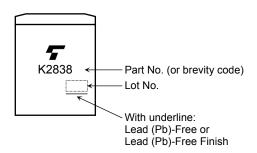
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	400	_	_	V
Gate threshold v	oltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A	_	0.84	1.2	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	2.0	4.4	_	S
Input capacitano	e	C <sub>iss</sub>		-	720	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	80	_	pF
Output capacita	Dutput capacitance C <sub>oss</sub>		_	250	_	1	
T Switching time F	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{OV}$ $V_{OUT}$ $V_{DD}$ $V_{DD}$	_	15	_	
	Turn-on time	t <sub>on</sub>		_	30	_	ne
	Fall time	t <sub>f</sub>		_	25	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	_	110	_	
Total gate charg plus gate-drain)		Qg			17	_	
		Q <sub>gs</sub>	$V_{DD}$ =320 V, $V_{GS}$ = 10 V, $I_{D}$ = 5.5 A	_	10	_	nC
		Q <sub>gd</sub>			7		

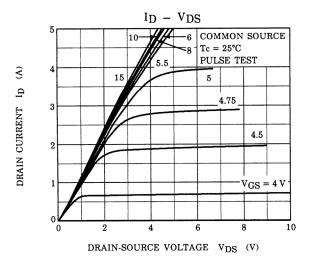
## Source-Drain Ratings and Characteristics (Ta = 25°C)

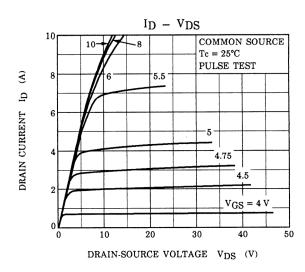
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	5.5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	22	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 5.5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 5.5 A, V <sub>GS</sub> = 0 V	-	350	1	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 Å / μs	_	2.1	_	μC

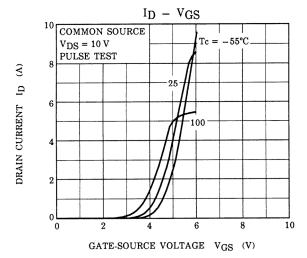
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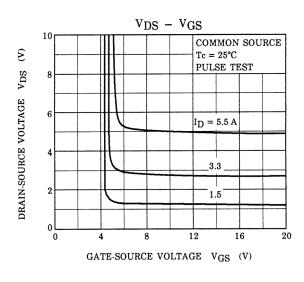
### Marking

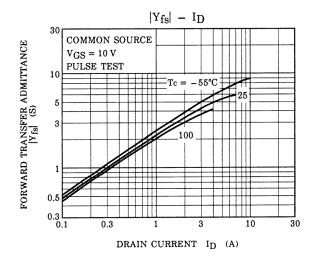


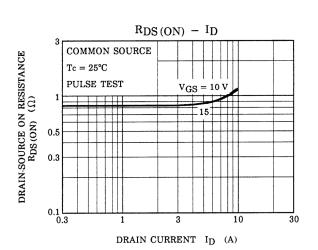


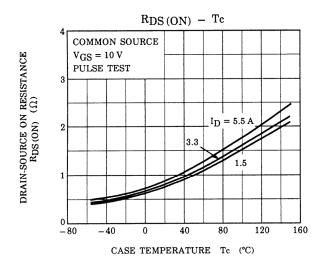


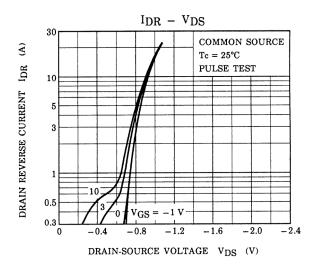


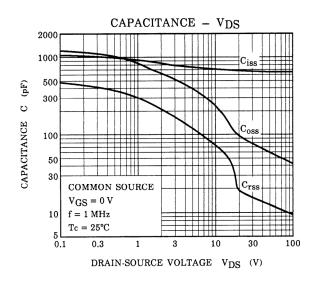


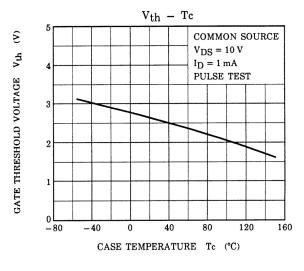


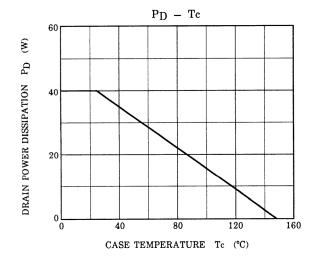


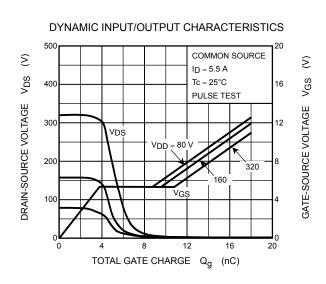


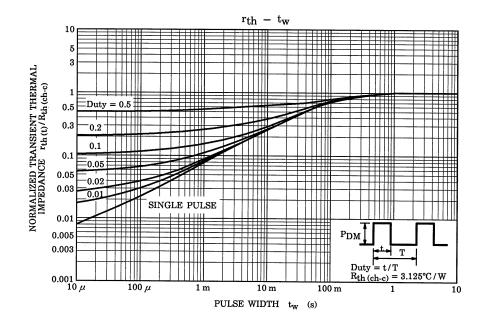


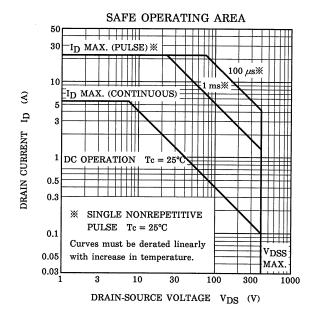


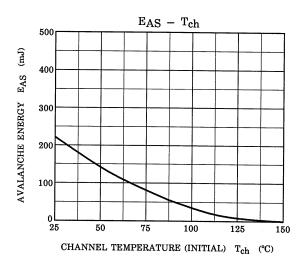


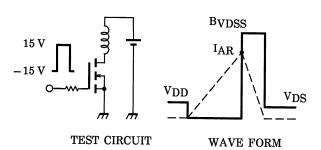












$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 90 V, L = 12 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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