TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

# 2SK2847

#### DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance :  $R_{DS(ON)} = 1.1 \Omega$  (typ.)

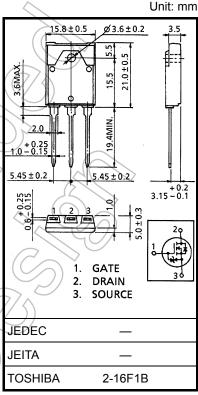
High forward transfer admittance : |Y<sub>fs</sub>| = 7.0 S (typ.)

Low leakage current :  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$ 

• Enhancement mode :  $V_{th} = 2.0 \text{ to } 4.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$ 

## Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	900	$\langle \langle \rangle \rangle$
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	$V_{DGR}$	900	/<(
Gate-source voltage		$V_{GSS}$	±30	<b>&gt;</b>
Drain current	DC (Note 1)	ΙD	8	Α
Diam current	Pulse (Note 1)	I <sub>DP</sub>	24	Α
Drain power dissipatio	n (Tc = 25°C)	PD	85	W
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	799	mJ mJ
Avalanche current		I <sub>AR</sub>	)) 8	Α
Repetitive avalanche	energy (Note 3)	EAR	8.5	mJ
Channel temperature		T <sub>ch</sub>	150	Ĵ°¢
Storage temperature ra	ange	Tstg	-55 to 150	\e



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th</sub> (ch-c)	1.47	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	41.6	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 22.9 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 8 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

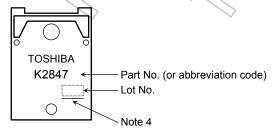
### **Electrical Characteristics (Ta = 25°C)**

Charae	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	/	_	100	μΑ
Drain-source bi	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	) /_	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A	)   	1.1	1.4	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A	3.0	7.0	_	S
Input capacitano	ce	C <sub>iss</sub>		\	2040	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	45	_	pF
Output capacitance		Coss		_	190	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} \stackrel{10V}{\underset{0V}{\text{ID}}} \stackrel{I_{D}=4A}{\underset{R_{I}}{\text{VOUT}}}$	- (	25	<u> </u>	
	Turn-on time	t <sub>on</sub>			60	) –	
	Fall time	t <sub>f</sub>	4.711 \$ V <sub>DD</sub>	7	20	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm W} = 10 \mu \rm s$		95	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	58	-	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$	_	32	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>		_	26	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	<u> </u>	_	_	8	Α
Pulse drain reverse current (Note 1)	\ I <sub>DRP</sub>	_	_	_	24	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V	_	1650	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs	_	21	_	μC

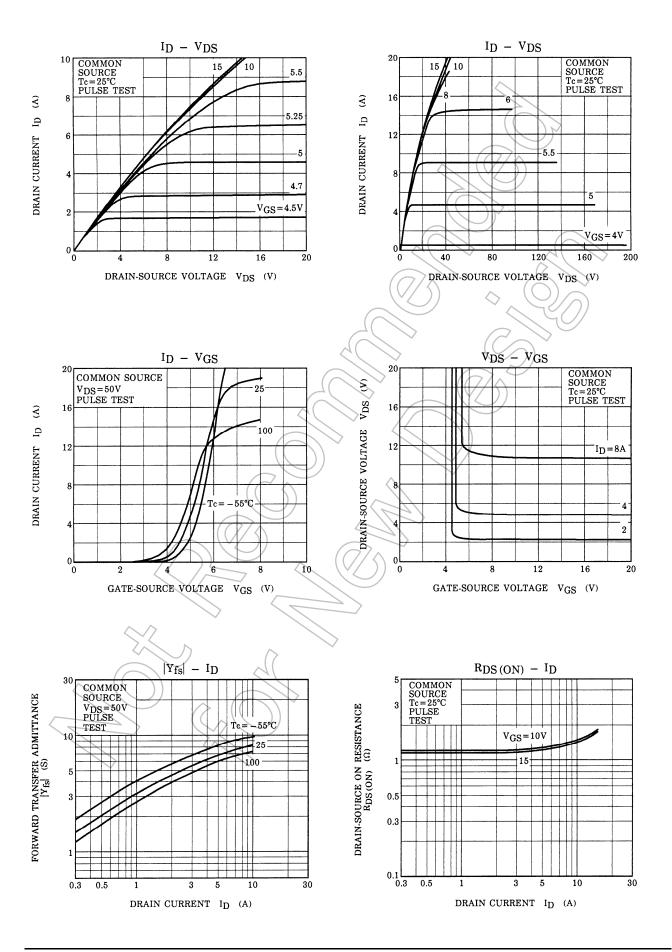
#### Marking

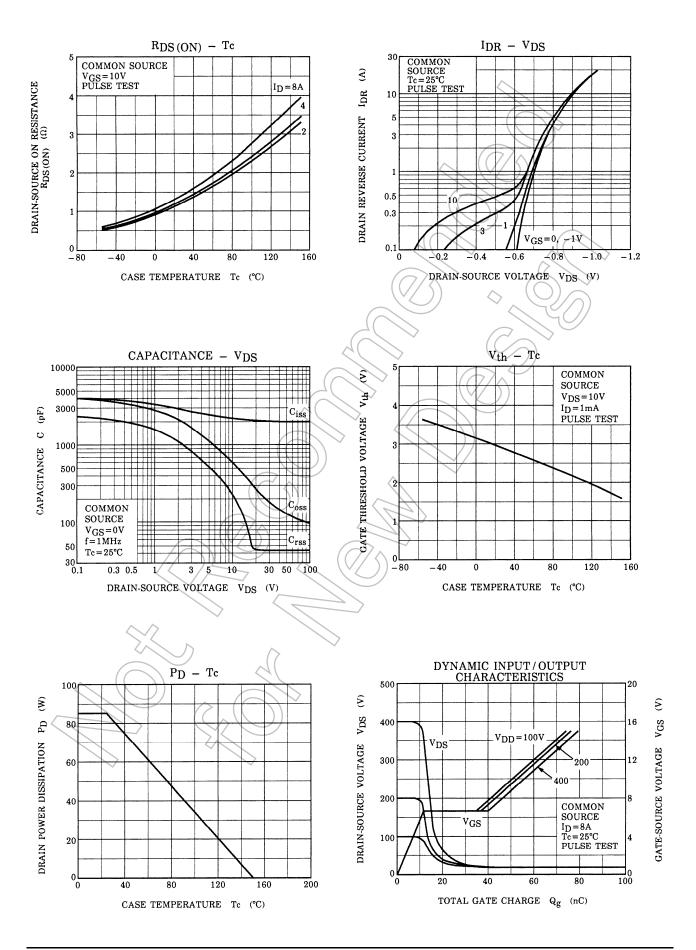


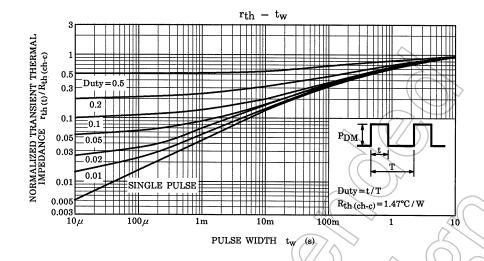
Note 4: A line under a Lot No. identifies the indication of product Labels.

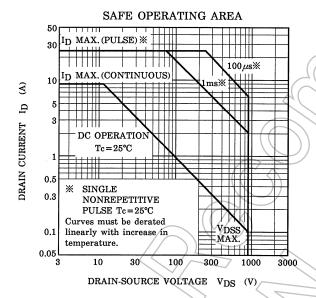
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

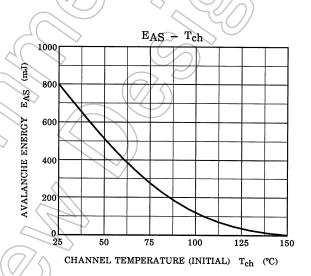
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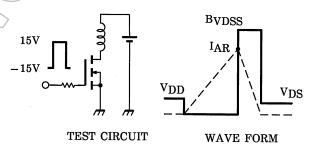












$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 90 V, L = 22.9 mH

$$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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