Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

## 2SK3090

# Chopper Regulator DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance :  $R_{DS (ON)} = 16 \text{ m}\Omega \text{ (typ.)}$ 

• High forward transfer admittance : |Y<sub>fs</sub>| = 26 S (typ.)

• Low leakage current :  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$ 

• Enhancement mode :  $V_{th}$  = 1.5 to 3.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	30	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	30	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	DC (Note 1)	ΙD	45	Α
	Pulse (Note 1)	$I_{DP}$	135	A
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	60	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	220	mJ
Avalanche current		I <sub>AR</sub>	45	Α
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	6	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature ra	ange	T <sub>stg</sub>	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.08	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C / W

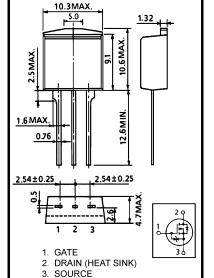
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 78  $\mu$ H,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 45 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

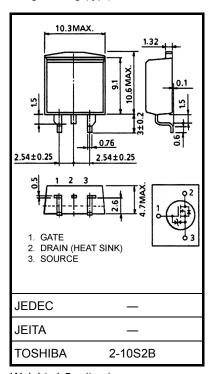
Please handle with caution.



2-10S1B

Weight: 1.5 g (typ.)

JEDEC JEITA TOSHIBA



Weight: 1.5 g (typ.)

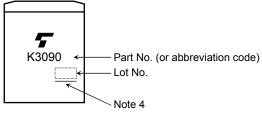
## **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition		Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V		_	±10	μΑ
Drain cut-off cur	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	30	_	_	V
Gate threshold v	roltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.0	V
Drain-source Ol	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	_	16	20	mΩ
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 25 A	13	26	_	S
Input capacitano	e	C <sub>iss</sub>			1500	_	pF
Reverse transfe	capacitance	sitance $C_{rss}$ $V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		_	480	_	
Output capacitance		Coss			680	_	
Switching time	Rise time	tr	$V_{GS}$ $V_{OV}$ $V_{OUT}$ $V_{OUT}$ $V_{DD}$ $V_{OUT}$ $V_{OUT}$	_	11	_	- ns
	Turn-on time	t <sub>on</sub>		_	18	_	
	Fall time	t <sub>f</sub>		_	60	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm w} = 10 \mu \rm s$		130	_	
Total gate charge (Gate-source plus gate-drain)		Qg			39	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$	_	25	_	nC
Gate-drain ("miller") charge		$Q_{gd}$			14	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	(Note 1)	$I_{DR}$	_	_	_	45	Α
Pulse drain reverse current	(Note 1)	I <sub>DRP</sub>	_	_	_	135	Α
Forward voltage (diode)		$V_{DSF}$	I <sub>DR</sub> = 45 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 45 A, V <sub>GS</sub> = 0 V	_	100	_	ns
Reverse recovery charge		Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 50 Å / μs	1	200	_	nC

### Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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